# muRata

**Reference Specification** 

**DEH Series** 

High Temperature Low Loss Lead Type Disc Ceramic Capacitors of class 2 for General Purpose

Product specifications in this catalog are as of Dec. 2017, and are subject to change or obsolescence without notice.

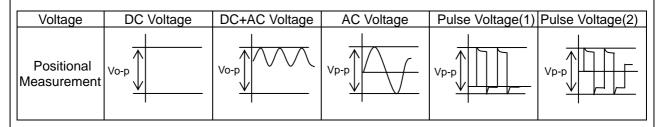
Please consult the approval sheet before ordering.Please read rating and Cautions first.

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# 1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.



# 2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. The allowable frequency should be in less than 300kHz in sine wave. Applied voltage should be the load such as self-generated heat is within 20 °C <u>on the condition of atmosphere temperature 25 °C.</u> When measuring, use a thermocouple of small thermal capacity-K of  $\phi$ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

#### 3. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

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# 4. LOAD REDUCTION AND SELF-GENERATED HEAT DURING APPLICATION OF HIGH-FREQUENCY AND HIGH-VOLTAGE

Since the heat generated by the low-dissipation capacitor itself is low, its allowable power is much higher than the general B characteristic. However, in case such an applied load that the self-heating temperature is 20 °C at the rated voltage, the allowable power may be exceeded.

Therefore, when using the low-dissipation capacitors in a high-frequency and high-voltage circuit with a frequency of 1kHz or higher, make sure that the Vp-p values including the DC bias, do not exceed the applied voltage value specified in Table 1. Also make sure that the self-heating temperature (the difference between the capacitor's surface temperature and the capacitor's ambient temperature) at an ambient temperature of 25 °C does not exceed the value specified in Table 1.

As shown in Fig. 2, the self-heating temperature depends on the ambient temperature. Therefore, if you are not able to set the ambient temperature to approximately 25 °C, please contact our sales representatives or product engineers.

Temp. DC Rated		Allowable Condi	Capacitor's	
Char. Voltage		Applied Voltage		
	0501/	(max.)	(25 °C Ambient Temp.) *1	Temp. *2
R	250V	250Vр-р	10 °C max.	
С	500V 500Vp-p		20 °C max.	
	1kV	800Vp-p	20 °C max.	
	IKV	1000Vp-p	5 °C max.	-25 to +85 °C
D	R 2kV 1400Vp-p 2000Vp-p		20 °C max.	-23 10 +85 C
			5 °C max.	
	2 1541	1600Vp-p	20 °C max.	
	3.15kV	3150Vp-p	5 °C max.	

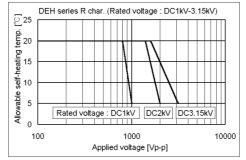
<Table 1> Allowable Conditions at High-frequency

\*1 Fig. 1 shows the relationship between the applied voltage and the allowable self-heating temperature regarding 1 to 3.15kV rated voltage of the DEH series R characteristic.

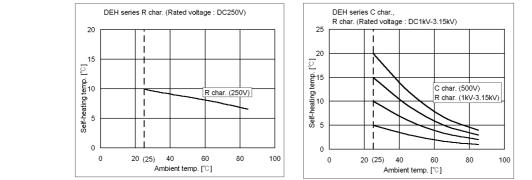
\*2 When the ambient temperature is 85 to 125 °C, the applied voltage needs to be further reduced. If the low-dissipation capacitors needs to be used at an ambient temperature of 85 to 125 °C, please contact our sales representatives or product engineers.

\*3 Fig. 3 shows reference data on the allowable voltage-frequency characteristic for a sine wave voltage.

<Fig. 1> Relationship Between Applied Voltage and Self-heating Temperature [Allowable Self-heating Temp. at 25 °C Ambient Temp. ]



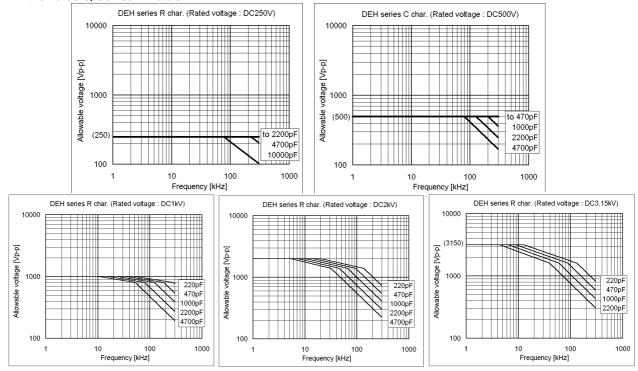
# <Fig. 2> Dependence of Self-heating Temperature on Ambient Temperature



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<Fig. 3> Allowable Voltage (Sine Wave Voltage) – Frequency Characteristic [At Ambient Temperature of 85 °C or less]

Because of the influence of harmonics, when the applied voltage is a rectangular wave or pulse wave voltage (instead of a sine wave voltage), the heat generated by the capacitor is higher than the value obtained by application of the sine wave with the same fundamental frequency. Roughly calculated for reference, the allowable voltage for a rectangular wave or pulse wave corresponds approximately to the allowable voltage for a sine wave whose fundamental frequency is twice as large as that of the rectangular wave or pulse wave. This allowable voltage, however, varies depending on the voltage and current waveforms. Therefore, you are requested to make sure that the self-heating temperature is not higher than the value specified in Table 1.



# 5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

#### 6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron tip : 400 °C max.

Soldering iron wattage : 50W max.

Soldering time : 3.5 s max.

#### 7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

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# 8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100  $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

#### 9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%. Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

#### **10. LIMITATION OF APPLICATIONS**

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

# NOTICE

#### 1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions. Rinse bath capacity : Output of 20 watts per liter or less. Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

#### 2. CAPACITANCE CHANGE OF CAPACITORS

- Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

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- 1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

		•						
1. Application								
	on is applied to High Tempe	rature Low Loss Lead Type Disc Ceramic Capacitors	of					
Class 2 of DEF	series used for General Ele	ectric equipment.	01					
Do not use these products in any automotive power train or safety equipment including battery chargers								
	cles and plug-in hybrids.		5					
	,							
2. Rating								
2-1. Operating t	emperature range							
-25	~ +125°C							
2-2. Part numbe	er configuration							
ex.) <u>DEH</u>	R3 3F	102 K B3 B						
Series	•		ridual					
	characteristic voltage	tolerance code style code specif	ication					
Tompor	aturo charactoristic							
• temper	ature characteristic Code	Temperature characteristic						
-	R3	R						
L		ification on [ Specification and test methods ].						
Г	lease commit detailed spec	incation on [ Specification and test methods ].						
<ul> <li>Rated v</li> </ul>	voltage							
	Code	Rated voltage						
	3D	DC2kV						
	3F	DC3.15kV						
L								
• Capaci	tance tolerance							
Plea	ase refer to [ Part number lis	t ].						
• Lead c	ada							
• Leau C	Code	Lead style						
-	A*	Vertical crimp long type						
-	C*	Straight long type						
	B*	Vertical crimp short type						
-	<u>B</u> * D*	Straight short type						
-	_	Vertical crimp taping type						
-	<u>N*</u> P*							
L	•	Straight taping type						
	* Please refer to [ Part num]	Der list j.						
Solo	der coated copper wire is ap	plied for termination.						
<ul> <li>Packing</li> </ul>	g style code							
	Code	Packing type						
_	В	Bulk type						
	Α	Ammo pack taping type						
_								
	ual specification							
		dentified without 'individual specification', it is added	at					
the	end of part number.							

# 3. Marking

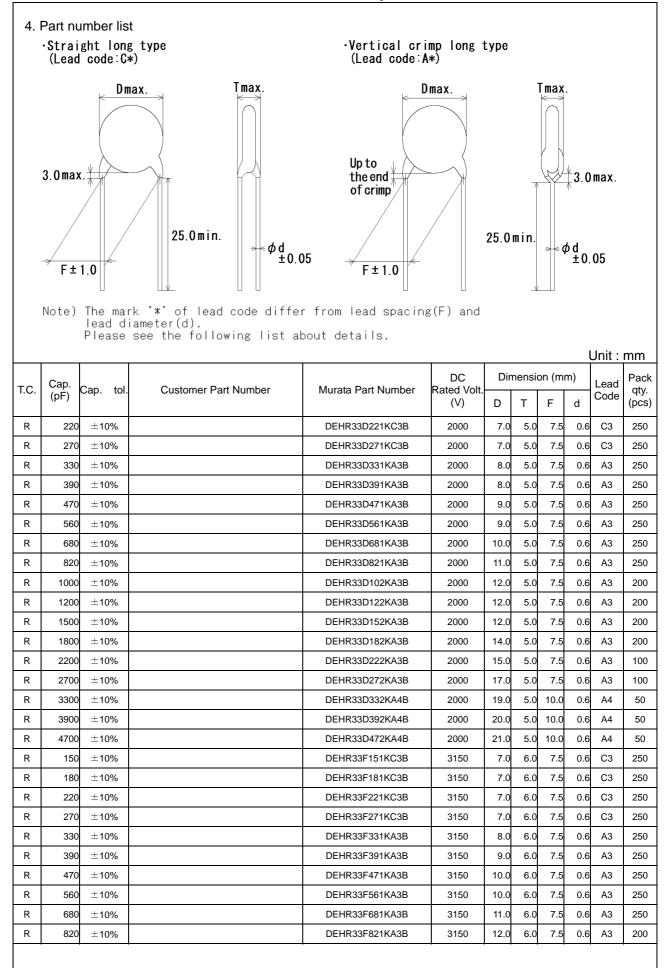
High temperature guaranteed cod Temperature characteristic	: Letter code
Nominal capacitance	: 3 digit system
Capacitance tolerance	: Code
Rated voltage	: Letter code(In case of DC3.15kV, marked with 3KV)
Company name code	: Abbreviation 🚱
	(Omitted for maximum body diameter () 9mm and under)
Manufacturing date	: Abbreviation

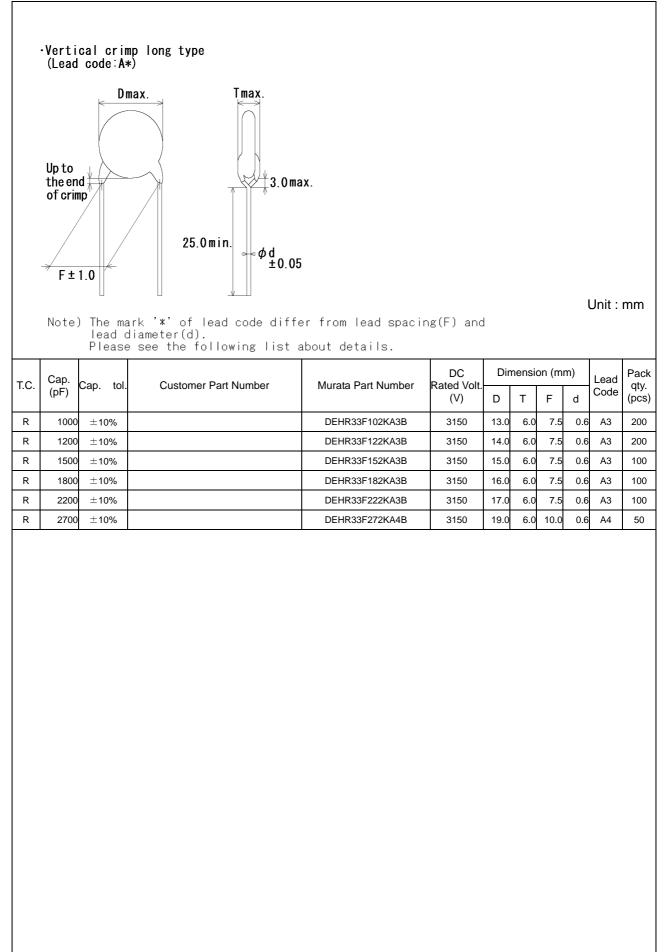
ex.) YEAR MONTH 2010 12(December) \_\_\_\_\_\_0D\*\_\_\_\_\_

\* From January to September : "1" to "9", October : "O" , November : "N" , December : "D"



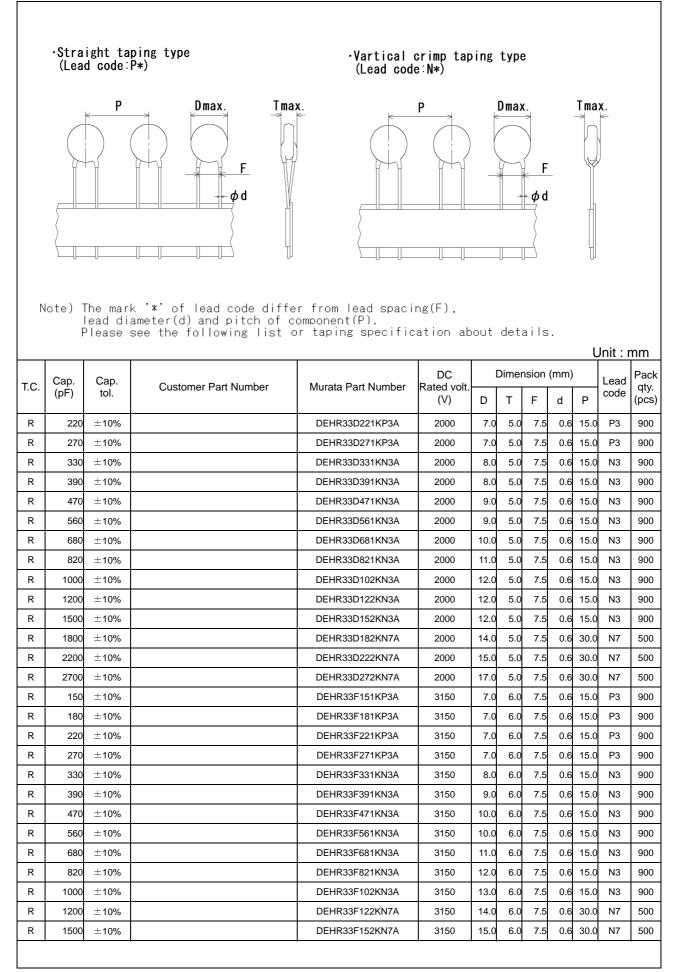


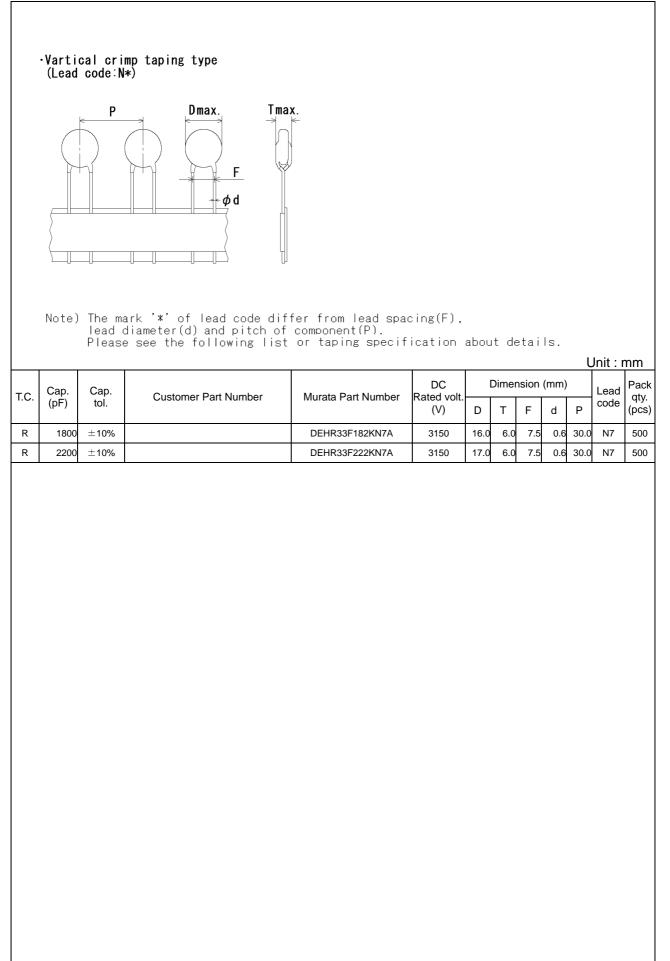




1				-							
·Straight short type ·Vertical crimp short type (Lead code:D*) (Lead code:B*)											
N		max.	Dmax. $5.0\pm1.0$ F $\pm 0.8$ Tmax. f = 0.8	$\pm 0.05$ $\downarrow$ $\downarrow$ $\downarrow$ $\pm 0.05$ F $\pm 0.8$							
		lead dia	meter(d). ee the following list abo		, y unu				l	Jnit :	mm
T.C.	Cap. (pF)	Cap. tol.	Customer Part Number	Murata Part Number	DC Rated Volt. (V)		nensi T	on (mi F	m) d	Lead Code	Pack qty. (pcs)
R	220	±10%		DEHR33D221KD3B	2000	7.0	5.0	7.5	0.6	D3	500
R	270	±10%		DEHR33D271KD3B	2000	7.0	5.0	7.5	0.6	D3	500
R	330	±10%		DEHR33D331KB3B	2000	8.0	5.0	7.5	0.6	B3	500
R	390	±10%		DEHR33D391KB3B	2000	8.0	5.0	7.5	0.6	B3	500
R	470	±10%		DEHR33D471KB3B	2000	9.0	5.0	7.5	0.6	B3	500
R	560	±10%		DEHR33D561KB3B	2000	9.0	5.0	7.5	0.6	B3	500
R	680	±10%		DEHR33D681KB3B	2000	10.0	5.0	7.5	0.6	B3	500
R	820	±10%		DEHR33D821KB3B	2000	11.0	5.0	7.5	0.6	B3	500
R	1000	±10%		DEHR33D102KB3B	2000	12.0	5.0	7.5	0.6	B3	250
R	1200	±10%		DEHR33D122KB3B	2000	12.0	5.0	7.5	0.6	B3	250
R	1500	±10%		DEHR33D152KB3B	2000	12.0	5.0	7.5	0.6	B3	250
R	1800	±10%		DEHR33D182KB3B	2000	14.0	5.0	7.5	0.6	B3	250
R	2200	±10%		DEHR33D222KB3B	2000	15.0	5.0	7.5	0.6	B3	200
R	2700			DEHR33D272KB3B	2000	17.0	5.0	7.5	0.6	B3	200
R	3300			DEHR33D332KB4B	2000	19.0	5.0	10.0	0.6	B4	100
R	3900			DEHR33D392KB4B	2000	20.0	5.0	10.0	0.6	B4	100
R	4700			DEHR33D472KB4B	2000	21.0	5.0	10.0	0.6	B4	100
R	150			DEHR33F151KD3B	3150	7.0	6.0	7.5	0.6	D3	500
R	180			DEHR33F181KD3B	3150	7.0	6.0	7.5	0.6	D3	500
R	220			DEHR33F221KD3B	3150	7.0	6.0	7.5	0.6	D3	500
R R	270			DEHR33F271KD3B	3150	7.0	6.0	7.5	0.6	D3 B3	500 500
R	330 390			DEHR33F331KB3B DEHR33F391KB3B	3150 3150	8.0 9.0	6.0 6.0	7.5 7.5	0.6 0.6	B3 B3	500
R	390 470			DEHR33F391KB3B	3150	9.0	6.0 6.0	7.5 7.5	0.6	B3 B3	500
R	560			DEHR33F561KB3B	3150	10.0	6.0	7.5	0.6	B3	500
R	680			DEHR33F581KB3B	3150	11.0	6.0	7.5	0.6	B3	500
R	820			DEHR33F821KB3B	3150	12.0	6.0	7.5	0.6	B3	250
	020	_ 1070			0.000	12.0	0.0	7.5	5.0	20	

Up to the end of crimp $F \pm 0.8$ Note) The mark '*' of lead code differ from lead spacing(F) and											
		lead di	ameter(d). see the following list al							1.20	
					DC	Dir	nensi	on (mm)		Unit : m	Deals
T.C.	Cap. (pF)	Cap. tol.	Customer Part Number	Murata Part Number R	Rated Volt. (V)	D	Т	F	d	Code	ativ
R	1000	±10%		DEHR33F102KB3B	3150	13.0	6.0	7.5	0.6	B3	250
R	1200	±10%		DEHR33F122KB3B	3150	14.0	6.0	7.5	0.6	B3	250
R	1500	±10%		DEHR33F152KB3B	3150	15.0	6.0	7.5	0.6	B3	200
R	1800	±10%		DEHR33F182KB3B	3150	16.0	6.0	7.5	0.6	B3	200
R	2200			DEHR33F222KB3B	3150	17.0			0.6		200
R	2700	±10%		DEHR33F272KB4B	3150	19.0	6.0	10.0	0.6	B4	100





# **Reference only**

		-								
					The econosite			d by polyo	davaa	
Appearance and c	limensions	form and dime	nsions.		for visible evidence of defect.					
Marking				orj.	The capacitor should be inspected by naked eyes.					
Dielectric strength	Dielectric Between lead No failure.				The capacito voltage of 20 between the	r should n 0% of the lead wires	ot be dama rated volta for 1 to 5	aged whei ige are ap s.	n DC	
	Body insulation	No failure.			balls of diameter 1mm so that each shortcircuited, is kept about 2mm off the balls as shown in the figure, and AC1250V (r.m.s.)<50/60Hz> is applied for 1 to 5 s between capacitor lead wires and small metals.					
Resistance (I.R.)	Between lead wires		-						red with	
Capacitance		Within specifie	d tolerance.						°C with	
Dissipation Factor	(D.F.)	0.2% max.							at 20°C	
7 Temperature characteristic Temp. char. The capacitance measur				urement sh		nade at				
							aced at	1		
			Step Temp.(°C)	1 20±	2 2 -25±3	3 20±2	4 125±2	5 20±2		
Strength of lead	Pull Bending	Lead wire should not cut off. Capacitor should not be broken.			As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 s. Each lead wire should be subjected to 5N of weight and bent 90° at the point of egress, in one direction, then returned to its original position and bent 90° in the opposite direction at the rate of one bend in 2 to					
Vibration	Appearance	No marked def	fect.			r should b	e firmly so	ldered to t	he	
resistance	Capacitance D.F.	Within specified tolerance. 0.2% max.			supporting lead wire and vibrated at a frequency range of 10 to 55Hz, 1.5mm in total amplitude, with about a 1min rate of vibration change from 10Hz to 55Hz and back to 10Hz. Apply for a total of 6 h; 2 h each in 3 mutually perpendicular directions.					
	Lead wire should be soldered with uniformly coated on the axial direction over 3/4 of the circumferential direction.			The lead wire of a capacitor should be dipped into a ethanol solution of 25wt% rosin and then into molten solder for 2±0.5 s. In both cases the depth of dipping is up to about 1.5 to 2mm from the root of lead wires. Temp. of solder : 245±5°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±5°C H63 Eutectic Solder						
* "room condition" I	emperature: 15 to	35°C, Relative f	numiaity: 45 to 7	′5%, <i>I</i>	Atmospheric p	ressure: 8	6 to 106kF	'a		
	Ite Appearance and c Marking Dielectric strength Insulation Resistance (I.R.) Capacitance Dissipation Factor Temperature chara Strength of lead	Dielectric strengthBetween lead wiresBody insulationBody insulationInsulation Resistance (I.R.)Between lead wiresCapacitanceDissipation Factor (D.F.)Temperature characteristicTemperature characteristicStrength of leadPullBendingBendingVibration resistanceAppearance Capacitance D.F.Solderability of leadsSolderability of leads	Item         Spe           Appearance and dimensions         No marked deform and dime Please refer to To be easily le Please refer to to easily le Strength         To be easily le Please refer to to easily le Please refer to to easily le Please refer to wires           Dielectric strength         Between lead wires         No failure.           Insulation Resistance (I.R.)         Between lead wires         10 000 MΩ min vires           Capacitance         Within specifie         0.2% max.           Temperature characteristic         T.C25 to +8E R within±15         R within±15           Strength of lead         Pull         Lead wire shot Capacitor shot Ca	Item         Specification           Appearance and dimensions         No marked defect on appeara form and dimensions. Please refer to [Part number li To be easily legible.           Marking         To be easily legible.           Dielectric strength         Between lead wires         No failure.           Body insulation         Between lead wires         10000MΩ min.           Resistance (I.R.)         Between lead wires         10000MΩ min.           Capacitance         Within specified tolerance.           Dissipation Factor (D.F.)         0.2% max.           Temperature characteristic         T.C.         Temp. chat. -25 to +85°C           R         within=15%         Within=15/3           Pre-treatment : Capacitor shout *room conditio         Step           Temp.(*C)         Step         Temp.(*C)           Strength of lead         Pull         Lead wire should not cut off. Capacitor should not be broke           Wibration resistance         Appearance         No marked defect.           O.F.         0.2% max.         Solderability of leads           Vibration resistance         Lead wire should be soldered. 0.F.         0.2% max.	Item         Specification           Appearance and dimensions         No marked defect on appearance form and dimensions. Please refer to [Part number list]. To be easily legible.           Marking         To be easily legible.           Dielectric strength         Between lead wires         No failure.           Body insulation         No failure.           Insulation Resistance (I.R.)         Between lead wires         10000MΩ min.           Dissipation Factor (D.F.)         0.2% max.           Temperature characteristic         T.C.         Temp.char. +85 to +126°C R           R         within specified tolerance.           Dissipation Factor (D.F.)         0.2% max.           Temperature characteristic         T.C.         Temp.char. +700 condition for Step           Image: Plant Resistance         Pre-treatment : Capacitor should be *room condition for Step           Strength of lead         Pull         Lead wire should not cut off. Capacitor should not be broken.           Bending         Bending         Vibration resistance         No marked defect. Capacitance           D.F.         0.2% max.         0.2% max.           Solderability of leads         Lead wire should be soldered with uniformly coated on the axial direction over 3/4 of the circumferential direction.	Item         Specification           Appearance and dimensions         No marked defect on appearance provide the provide the prov	Item         Specification         Term           Appearance and dimensions         No marked defect on appearance form and dimensions. Please refer to [Part number list].         The capacitor should be for visible evidence of to solve easily legible.         The capacitor should be voltage of 200% of the between the lead wires (Charge/Discharge cur balls of diameter 1 mm shortcircuited, is kept a of the balls as shown in the figure, and AC1 250V (rm.s.)-500 is applied for 1 to 5 s between capacitor lead wires           Insulation         Between lead wires         10000MΩ min.         The capacitor is place balls of diameter 1 mm shortcircuited, is kept a of the balls as shown in the figure, and AC1 250V (rm.s.)-500 is applied for 1 to 5 s between capacitor lead wires and mattabal (Charge/Discharge cur The insulation resistan Charge Cur The insulation resistan Charge Cur The insulation resistan Charge Cur The insulation resistan Charge Cur The capacitance shoul 1-0.24Hz and ACSV(r. within 50-250V within 60- The capacitance shoul 1-0.24Hz and ACSV (rm.s.)-24Hz and AC	Item         Specification         Test method           Appearance and dimensions.         Please refer to [Part number list].         The capacitor should be inspects for mand dimensions.         Dimensions should be measured for be easily legible.           Marking         Dielectric         Between lead wires         No failure.         The capacitor should not be dam voltage of 200% of the rated voltage for to be dam.           Body         Body         No failure.         The capacitor should not be dam.           Body         No failure.         The capacitor should not be dam.           Body         No failure.         The capacitor should not be dam.           Body         No failure.         The capacitor should not be dam.           Body         No failure.         The capacitor should not be dam.           Body         No failure.         The capacitor should not be dam.           Charge/Discharge currents50m/d         The capacitor should not the capacitor is placed in the cor abilis of diameter trum so that eas shorticruited, is kept aboult 2m.           Insulation         Between lead         10000MΩ min.         The insulation resistance should be expective should be mease to the paperiod read           Insulation         Between lead         10000MΩ min.         The capacitor should be mease to the paperiod read           Insulation         Between lead         10000MΩ min.	Item         Specification         Testmethod           Appearance and dimensions         No marked defect on appearance, for visible evidence of defert.         The capacitor should be inspected by nake.           Marking         To be easily legible.         The capacitor should be inspected by nake with side withs disc appearance in the capacitor should be inspected by nake with side appearance with side appearance in the capacitor should be inspected by nake with side appearance in the capacitor should be inspected by nake in the container with bias as shown in the balas as shown in the fault and short (cuted, is kept about 2 mm of the balas as shown in the fault appearing currents:50mA).           Insulation         Between lead         10000MΩ min.         The insulation resistance should be measued at 200% (cm.s.) max.           Resistance (LR.)         wires         Within specified tolerance.         The capacitor should be measured at 200×100 with the 200×100 within the 200×10 withi	

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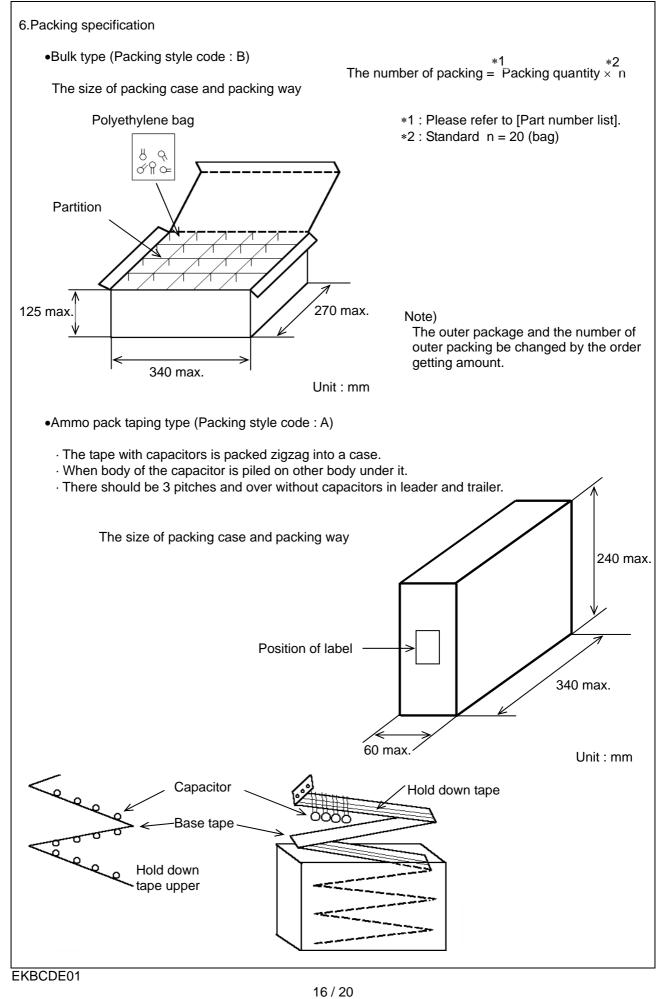
# **Reference only**

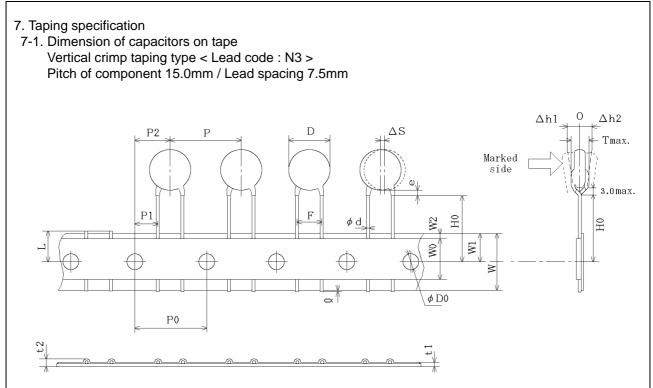
No.	lte	m	Specification	Test method
11	Soldering effect		No marked defect.	The lead wire should be immersed into the melted
		Appearance		
	(Non-preheat)	Capacitance	Within $\pm$ 10%	solder of 350±10°C up to about 1.5 to 2.0mm from
1		change		the main body for 3.5±0.5 s.
		Dielectric	Per item 3.	Pre-treatment:
		strength		Capacitor should be stored at 125±3°C for 1 h,
		(Between lead		then placed at *room condition for $24\pm2$ h
		wires)		before initial measurements.
				Post-treatment :
				Capacitor should be stored for 24±2 h at *room
				condition.
40	Caldening offerst	A	No montred defect	
12	Soldering effect	Appearance	No marked defect.	First the capacitor should be stored at 120+0/-5°C
	(On-preheat)	Capacitance	Within ± 10%	for 60+0/-5 s.
		change		Then, as in figure, the lead wires should be
		Dielectric	Per item 3.	immersed solder of 260+0/-5°C up to 1.5 to 2.0mm
				from the root of terminal for 7.5+0/-1 s.
		strength		
		(Between lead		
		wires)		Thermal Capacitor
				insulating
				= ===   = +== \ ^
				└└───────────────────────────────────
				Des terreterest
				Pre-treatment :
				Capacitor should be stored at 125±3°C for 1 h,
1				then placed at *room condition for 24±2 h
				before initial measurements.
1				Post-treatment :
				Capacitor should be stored for 24±2 h at *room
				condition.
13	Humidity	Appearance	No marked defect.	Set the capacitor for 500 +24/-0 h at 40±2°C in 90
	(Under steady	Capacitance		to 95% relative humidity.
			Within ±10%	
	state)	change		Pre-treatment :
		D.F.	0.4% max.	Capacitor should be stored at 125±3°C for 1 h,
		I.R.	1 000MΩ min.	then placed at *room condition for 24±2 h
				before initial measurements.
				Post-treatment :
				Capacitor should be stored for 1 to 2 h at *room
				condition.
14	Humidity loading	Appearance	No marked defect.	Apply the rated voltage for 500 +24/-0 h at 40±2°C
		Capacitance	Within ±10%	in 90 to 95% relative humidity.
				(Charge/Discharge current≤50mA.)
		change		
		D.F.	0.6% max.	Pre-treatment :
		I.R.	1 000MΩ min.	Capacitor should be stored at 125±3°C for 1 h,
				then placed at *room condition for 24±2 h
				before initial measurements.
				Post-treatment :
1				Capacitor should be stored for 1 to 2 h at *room
L				condition.
15	Life	Appearance	No marked defect.	Apply a DC voltage of 150% of the rated voltage
1		Capacitance	Within ±10%	for 1000 +48/-0 h at 125±2°C, and relative humidity
1		change		of 50% max
1		D.F.	0.4% max.	(Charge/Discharge current≤50mA.)
1				Pre-treatment :
1		I.R.	2000MΩ min.	
				Capacitor should be stored at 125±3°C for 1 h,
1				then placed at *room condition for 24±2 h
1				before initial measurements.
1				Post-treatment :
1				Capacitor should be stored at 125±3°C for 1 h, then
1				
<u> </u>				placed at *room condition for 24±2 h.
1	* "room condition"	Temperature: 15 to	35°C, Relative humidity: 45 to 75%,	, Atmospheric pressure: 86 to 106kPa
1			-	
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ESDEH02A

# **Reference only**

		Reference on	
No. I	tem	Specification	Test method
16 Temperature	Appearance	No marked defect.	The capacitor should be subjected to 5 temperature
cycle	Capacitance	Within ±10%	cycles.
	change		<temperature cycle=""></temperature>
	D.F.	0.4% max.	Step Temperature(°C) Time
	I.R.	1 000MΩ min.	
	Dielectric	Per item 3.	2 Room Temp. 3 min
	strength		3 +125±3 30 min
	(Between lead		4 Room Temp. 3 min
	wires)		
			Cycle time : 5 cycle
			Pre-treatment :
			Capacitor should be stored at 125±3°C for 1 h,
			then placed at *room condition for 24±2 h
			before initial measurements.
			Post-treatment :
			Capacitor should be stored for 24±2 h at *room
			condition. 5%, Atmospheric pressure: 86 to 106kPa



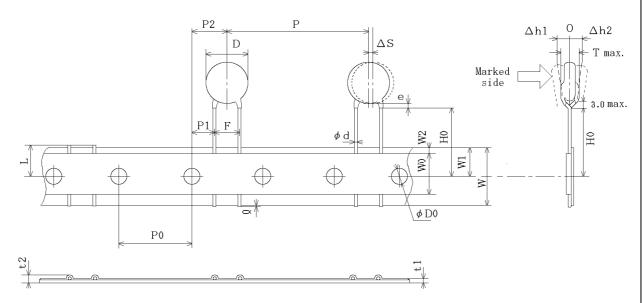


Unit : mm

Item	Code	Dimensions	Remarks
Pitch of component	Р	15.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	$18.0\pm^{2.0}_{0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	There is should be following to be a this large set
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	2.0 may	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of	crimp
Body thickness	Т	Please refer to [	Part number list ].

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Vertical crimp taping type < Lead code : N7 > Pitch of component 30.0mm /Lead spacing 7.5mm

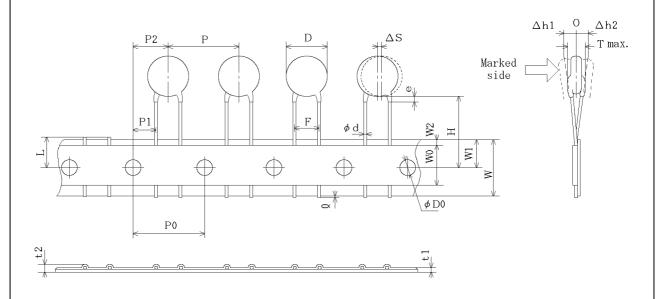


Unit : mm

Item	Code	Dimensions	Remarks
Pitch of component	Р	30.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend.
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	$18.0\pm^{2.0}_{0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	0.0	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of	crimp
Body thickness	Т	Please refer to [	Part number list ].

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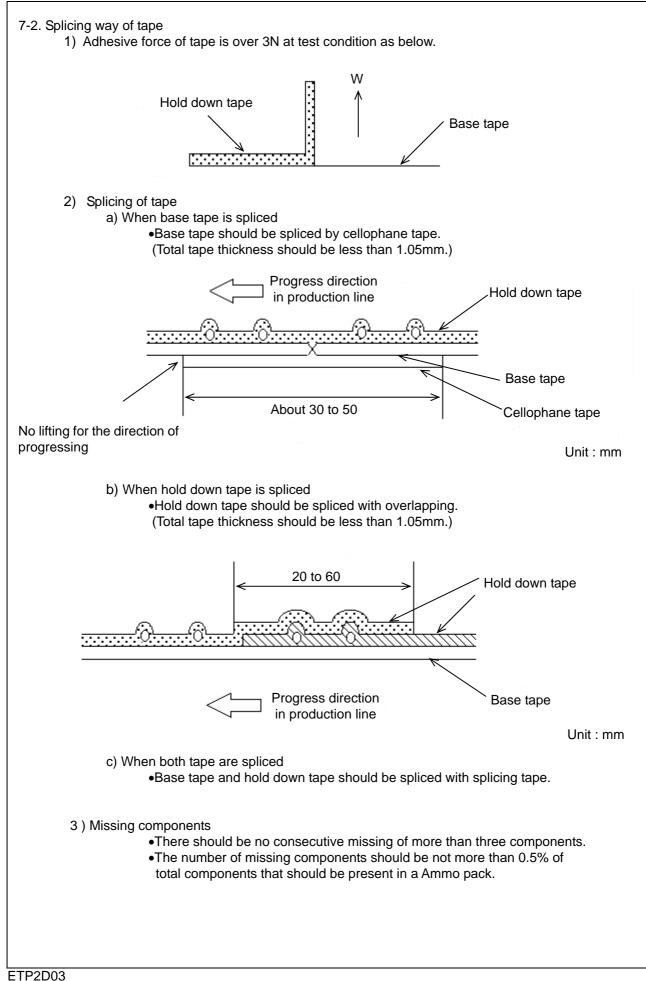
Straight taping type < Lead code : P3 > Pitch of component 15.0mm / Lead spacing 7.5mm



Unit : mm

	<b>.</b> .	<b>_</b>	
Item	Code	Dimensions	Remarks
Pitch of component	Р	15.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	н	$20.0\pm^{1.5}_{1.0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1		
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	3.0 max.	
Body thickness	Т	Please refer to [	Part number list ].

ETP1P30101A



This products of the following crresponds to EU RoHS 当製品は以下の欧州RoHSに対応しています。

(1) RoHS

EU RoHs 2011/65/EC compliance 2011/65/EC(改正RoHS指令)に対応

maximum concentration values tolerated by weight in homogeneous materials

1000 ppm maximum Lead

•1000 ppm maximum Mercury

•100 ppm maximum Cadmium

•1000 ppm maximum Hexavalent chromium

•1000 ppm maximum Polybrominated biphenyls (PBB)

•1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

鉛:1000ppm以下
 水銀:1000ppm以下
 カドミウム:100ppm以下
 六価クロム:1000ppm以下
 ポリ臭化ビフェニル(PBB):1000ppm以下
 ポリ臭化ジフェニルエーテル(PBDE):1000ppm以下