



Dual P-Channel 2.5-V (G-S) MOSFET

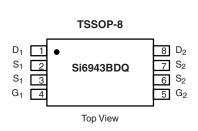
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 12	0.08 at $V_{GS} = -4.5 \text{ V}$	- 2.5		
	0.105 at V _{GS} = - 2.5 V	- 1.9		

FEATURES

- Halogen-free Option Available
 TrenchFET® Power MOSFETs

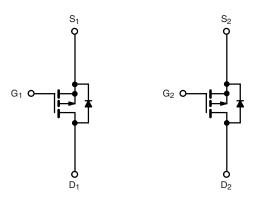


RoHS³ COMPLIANT



Ordering Information: Si6943BDQ-T1

Si6943BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 12		V	
Gate-Source Voltage		V_{GS}	± 8			
Continuous Dunin Courset /T 450 °C\d	T _A = 25 °C	I _D	- 2.5	- 2.3		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 2.2	- 1.8		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 20		A	
Continuous Source Current (Diode Conduction) ^a		l _S - 1.0 - 0.7		- 0.7		
Mariana Barra Birainating	T _A = 25 °C	P _D	1.10	0.80	W	
Maximum Power Dissipation ^a	T _A = 70 °C		0.70	0.50		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	R _{thJA}	89	110		
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	120	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	70	90		

a. Surface Mounted on 1" x 1" FR4 board.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

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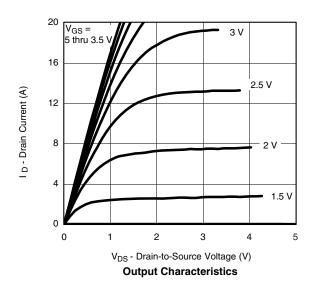
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = - 9.6 V, V _{GS} = 0 V			- 1	μА	
	I _{DSS}	$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 10			Α	
	D	$V_{GS} = -4.5 \text{ V}, I_D = -2.5 \text{ A}$	$I_{GS} = -4.5 \text{ V}, I_D = -2.5 \text{ A}$		0.08		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 1.9 A		0.08	0.105	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 2.5 A		8		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.0 A, V _{GS} = 0 V		- 0.75	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			5.7	10		
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.5 \text{ A}$		0.8		nC	
Gate-Drain Charge	Q_{gd}			1.6			
Turn-On Delay Time	t _{d(on)}			15	25		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		35	60		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1.0 A, $V_{GEN}=$ - 4.5 V, $R_G=6~\Omega$		35	60	ns	
Fall Time	t _f			30	50		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = -1.0 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		30	60		

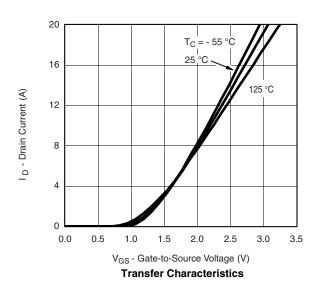
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

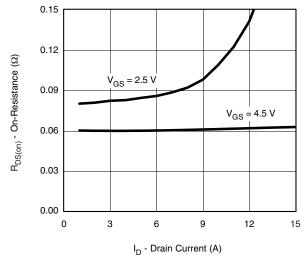
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



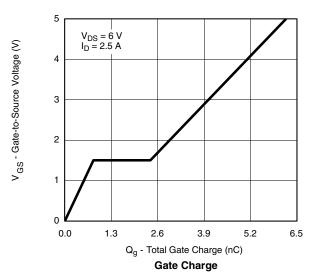




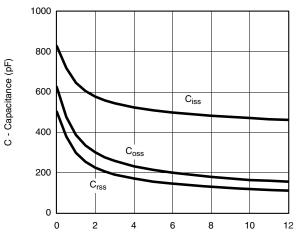
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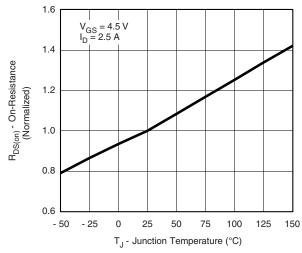
On-Resistance vs. Drain Current



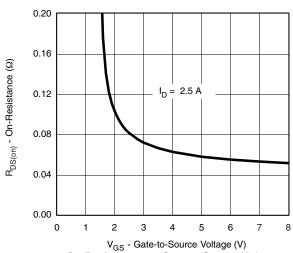
 $T_{J} = 150 \, ^{\circ}\text{C}$ $T_{J} = 25 \, ^{\circ}\text{C}$ $V_{SD} - \text{Source-to-Drain Voltage (V)}$ Source-Drain Diode Forward Voltage



V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

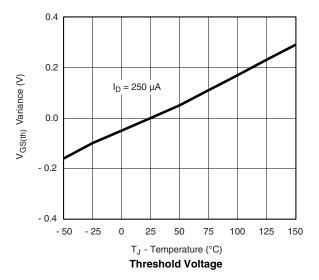
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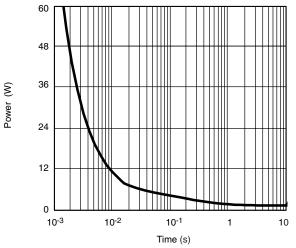
Is - Source Current (A)

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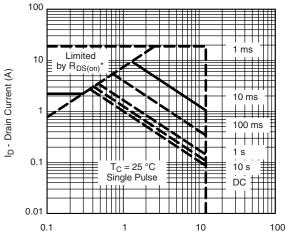
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TYPICAL CHARACTERISTICS 25 °C. unless otherwise noted

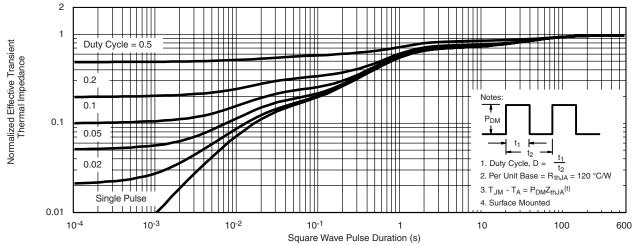




Single Pulse Power, Junction-to-Ambient



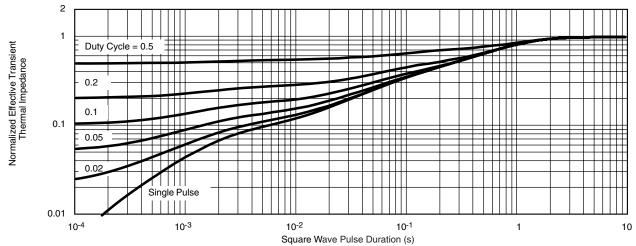
$$\begin{split} &V_{DS}\text{ - Drain-to-Source Voltage (V)}\\ ^*V_{GS}\text{ > minimum }V_{GS}\text{ at which }R_{DS(on)}\text{ is specified}\\ \textbf{Safe Operating Area, Junction-to-Case} \end{split}$$



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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Document Number: 72016 S-81056-Rev. B, 12-May-08

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