



Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$ $I_D(A$			
40	0.021 at V _{GS} = 10 V	7.4		
	0.028 at V _{GS} = 4.5 V	6.4		

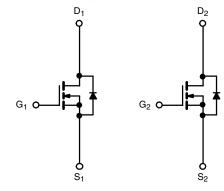
• Halogen-fre

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

ROHS COMPLIANT HALOGEN

APPLICATIONS

- Low Power Synchronous Rectifier
- Automotive 12 V Systems



N-Channel MOSFET

N-Channel MOSFET

		SO-8		
S ₁	1		8	D ₁
G ₁	2		7	D_1
S_2	3		6	D_2
G_2	4		5	D_2
	ļ	Top View	ļ	

Ordering Information: Si4942DY-T1-E3 (Lead (Pb)-free)

Si4942DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T	_A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	40		V
Gate-Source Voltage		V _{GS}	± 20		V
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	- I _D	7.4	5.3	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		5.8	4.3	
Pulsed Drain Current		I _{DM}	30		Α
Avalanche Current	L = 0.1 mH	I _{AS}	25		
Continuous Source Current (Diode Conduction) ^a		I _S	1.8	0.9	
Mariana Pana Pinatanting	T _A = 25 °C	P _D	2.1	1.1	W
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian Instanta Ambienta	t ≤ 10 s	- R _{thJA}	50	60	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	28	34		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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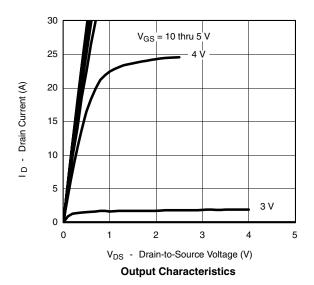
Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit
Static	<u> </u>		1			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	1	V _{DS} = 40 V, V _{GS} = 0 V V _{DS} = 40 V, V _{GS} = 0 V, T _J = 55 °C			1	μΑ
	I _{DSS}				5	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
Drain-Source On-State Resistance ^a	В	$V_{GS} = 10 \text{ V}, I_D = 7.4 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 6.4 \text{ A}$		0.017	0.021	Ω
	R _{DS(on)}			0.023	0.028	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 7.4 A		25		S
Diode Forward Voltage ^a	V_{SD}	I _S = 1.8 A, V _{GS} = 0 V		0.75	1.1	V
Dynamic ^b			1			
Total Gate Charge	Q_g			21	32	
Gate-Source Charge	Q_{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5.7 \text{ A}$		3.3		nC
Gate-Drain Charge	Q_{gd}			5.8		
Gate Resistance	R_{g}		0.5	1.1	1.6	Ω
Turn-On Delay Time	t _{d(on)}			13	20	
Rise Time	t _r	V_{DD} = 20 V, R_L = 20 Ω		10	15	1
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A, V}_{GEN} = 10 \text{ V, R}_g = 6 \Omega$		31	50	ns
Fall Time	t _f			11	20	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.8 A, dI/dt = 100 A/μs		30	60	

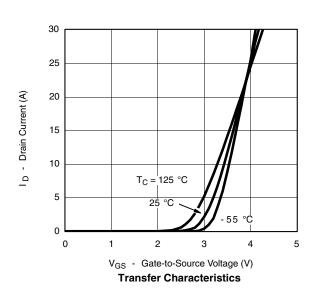
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C unless otherwise noted

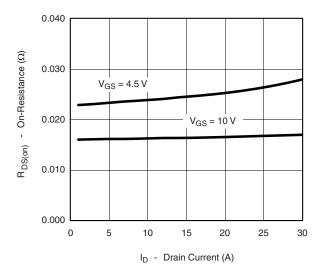




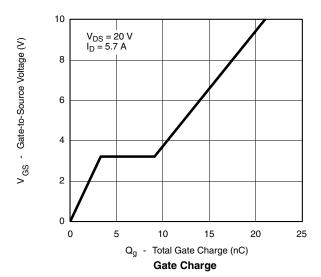


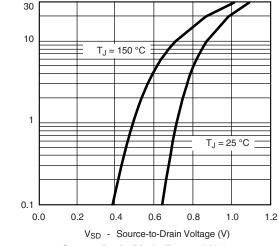


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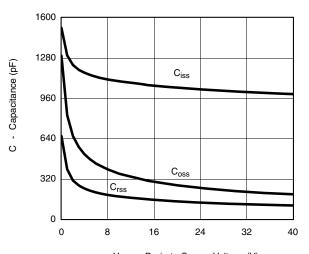


On-Resistance vs. Drain Current



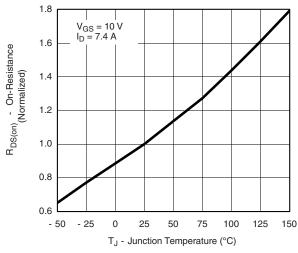


Source-Drain Diode Forward Voltage

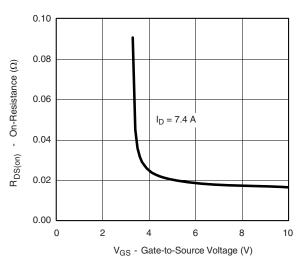


V_{DS} - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature



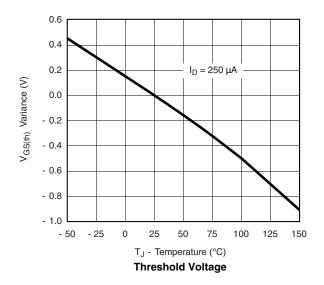
On-Resistance vs. Gate-to-Source Voltage

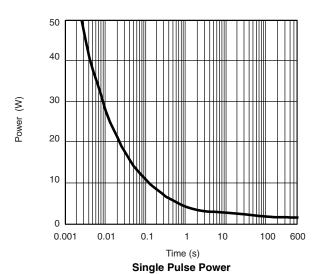
Is - Source Current (A)

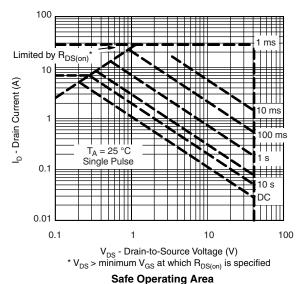
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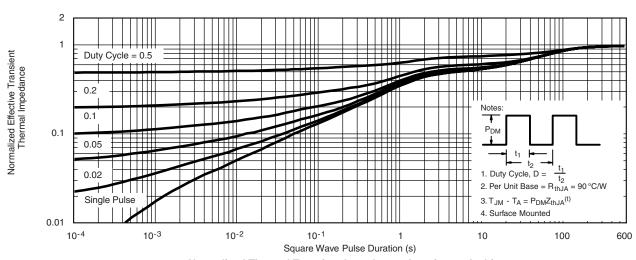
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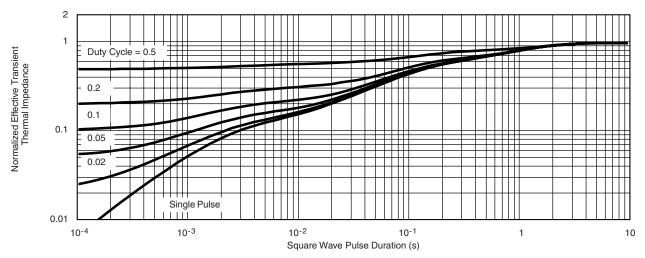








TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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