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Vishay Semiconductors

High Speed Infrared Emitting Diodes, 940 nm,

Surface Emitter Technology



VSMY2941RGX01



DESCRIPTION

As part of the SurfLightTM portfolio, the VSMY2941X01 series are infrared, 940 nm emitting diodes based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

APPLICATIONS

- · Miniature light barrier
- Photointerrupters
- Optical switch

FEATURES

· Package type: surface-mount

· Package form: GW, RGW

Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8

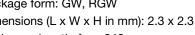
• Peak wavelength: $\lambda_p = 940 \text{ nm}$

- AEC-Q101 qualified
- High radiant power
- · Very high radiant intensity
- Angle of half intensity: φ = ± 8°
- · Terminal configurations: gullwing or reverse gullwing



• Floor life: 4 weeks, MSL 2a, according to J-STD-020

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





AUTOMOTIVE GRADE



- Emitter source for proximity sensors

PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (deg)	$\lambda_{\mathbf{P}}$ (nm)	t _r (ns)	
VSMY2941RGX01	160	± 8	940	5	
VSMY2941GX01	160	± 8	940	5	

Test conditions see table "Basic Characteristics"

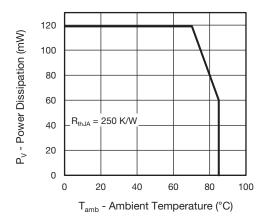
ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY2941RGX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing
VSMY2941GX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_{R}	5	V
Forward current		I _F	70	mA
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	140	mA
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA
Power dissipation		P _V	120	mW
Junction temperature		Tj	100	°C
Operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Soldering temperature	According to Fig. 10, J-STD-020	T _{sd}	260	°C
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	R _{thJA}	250	K/W

Rev. 1.0, 08-Nov-17 Document Number: 84573







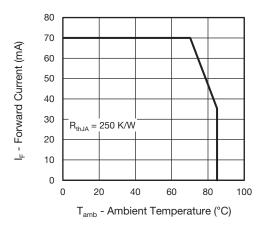


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 50 \text{ mA}, t_p = 20 \text{ ms}$	V _F	-	1.4	1.7	V
Forward voltage	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.5	-	V
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	V _F	-	2.6	-	V
Temperature coefficient of V _F	I _F = 50 mA	TK _{VF}	-	-0.7	-	mV/K
Reverse current		I _R	Not designed for reverse operation µA		μΑ	
Junction capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}, E = 0 \text{ mW/cm}^2$	CJ	-	30	-	pF
	$I_F = 50 \text{ mA}, t_p = 20 \text{ ms}$	l _e	60	115	170	mW/sr
Radiant intensity	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	l _e	-	160	-	mW/sr
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	l _e	-	850	-	mW/sr
Radiant power	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	φ _e	-	40	-	mW
Temperature coefficient of radiant power	I _F = 50 mA	TΚφ _e	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 8	-	deg
Peak wavelength	I _F = 50 mA	λ_{p}	920	940	960	nm
Spectral bandwidth	I _F = 70 mA	Δλ	_	55	_	nm
Temperature coefficient of λ _p	I _F = 70 mA	TKλ _p	-	0.28	-	nm/K
Rise time	I _F = 70 mA, 10 % to 90 %	t _r	_	5	-	ns
Fall time	I _F = 70 mA, 10 % to 90 %	t _f	-	6	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

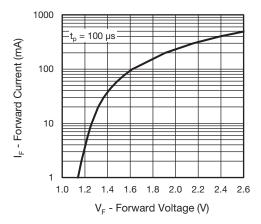


Fig. 3 - Forward Current vs. Forward Voltage

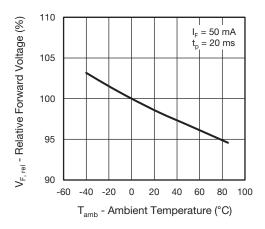


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

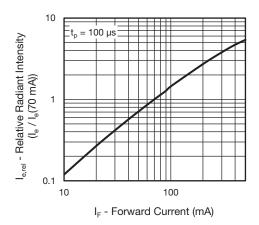


Fig. 5 - Radiant Intensity vs. Forward Current

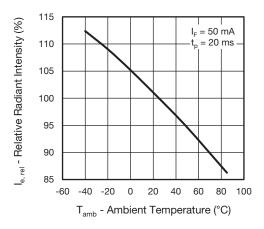


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

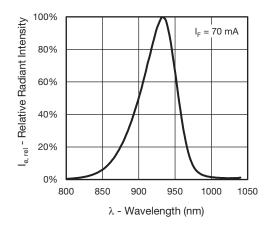


Fig. 7 - Relative Radiant Intensity vs. Wavelength

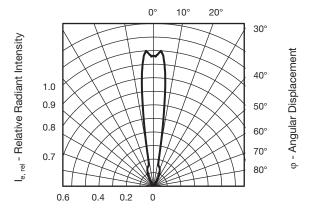


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

SOLDER PROFILE

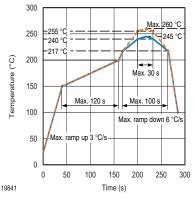


Fig. 9 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

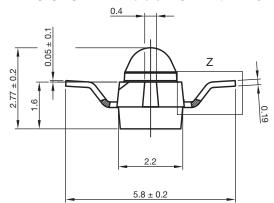
Conditions: T_{amb} < 30 °C, RH < 60 %

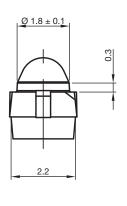
Moisture sensitivity level 2a, according to J-STD-020.

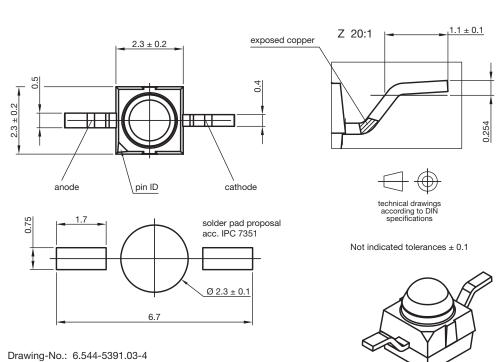
DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

PACKAGE DIMENSIONS in millimeters: VSMY2941RGX01





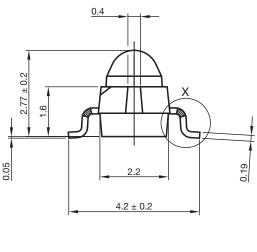


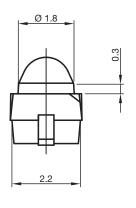
Rev. 1.0, 08-Nov-17 **4** Document Number: 84573

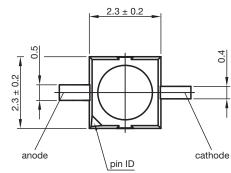
Issue: 2; 19.09.14

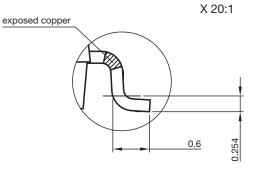
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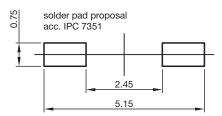
PACKAGE DIMENSIONS in millimeters: VSMY2941GX01





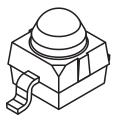








Not indicated tolerances ± 0.1

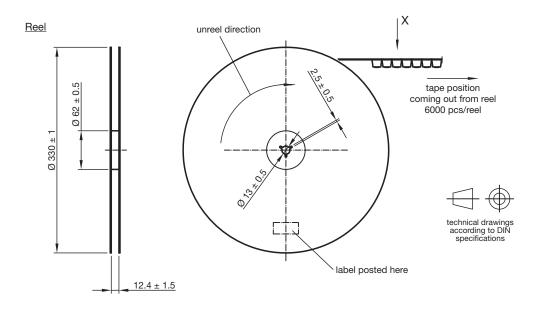


Drawing-No.: 6.544-5383.03-4

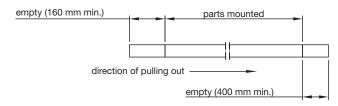
Issue: 2; 19.09.14



TAPING AND REEL DIMENSIONS in millimeters: VSMY2941RGX01



Leader and trailer tape



Terminal position in tape

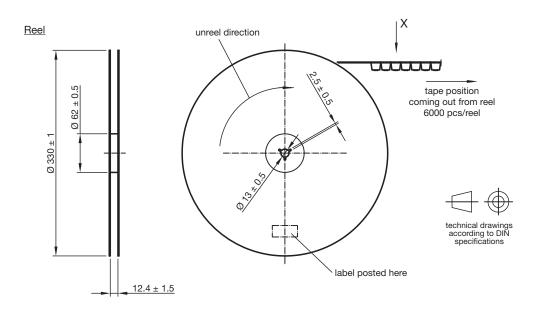
Lead I	Lead II	
Collector Emitte		
Collector	Emitter	
Cathode	Anode	
Anada	Cathode	
Anoue	Califode	
	Collector	

Drawing-No.: 9.800-5100.01-4

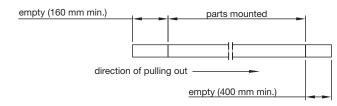
Issue: 4; 19.09.14



TAPING AND REEL DIMENSIONS in millimeters: VSMY2941GX01

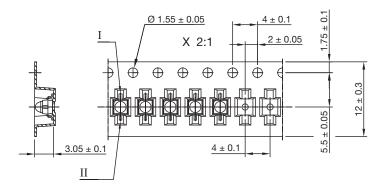


Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II		
VSMB2020				
VSMG2020		Anode		
VEMD2020	Cathode			
VEMD2520				
VSMF2890G				
VEMT2020	Collector	Emitter		
VEMT2520	Collector	Emiller		
VSMY2850G	Anode	Cathode		
VSMY2940G	Alloue	Callioue		



Drawing-No.: 9.800-5091.01-4

Issue: 5; 19.09.14

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