



# N- and P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY							
	V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)				
N-Channel	20	0.0145 at V <sub>GS</sub> = 10 V	9.6				
	20	0.017 at V <sub>GS</sub> = 4.5 V	8.6				
P-Channel	00	0.033 at V <sub>GS</sub> = - 4.5 V	- 6.2				
	- 20	0.050 at V <sub>GS</sub> = - 2.5 V	- 5				

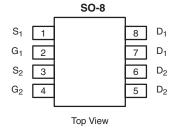
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS directive 2002/95/EC



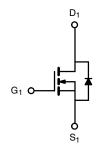
#### **APPLICATIONS**

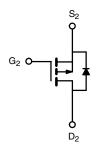
- Level Shift
- Load Switch



Ordering Information: Si4511DY-T1-E3 (Lead (Pb)-free)

Si4511DY-T1-GE3 (Lead (Pb)-free and Halogen-free)





ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted								
Parameter			N-	Channel	P-Channel			
		Symbol	10 s	Steady State	10 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	20		- 20		V	
Gate-Source Voltage	ate-Source Voltage		± 16		± 12		V	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	l <sub>o</sub>	9.6	7.2	- 6.2	- 4.6		
Continuous Drain Current (1) = 150 °C)	T <sub>A</sub> = 70 °C	I <sub>D</sub>	7.7	5.8	- 4.9	- 3.7	Α	
Pulsed Drain Current		I <sub>DM</sub>		40		^		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	0.9	- 1.7	- 0.9		
Marian and Device Dissipations	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2	1.1	2	1.1	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	' D	1.3	0.7	1.3	0.7	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS									
Parameter			N-Ch	annel	P-Ch	P-Channel			
		Symbol	Тур.	Max.	Тур.	Max.	Unit		
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	50	62.5	50	62.5			
	Steady State	' 'thJA	85	110	90	110	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>th.IF</sub>	30	40	30	35			

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b.  $t \le 10 \text{ s}$ .



SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions		Min.	Тур.	Max.	Unit	
Static								
Cata Thurshald Valtage	\/	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	N-Ch	0.6		1.8	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	P-Ch	- 0.6		- 1.4	v	
0 . 5	1	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 16 \text{ V}$	N-Ch			± 100	nA	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	P-Ch			± 100	na 	
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	N-Ch			1		
Zero Gate Voltage Drain Current	Inno	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			- 1	μΑ	
Zero Gate Voltage Diam Current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	N-Ch			5	μΑ	
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	P-Ch			- 5		
On Chata Duain Commanth		$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	40			А	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	- 40				
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 9.6 A	N-Ch		0.0115	0.0145	Ω	
Books Comment On Otala Bookston of	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 6.2 A	P-Ch		0.022	0.033		
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 8.6 A	N-Ch		0.0135	0.017		
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 5 A	P-Ch		0.035	0.050		
h	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 9.6 A	N-Ch		33		٠	
Forward Transconductance <sup>b</sup>		V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 6.2 A	P-Ch		17		S	
5: 1 5 1V 11 h	V <sub>SD</sub>	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V	N-Ch		0.8	1.2		
Diode Forward Voltag <sup>b</sup>		I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V	P-Ch		- 0.8	- 1.2	V	
Dynamic <sup>a</sup>								
Total Gate Charge	Q <sub>g</sub>	N. Charrie	N-Ch		11.5	18		
Total Gate Charge		N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 9.6 \text{ A}$	P-Ch		17	20		
Gate-Source Charge	Q <sub>gs</sub>	The state of the s	N-Ch		3.7		nC	
		P-Channel	P-Ch		4.1 3.3			
Gate-Drain Charge		$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -6.2 \text{ A}$	N-Ch P-Ch		4.3			
			N-Ch		12	20		
Turn-On Delay Time	t <sub>d(on)</sub>	N-Channel	P-Ch		25	40		
		$V_{DD} = 10 \text{ V}, R_L = 10 \Omega$	N-Ch		12	20		
Rise Time		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$	P-Ch		30	45		
Turn-Off Delay Time	t <sub>d(off)</sub>	P-Channel	N-Ch		55	85	ns	
on boldy time	u(OII)	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$	P-Ch		70	105	113	
Fall Time	t <sub>f</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$	N-Ch		15	25 75		
	· .	I <sub>F</sub> = 1.7 A, dI/dt = 100 A/μs	P-Ch		50	75		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 1.7 \text{ A, dl/dt} = 100 \text{ A/µs}$ $I_F = -1.7 \text{ A, dl/dt} = 100 \text{ A/µs}$	N-Ch		50	100		
<u> </u>		i <sub>F</sub> = - 1.7 A, αί/αξ = 100 A/μS	P-Ch		40	80		

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

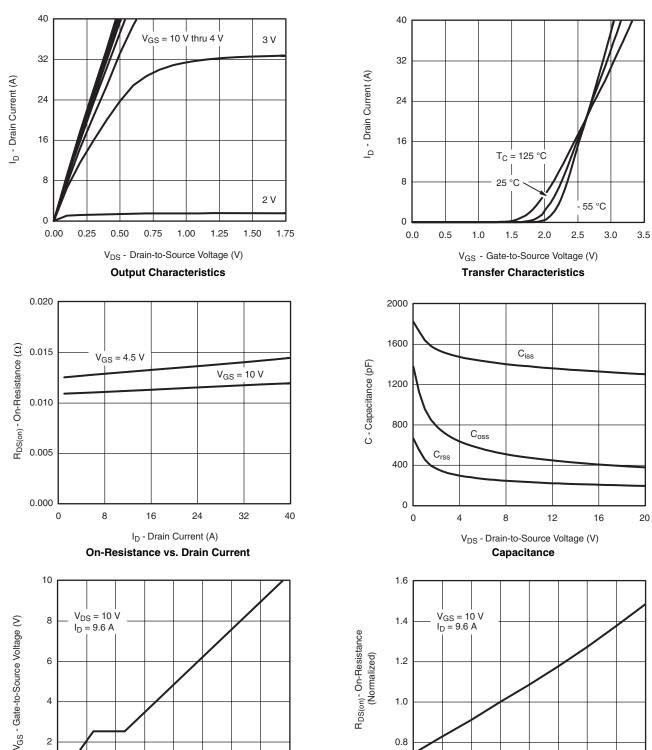
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.





## N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T<sub>J</sub>- Junction Temperature (°C) On-Resistance vs. Junction Temperature

50

75

100

25

0.8

0.6

- 50

- 25

0

2

0

0

3

6

9

12

Q<sub>g</sub> - Total Gate Charge (nC)

**Gate Charge** 

15

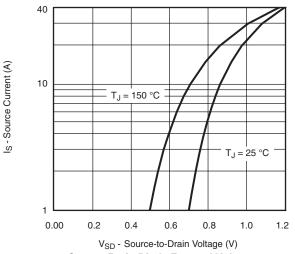
18

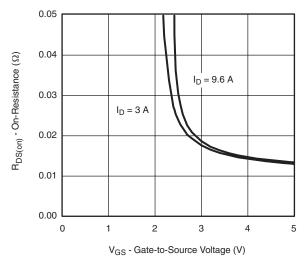
21

24

125 150

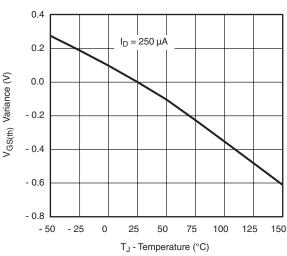
## N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

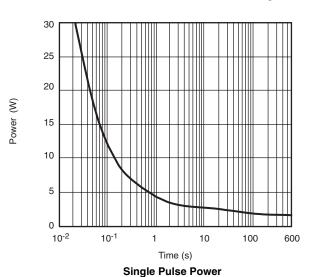




Source-Drain Diode Forward Voltage

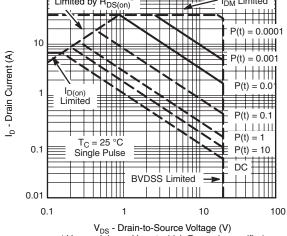
On-Resistance vs. Gate-to-Source Voltage





**Threshold Voltage** 

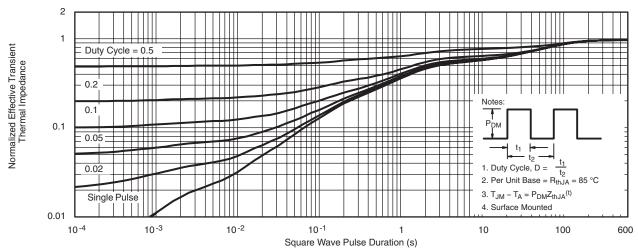
100 Limited by R<sub>DS(on)</sub>  $P(t) = 0.000^{\circ}$ 



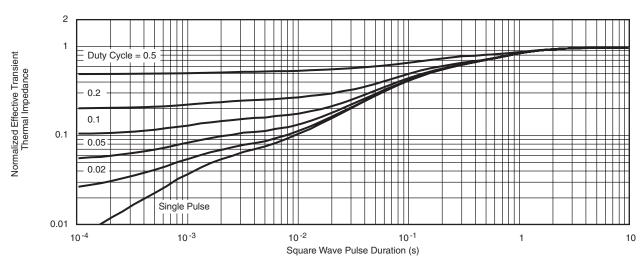
 $\label{eq:VDS} V_{DS} \text{ - Drain-to-Source Voltage (V)} \\ \text{* } V_{DS} \text{ > minimum } V_{GS} \text{ at which } R_{DS(on)} \text{ is specified}$ 



## N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



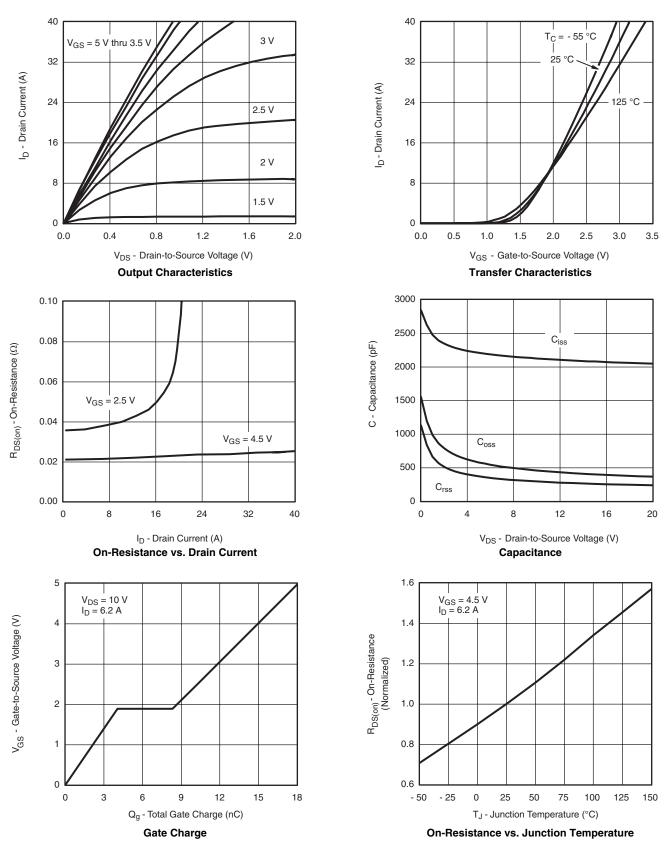
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



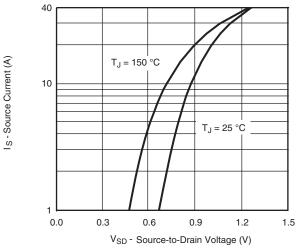
## P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

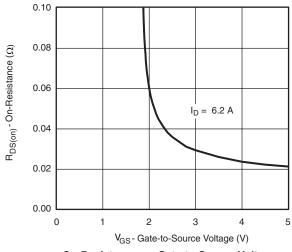






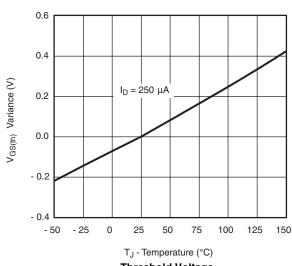
## P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

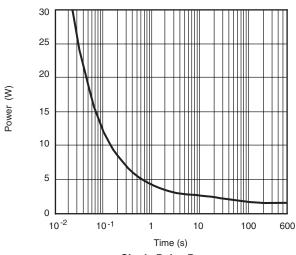




Source-Drain Diode Forward Voltage

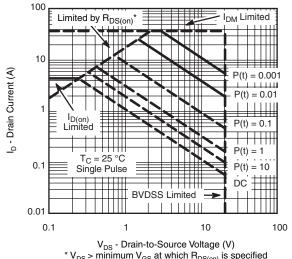






**Threshold Voltage** 

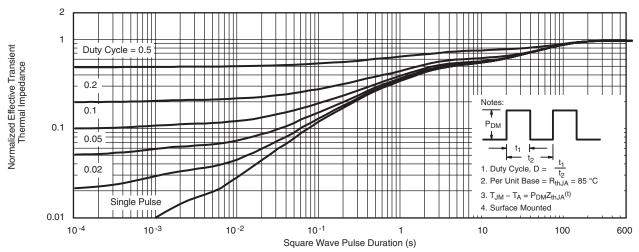
Single Pulse Power



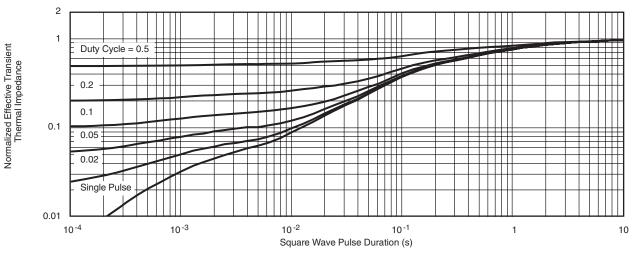
\*  $V_{DS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified Safe Operating Area



## P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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