

Vishay Siliconix

RoHS

COMPLIANT

FREE

Available

Dual P-Channel 12 V (D-S) MOSFET

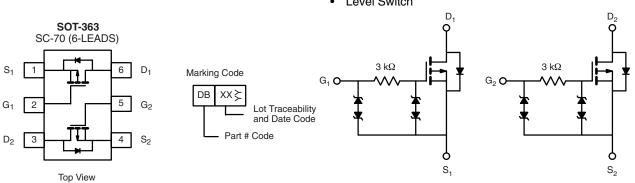
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
	0.370 at V _{GS} = - 4.5 V	- 1.15		
- 12	0.575 at V _{GS} = - 2.5 V	- 0.92		
	0.800 at V _{GS} = - 1.8 V	- 0.78		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
- ESD Protected: 3000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switching
- PA Switch
- Level Switch



Ordering Information: Si1917EDH-T1-E3 (Lead (Pb)-free) Si1917EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 12		v	
Gate-Source Voltage		V _{GS}	± 12		- V	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 1.15	- 1.00		
Continuous Drain Current $(1_j = 150^{\circ} C)$	T _A = 85 °C		- 0.83	- 0.73	A	
Pulsed Drain Current		I _{DM}	- 3			
Continuous Diode Current (Diode Conduction) ^a		ا _S	- 0.61	- 0.47		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	0.73	0.57	w	
	T _A = 85 °C		0.38	0.30		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \le 5 s$	_	130	170	°C/W
	Steady State		170	220	
Maximum Junction-to-Foot (Drain)	Steady State		80	100	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -100 \ \mu A$	- 0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 1.5	μΑ	
		$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 10	mA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -9.6 V, V_{GS} = 0 V$			- 1.0		
		$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 5.0	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 2.0			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.0 A		0.300	0.370		
		V _{GS} = - 2.5 V, I _D = - 0.81 A		0.470	0.575	Ω	
		V _{GS} = - 1.8 V, I _D = - 0.2 A		0.660	0.800		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 1.0 A		1.7		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 0.47 A, V _{GS} = 0 V		- 0.85	- 1.2	V	
Dynamic ^b	·	·					
Total Gate Charge	Qg			1.3	2.0	nC	
Gate-Source Charge	Q _{gs}	V _{DS} = - 6 V, V _{GS} = - 4.5 V, I _D = - 1.0 A		0.31			
Gate-Drain Charge	Q _{gd}			0.31			
Turn-On Delay Time	t _{d(on)}			0.17	0.26		
Rise Time	t _r	$V_{DD} = -6 V, R_1 = 12 \Omega$		0.47	0.71	- μs	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -0.5 \text{ Å}, V_{GEN} = -4.5 \text{ V}, R_g = 6 \Omega$		0.96	1.4		
Fall Time	t _f	1		1.0	1.5		

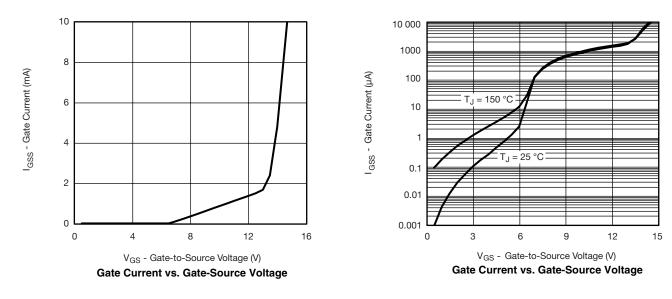
Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

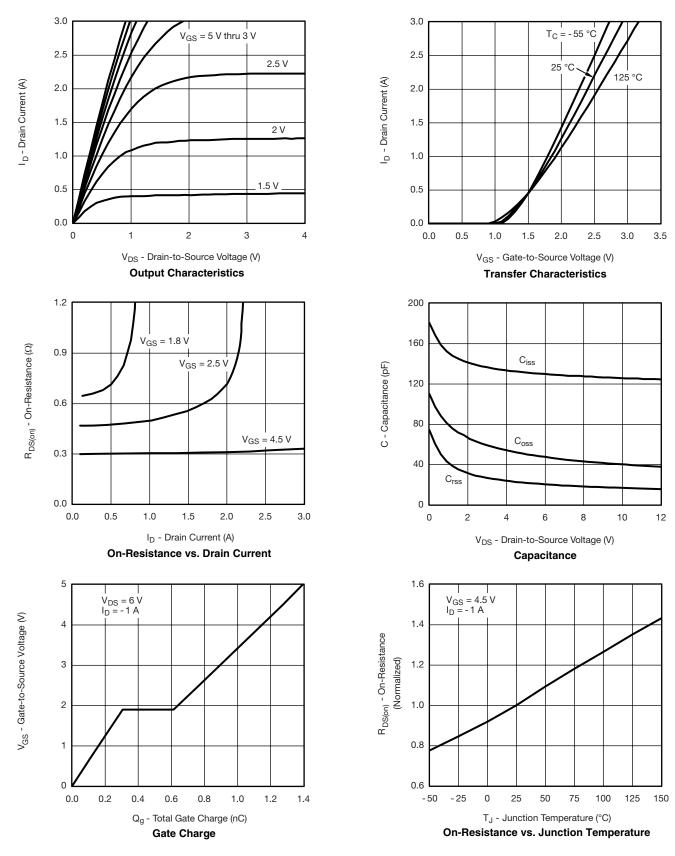


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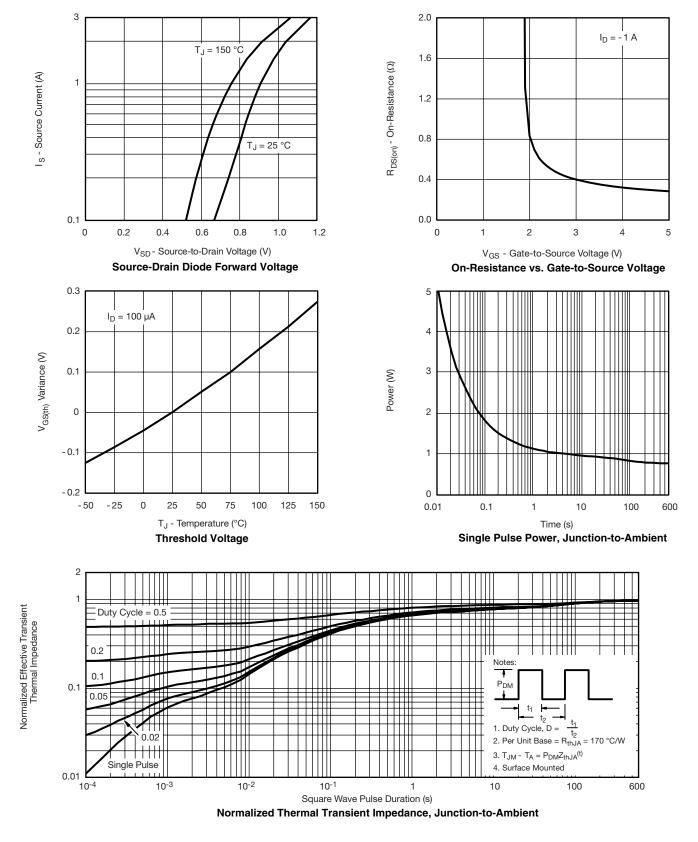
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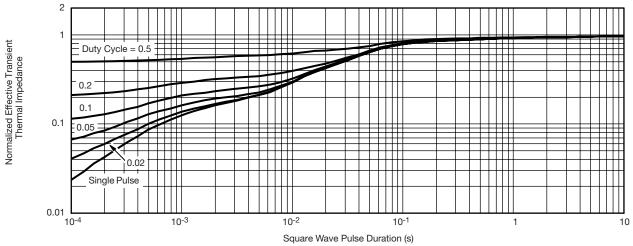
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg271414.



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