

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
30	0.011 at V _{GS} = 10 V	12		
	0.016 at V _{GS} = 4.5 V	9.8		

FEATURES

Halogen-free According to IEC 61249-2-21
Available

GO

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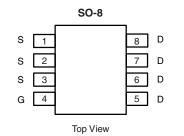
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N-Channel MOSFET

- TrenchFET[®] Power MOSFET
- 100 % R_g Tested



HALOGEN FREE Available





Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	– I _D	12	8.9	
	T _A = 70 °C		9.5	7.1	
Pulsed Drain Current		I _{DM}	40		А
Continuous Source Current (Diode Conduction) ^a		۱ _S	2.3	1.3	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS} 20		20	
Avalanche Energy	L = 0.1 mm	E _{AS}	20		mJ
Mariana David Diasia di sul	T _A = 25 °C	P _D 2.5 1.4		1.4	W
Maximum Power Dissipation ^a	T _A = 70 °C	- ' ⁻ D	1.6	0.9	vv
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	R _{thJA}	43	50		
Maximum Junction-to-Ambient ^a	Steady State	nthJA	73	90	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	19	25		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	· · · · · ·		• 	·			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zaus Cata Maltana Dusia Cumunat	1	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55 \text{ °C}$			1	μA	
Zero Gate Voltage Drain Current	IDSS				5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	30			А	
	Б	V _{GS} = 10 V, I _D = 12 A		0.009	0.011	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 9.8 \text{ A}$		0.013	0.016		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 12 A		32		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = 2.3 A, $V_{\rm GS}$ = 0 V		0.76	1.1	V	
Dynamic ^b							
Input Capacitance	C _{iss}			1580		pF	
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		295			
Reverse Transfer Capacitance	C _{rss}			140			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 12 \text{ A}$		13.2	20		
Iotal Gale Charge	Чg			25.4	38	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 12 A		5.3			
Gate-Drain Charge	Q _{gd}			4.3]	
Gate Resistance	Rg		0.9	1.8	2.7	Ω	
Turn-On Delay Time	t _{d(on)}			13	20		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		10	15]	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong\text{1}$ A, V_GEN = 10 V, R_g = 6 Ω		33	50	ns	
Fall Time	t _f			10	15		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.3 A, dl/dt = 100 A/μs		25	40		

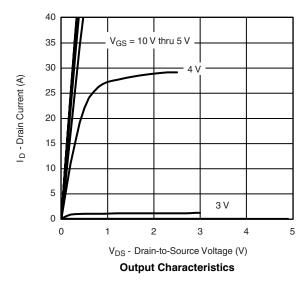
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

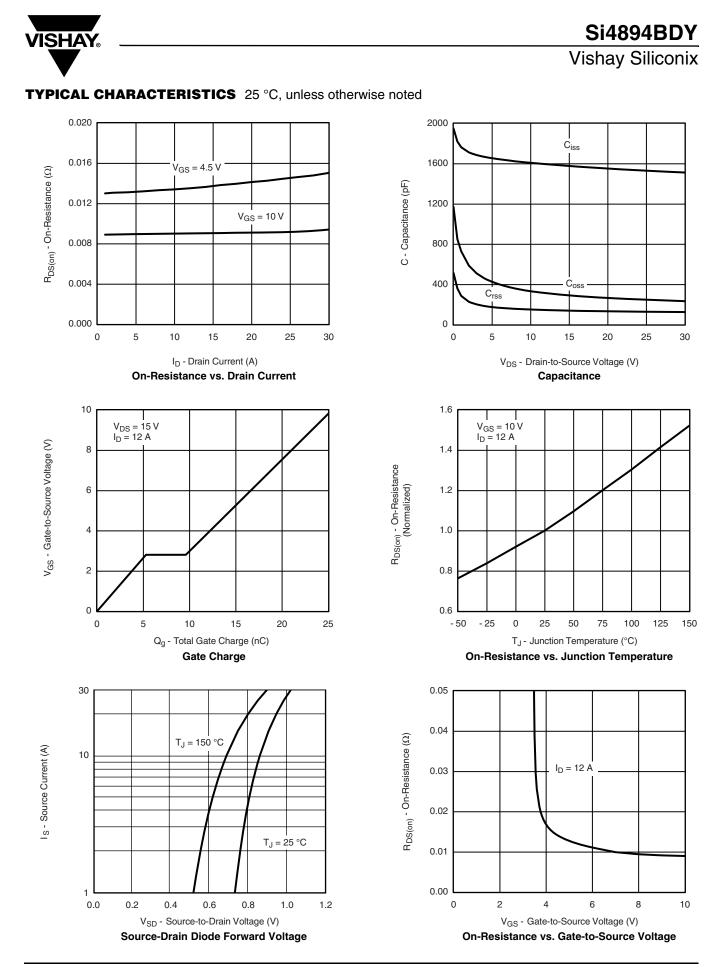
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



40 35 30 I_D - Drain Current (A) 25 20 15 T_C = 125 °C 10 5 25 55 °C 0 0.5 2.0 2.5 3.0 0.0 1.0 1.5 3.5 4.0 4.5 V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**

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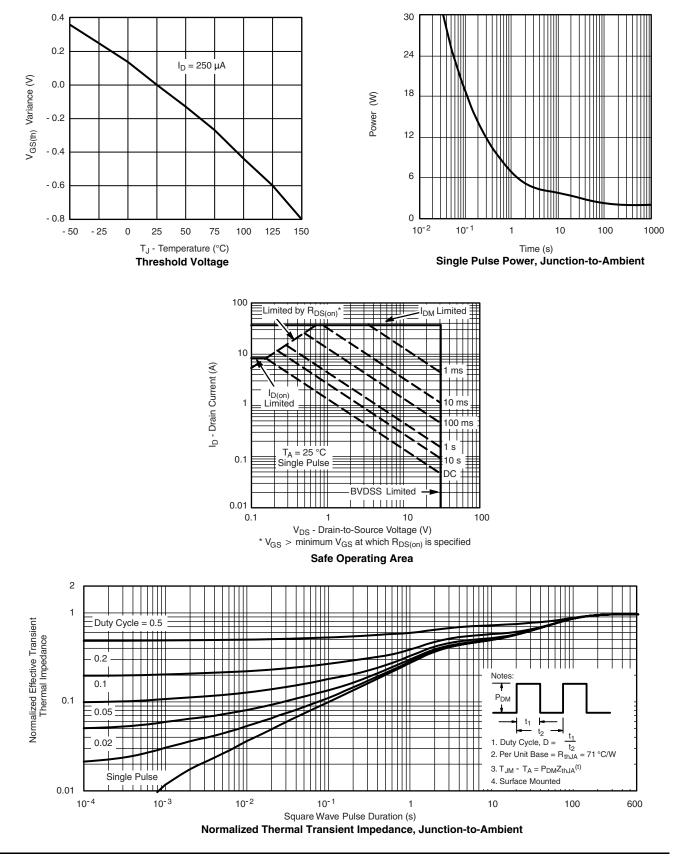
Document Number: 72993 S09-0540-Rev. D, 06-Apr-09

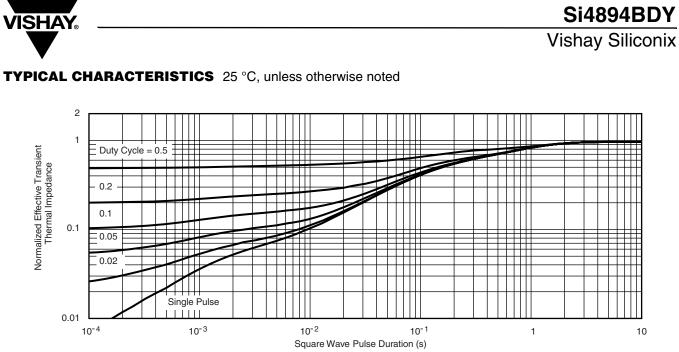
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

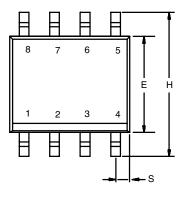
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72993.



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





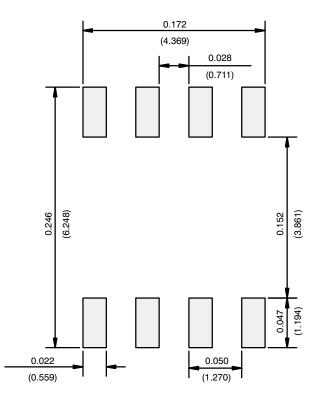
	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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