

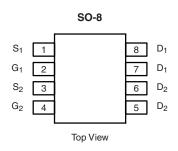
Dual P-Channel 30-V (D-S) MOSFET

| PRODUCT SUMMARY | | | | |
|---------------------|------------------------------------|--------------------|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | | |
| - 30 | 0.053 at V _{GS} = - 10 V | - 4.9 | | |
| | 0.090 at V _{GS} = - 4.5 V | - 3.7 | | |

FEATURES

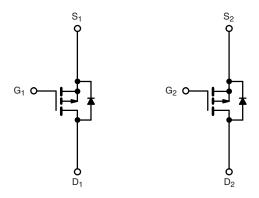
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4953ADY-T1-E3 (Lead (Pb)-free)

Si4953ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted | | | | | | |
|---|------------------------|-----------------------------------|-------------|--------------|------|--|
| Parameter | | Symbol | 10 s | Steady State | Unit | |
| Drain-Source Voltage | | V _{DS} | - 30 | | V | |
| Gate-Source Voltage | | V _{GS} | ± 20 | | | |
| Continuous Dunin Courset /T 150 °C) | T _A = 25 °C | - I _D | - 4.9 | - 3.7 | | |
| Continuous Drain Current (T _J = 150 °C) ^a | T _A = 70 °C | | - 3.9 | - 2.9 | ^ | |
| Pulsed Drain Current | | I _{DM} | - 30 | | Α | |
| Continuous Source Current (Diode Conduction) ^a | | I _S | - 1.7 | - 0.9 | | |
| Maximum Power Dissipation ^a | T _A = 25 °C | P _D | 2.0 | 1.1 | W | |
| | T _A = 70 °C | | 1.3 | 0.7 | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | | °C | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|--------------|---------------------|---------|---------|------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| Marrian Instanta Ambienta | t ≤ 10 s | - R _{thJA} | 52 | 62.5 | | |
| Maximum Junction-to-Ambient ^a | Steady State | | 90 | 110 | °C/W | |
| Maximum Junction-to-Foot (Drain) | Steady State | R_{thJF} | 32 | 40 | | |

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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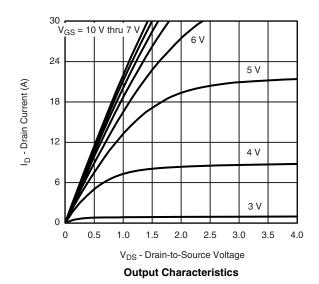
| SPECIFICATIONS T _J = 25 °C, unless otherwise noted | | | | | | | | |
|--|---------------------|--|-------|-------|-------|------|--|--|
| Parameter | Symbol | Test Conditions Min. | | Тур. | Max. | Unit | | |
| Static | | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = -250 \mu A$ | - 1 | | | V | | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA | | |
| Zava Cata Valtaga Dvain Current | I | V _{DS} = - 30 V, V _{GS} = 0 V | | | - 1 | | | |
| Zero Gate Voltage Drain Current | IDSS | V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C | | | - 25 | μΑ | | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} = - 5 V, V _{GS} = - 10 V | - 30 | | | Α | | |
| | D | V _{GS} = - 10 V, I _D = - 4.9 A | 0.045 | | 0.053 | Ω | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = -4.5 \text{ V}, I_D = -3.7 \text{ A}$ | | 0.075 | 0.090 | 52 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 10 V, I _D = - 4.9 A | | 9 | | S | | |
| Diode Forward Voltage ^a | V_{SD} | I _S = - 1.7 A, V _{GS} = 0 V | | - 0.8 | - 1.2 | V | | |
| Dynamic ^b | | | | | | | | |
| Total Gate Charge | Q_g | | | 15 | 25 | | | |
| Gate-Source Charge | Q _{gs} | $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -4.9 \text{ A}$ | | 4 | | nC | | |
| Gate-Drain Charge | Q _{gd} | | | 2 | | | | |
| Turn-On Delay Time | t _{d(on)} | | | 7 | 15 | | | |
| Rise Time | t _r | V_{DD} = - 15 V, R_L = 15 Ω | | 10 | 20 | | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D\cong$ - 1 A, $V_{GEN}=$ - 10 V, $R_g=6~\Omega$ | | 40 | 80 | ns | | |
| Fall Time | t _f | | | 20 | 40 | | | |
| Source-Drain Reverse Recovery Time | t _{rr} | $I_F = -1.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$ | | 30 | 60 | | | |

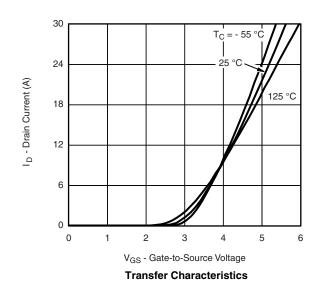
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

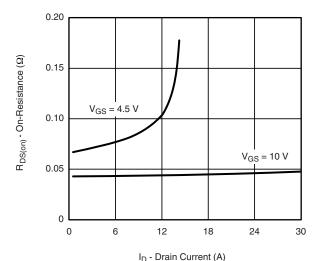
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



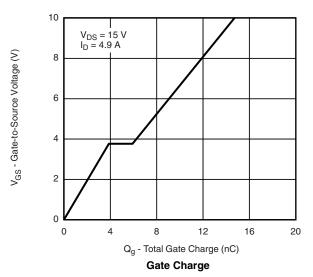


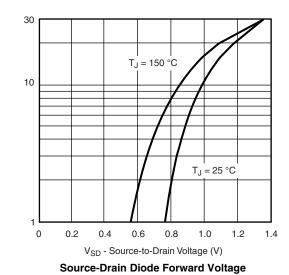


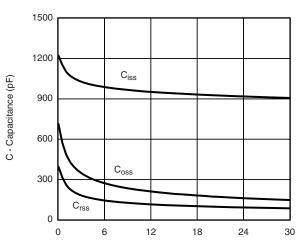
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current

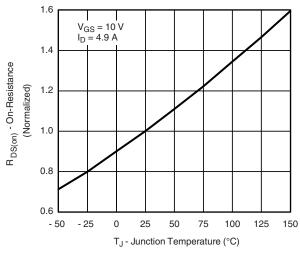




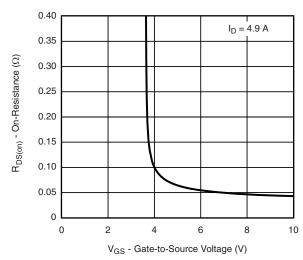


V_{DS} - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature



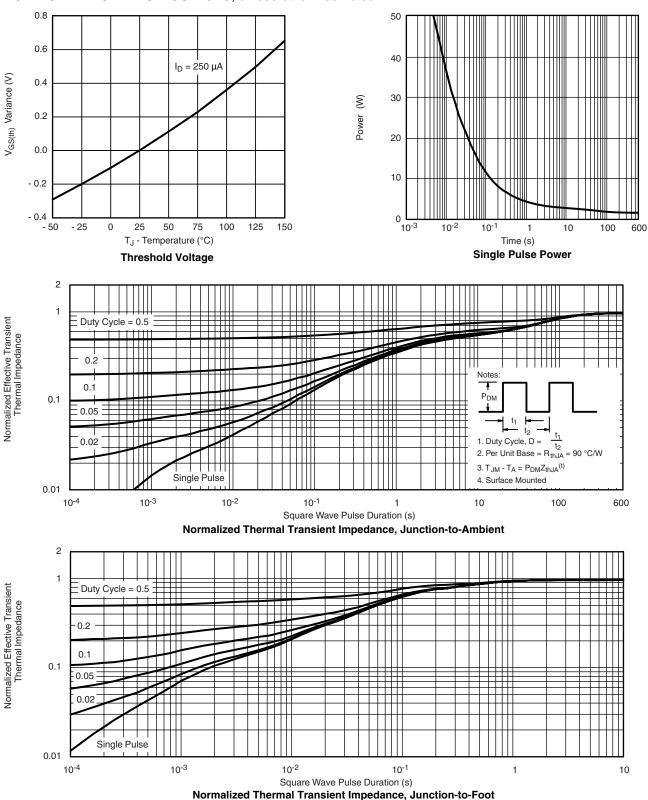
On-Resistance vs. Gate-to-Source Voltage

Is - Source Current (A)

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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