



EVAL-21479-EZLITE

SHARC 21479 EZ-KIT LITE FOR THE ADSP-2147X
SHARC FAMILY

Overview

System Requirements

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Software

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Buy



**Evaluation
Kit Manuals**
Rev.1.1

View All



**Board
Design
Database**
Rev.0.1

View All

Features

- [ADSP-21479](#): 196-pin BGA package, 16.625 MHz CLKIN oscillator
- Flash Memory: 4 M x 8-bit, Numonyx
- SDRAM Memory: 16Mb x16 bit, Micron MT48LC16M16A2P
- Asynch SRAM: 1M x16 bit, ISSI IS61WV102416BLL-10TLI
- SPI Memory: 16 Mbit
- Analog Audio: [AD1939](#) Codec
- SPDIF: RCA phone jacks
- UART: [ADM3202](#) RS-232 line driver/receiver
- Power Supply: 5V @ 3.6 Amps
- LEDs: 10 LEDs: 1 power (green), 1 board reset (red), and 8 general purpose (amber)
- Push Buttons: (5) push buttons: (1) reset, (2) connected to DAI, (2). IRQ/Flag
- SHARC Expansion Interface II: AMI. FlaaS/IRQs. DAI, DPI,

Applicable Parts

- [ADSP-21479](#)
- [ADSP-21478](#)
- [ADSP-21477](#)

Package Contents

EZ-Board Package Contents

- ADSP-21479 EZ-Board
- Universal 5.0V DC power supply
- 3.5 mm stereo headphones
- 6-foot RCA audio cable
- 6-foot 3.5 mm/RCA x 2 Y-cable
- 3.5 mm stereo female to RCA male Y-cable

EZ-KIT Package Contents

- ADSP-21479 EZ-Board
- [Standalone debug agent \(SADA2\)](#)
- USB cable
- Universal 5.0V DC power supply
- 3.5 mm stereo headphones
- 6-foot RCA audio cable
- 6-foot 3.5 mm/RCA x 2 Y-cable
- 3.5 mm stereo female to RCA male Y-cable

PWR_IN, 3.3V, GND

- Other Features: Standalone Debug Agent Interface, RTC (Real Time Clock) implementation, Shift Register Interface, MP JTAG IN and OUT Connectors, 0-ohm resistors for DSP current measurement, CE compliant

Product Details

The ADSP-21479 EZ-KIT Lite® provides developers with a cost-effective method for initial evaluation of the ADSP-21478/21479 SHARCS® Processors via a USB-based, PC-hosted tool set. With this EZ-KIT Lite, users can learn more about the Analog Devices (ADI) ADSP-21479 hardware and software development, and quickly prototype a wide range of applications.

The EZ-KIT Lite includes an ADSP-21479 SHARC Processor desktop evaluation board along with an evaluation suite of the VisualDSP++® development and debugging environment, including the C/C++ compiler, assembler, and linker. The evaluation suite of VisualDSP++ is designed to be used with the EZ-KIT Lite only.

The EZ-KIT Lite also comes with a standalone debug agent board that is removable... [Show More..](#)

System Requirements

- Windows XP Professional SP3 (32-bit only).
- Windows Vista Business/Enterprise/Ultimate SP2 (32-bit only). It is recommended to install the software in a non-UAC-protected location.
- Windows 7 Professional/Enterprise/Ultimate (32 and 64-bit). It is recommended to install the software in a non-UAC-protected location.
- Minimum of 2 GHz single core processor, 3.3 GHz dual core is recommended.
- Minimum of 1 GB memory (RAM), 4 GB is recommended.
- Minimum of 2 GB hard disk (HDD) space is required.
- CrossCore® Embedded Studio for Analog Devices Processors.
- VisualDSP++ 5.0 and Latest Update.

Documentation

3 See All

1 Evaluation Kit Manuals

1 Emulator Manuals

1 Board Design Database

ADSP-21479 EZ-KIT Lite® Evaluation System Manual (Rev. 1.1)

PDF

678 kB

HPUSB, USB, and HPPCI Emulator User's Guide (Rev. 3.2)

PDF

746 kB

ADSP-21479 EZ-Board Design Database (Rev. 0.1)

ZIP

Contains all of the electronic information required for the design, layout, fabrication and assembly of the ADSP-21479 EZ-Board.

9824 kB



7 See All

1 DSP Software

1 Design Tools

1 Software Development Tools

4 Software Modules

21479 EZ-Kit Board Support Package (Rev. 1.0.0)

[Download Software](#)

[Download Release Notes](#)

SHARC Processors Software and Tools

SigmaStudio for SHARC

SigmaStudio™ for SHARC is a programming, development, and tuning software environment that allows an audio designer to graphically design and program audio applications utilizing an extensive set of pre-built audio algorithms.

DTS Boost Decoder, SHARC

The Digital Theatre Systems (DTS) Neo:X Decoder library for the SHARC processor is an audio post-processing module that maximizes the perceived loudness for a stereo source without cause audible distortion.

DTS Enhance Decoder, SHARC

The Digital Theatre Systems (DTS) Enhance Decoder library for the SHARC processor dynamically equalizes stereo audio to give improved brightness at all volume levels.

DTS Surround Decoder, SHARC

The Digital Theatre Systems (DTS) Surround Sensation Decoder library for the SHARC processor is an audio post-processing module that delivers an enhanced stereo experience from a multi-channel surround source.

MPEG-4 HE-AAC v2 Decoder, SHARC

The MPEG-4 HE-AAC v2 decoder library (with DAB and DRM support) for the SHARC processor implements a combination of Advanced Audio Coding (AAC), Spectral Band Replication (SBR) and Parametric Stereo (PS), standardized as the High-Efficiency v2 profile in MPEG-4 (HE AAC v2).

Related Hardware



Emulator Hardware

EMULATOR-USB & HP USB ICE

USB-Based Emulator and High Performance USB-Based Emulator

Evaluation Boards

Pricing displayed is based on 1-piece.

Model	Description	Price	RoHS
ADZS-21479-EZBRD Production	Evaluation Board		Yes
ADZS-21479-EZLITE Production	Evaluation Board		Yes

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