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EVAL-TPG-ZYNQ3

Xilinx Zynq®-7000 All Programmable SoC ZC706
 Evaluation Kit

Overview

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Features

- Upgraded with -3 speed grade XC7Z045 FFG900 supporting up to 12.5 Gbps JESD204B Lane Rates
- 1 GB (PS) DDR3 Component Memory
- 1 GB (PL) SODIM Memory
- GigE Ethernet
- USB Host
- FMC LPC and HPC Expansion Ports
- 12 V Adapter powers Zynq® Evaluation Kit
 - Provides Power for RadioVerse Transceiver

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RadioVerse: Concept to Creation at Lightspeed

Applicable Parts

- [ADG707](#)
- [ADP123](#)
- [ADV7511](#)

Package Contents

- ZC706 evaluation board upgraded with XC7Z045 FFG900-SOC
- Power supply
- For the user guide with AD9371 and AD9375, please refer to the section "AD9371 Demonstration System Overview" of the AD9371 User Guide UG-992.

Product Details

The Zynq[®]-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B provides a data capture platform for many of the RadioVerse families of wideband transceiver evaluation boards. A single 12 V power supply provides power for both the Zynq[®] and the RadioVerse evaluation boards. The data capture platform interfaces to the RadioVerse evaluation boards through a variety of interfacing standards including JESD207 and JESD204B. The JESD204B interface on the Zynq[®] evaluation system supports up to 12.5 Gbps lane rates. A variety of interface options allows the evaluation kit to interface directly to a PC monitor, keyboard, and mouse as well to a PC running the Transceiver Evaluation or Prototyping... [Show More..](#)

Documentation

2 See All

2 User Guides

SDR Integrated Transceiver Design Resources

This site contains the device documentation packages for the SDR Integrated Transceivers including user guides, IBIS models, and PCB files.

Zynq-7000 All Programmable SoC ZC706 Evaluation Kit (ISE Design Suite 14.5) Getting Started Guide

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Evaluation Boards

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Model	Description	Price	RoHS
EVAL-TPG-ZYNQ3 Contact ADI	Evaluation Board		Yes

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