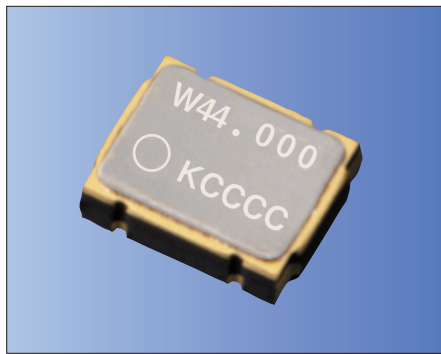




CMOS/ 3.3V/ 3.2x2.5mm



RoHS Compliant

**Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage Vcc=3.3V
- ±25×10<sup>-6</sup> available

**Table 1**

Freq. Tol. Code	× 10 <sup>-6</sup>	Operating Temperature Range (°C)	Note
0	± 50	-10 to +70	Standard specifications
S	± 30		
U	± 25	-40 to +85	Please contact us for available frequencies.
F	± 100		
G	± 50	-40 to +105	
6	± 50		

**How to Order**

**KC3225A 25.0000 C 3 □ E 00**  
①                      ②                      ③ ④ ⑤ ⑥ ⑦

- ① Series
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function (45/ 55%)
- ⑦ Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

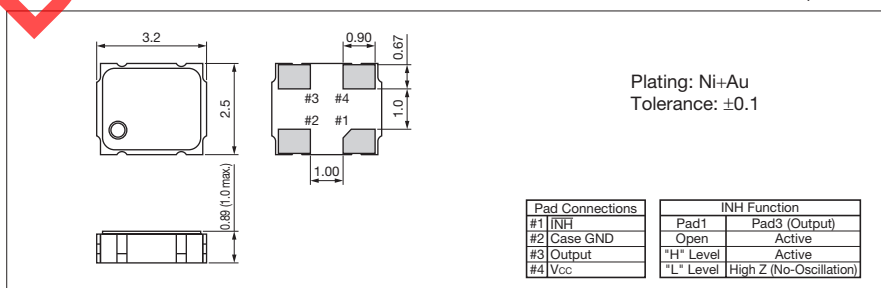
**Specifications**

Item	Symbol	Conditions	Min.	Max.	Unit	
Output Frequency Range	f <sub>o</sub>		1.5	125	MHz	
Frequency Tolerance	f <sub>tol</sub>	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Temp.: -40 to +85°C	-100	+100	× 10 <sup>-6</sup>
			Temp.: -10 to +70°C / -40 to +85°C / -40 to +105°C	-50	+50	
			Temp.: -10 to +70°C	-30	+30	
Storage Temperature Range	T <sub>stg</sub>		-55	+125	°C	
Operating Temperature Range	T <sub>use</sub>	Standard Specifications	-10	+70	°C	
		Extend (Option)	-40	+85		
Max. Supply Voltage	—		-40	+105		
Supply Voltage	V <sub>cc</sub>	Freq. Tol. Code: 0, S, F	+2.97	+3.63	V	
		Freq. Tol. Code: U, G, 6	+3.14	+3.46	V	
Current Consumption (Maximum Loaded)	I <sub>cc</sub>	1.5 ≤ f <sub>o</sub> ≤ 26MHz	—	6	mA	
		26 < f <sub>o</sub> ≤ 50MHz	—	8		
		50 < f <sub>o</sub> ≤ 67.5MHz	—	12		
		67.5 < f <sub>o</sub> ≤ 95MHz	—	20		
		95 < f <sub>o</sub> ≤ 125MHz	—	25		
Stand-by Current	I <sub>std</sub>		—	10	μA	
Symmetry	SYM	@ 50% V <sub>cc</sub>	45	55	%	
Rise/ Fall Time (10% V <sub>cc</sub> to 90% V <sub>cc</sub> Maximum Loaded)	Tr/ Tf	1.5 ≤ f <sub>o</sub> ≤ 67.5MHz	—	5	ns	
		67.5 < f <sub>o</sub> ≤ 125MHz	—	3		
Low Level Output Voltage	V <sub>OL</sub>	I <sub>OL</sub> = 4mA	—	10% V <sub>cc</sub>	V	
High Level Output Voltage	V <sub>OH</sub>	I <sub>OH</sub> = 4mA	90% V <sub>cc</sub>	—	V	
CMOS Load	L <sub>CMOS</sub>	CMOS Output	—	15	pF	
Input Voltage Range	V <sub>IN</sub>		0	V <sub>cc</sub>	V	
Low Level Input Voltage	V <sub>L</sub>		—	30% V <sub>cc</sub>	V	
High Level Input Voltage	V <sub>H</sub>		70% V <sub>cc</sub>	—	V	
Disable Time	t <sub>dis</sub>		—	150	ns	
Enable Time	t <sub>ena</sub>		—	5	ms	
Start-up Time	t <sub>str</sub>	@ Minimum operating voltage to be 0 sec.	—	10	ms	
1 Sigma Jitter	J <sub>Sigma</sub>	Measured with Wavcrest SIA-3000	1.5 ≤ f <sub>o</sub> ≤ 60MHz	—	8	ps
			60 < f <sub>o</sub> ≤ 125MHz	—	5	
Peak to Peak Jitter	J <sub>PK-PK</sub>	Measured with Wavcrest SIA-3000	1.5 ≤ f <sub>o</sub> ≤ 60MHz	—	80	ps
			60 < f <sub>o</sub> ≤ 125MHz	—	40	

Note: All electrical characteristics are defined at the maximum load and operating temperature range. Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

**Dimensions**

(Unit: mm)



**Recommended Land Pattern**

(Unit: mm)

