This version (30 May 2019 18:29) was ***approved*** by Brandon.
The Previously approved version (23 May 2019 07:00) is available.

AD-FMCOMMS11-EBZ User Guide

Table of Contents

The AD-FMComms11-EBZ board is a system platform board for communication infrastructure applications that demonstrates the Direct to RF (DRF) transmitter and observation receiver architecture. Using high sample rate RFDAC(s) and RFADC(s), a number of components in previous generation transmitters can be eliminated, such as mixers, modulators, IF amplifiers and filters. The objective being to bring the ADC or DAC as close to the antenna as possible, leading to possibly more cost effective and efficient communications solution. It is composed of multi-GSps RF ADC AD9625 and DAC AD9162, Cheetah and Barium respectively. The transmit path contains a balun, low pass filter, gain block and variable attenuation to produce an output appropriate for a power amplifier module. Along the observation path, the PA output is coupled back into the board through a variable attenuator, a balun and finally the ADC. Clock management is taken care of on board; all the necessary clocks are generated from a reference. Power management is present as well. We will provide typical performance data for the entire range (70 MHz – 6 GHz) which is supported by the platform. This is primarily for system investigation and bring up of various waveforms from a software team before their custom hardware is complete, where they want to see waveforms, but are not concerned about the last 1dB or 1% EVM of performance.

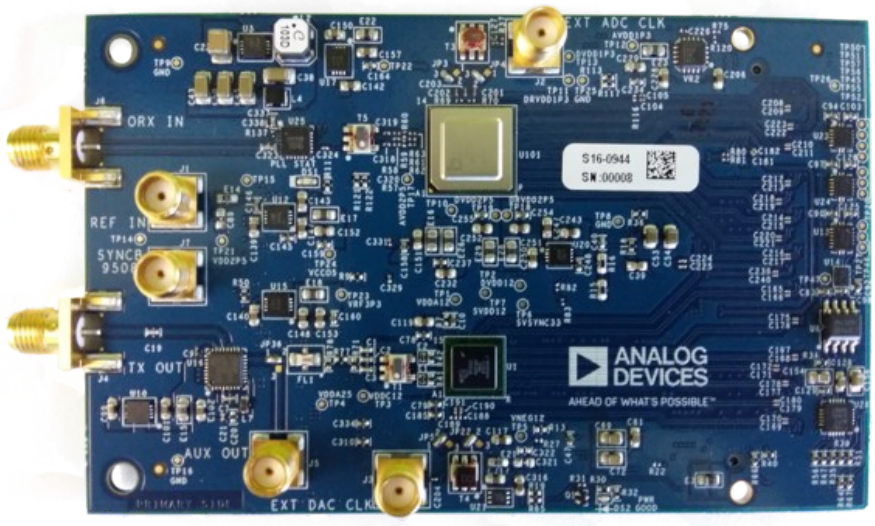

Table of Contents

1. What you need to get started
2. Quick-Start Guides
    1. Linux on ZC706
    2. Configure a pre-existing SD-Card
    3. Update the old card you received with your hardware
3. Linux Applications
    1. IIO Scope
        1. FMCOMMS11 Control in the IIO Scope Plugin
4. HDL reference design

4. Design with the FMCOMMS11's AD9625 and AD9162
    1. Understanding the FMCOMMS11
        1. AD9162 Product page
        2. AD9625 Product page
5. Help and Support

ADI Articles

- Four Quick Steps to Production: Using Model-Based Design for Software-Defined Radio
    - Part 1—the Analog Devices/Xilinx SDR Rapid Prototyping Platform: Its Capabilities, Benefits, and Tools
    - Part 2—Mode S Detection and Decoding Using MATLAB and Simulink
    - Part 3—Mode S Signals Decoding Algorithm Validation Using Hardware in the Loop
    - Part 4 - Rapid Prototyping Using the Zynq SDR Kit and Simulink Code Generation Workflow

ADI Videos

- Silent Switcher µModule Regulators Powering GSPS Sampling ADC

MathWorks Webinars

- Modelling and Simulating Analog Devices' RF Transceivers with MATLAB and SimRF
- Getting Started with Software-Defined Radio using MATLAB and Simulink

Warning

All the products described on this page include ESD (electrostatic discharge) sensitive devices. Electrostatic charges as high as *4000V* readily accumulate on the human body or test equipment and can discharge without detection.

Although the boards feature ESD protection circuitry, permanent damage may occur on devices subjected to high-energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality. This includes removing static charge on external equipment, cables, or antennas before connecting to the device.