

S29GL128S13FAEV13

Status: In Production

Datasheet



English

File Type: PDF

Updated: Apr 29, 2018

Product Specs

Packaging/Ordering

Quality and RoHS

Technical Documents

S29GL128S13FAEV13

Automotive Qualified	N
Density (Mbit)	128
Initial Access Time (ns)	0
Interface	Parallel
Interface Frequency (SDR / DDR) (MHz)	NA
Max. Operating Voltage (V)	3.60
Min. Operating Voltage (V)	2.70
Operating Voltage (V)	3
Part Family	Parallel NOR
Series	GL-S
Tape & Reel	Y
Temperature Range (Min C to Max C)	-55C to +125C

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Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors
Out of Stock	0	Please click here to check lead times	Contact Sales	Contact Sales

* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

PACKAGING/ORDERING

No. of Pins	64
Package Cross Section Drawing	Download
Package Carrier	REEL
Package Carrier Drawing / Orientation	Package Orientation
Standard Pack Quantity	1
Minimum Order Quantity (MOQ)	1600
Order Increment	1600
Estimated Lead Time (days)	91
HTS Code	8542.32.0051
ECCN Suball	3A001.A.2.A

QUALITY AND ROHS

Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)
RoHS Compliant	N Print RoHS Certificate of Compliance
PB Free	N
Lead/Ball Finish	N/A
Marking	Cypress Marking Format

Device Qualification Reports

Downloaded from [Arrow.com](#), and then up loaded into the Device Qualification Report.

TRAINING

- [Workshops](#)
- [Training On-Demand](#)
- [Webinars](#)

TECHNICAL DOCUMENTS

Application Notes (27)

AN200621 - Cypress GL-S and GL-T MirrorBit(R) Flash Nonvolatile Memory Families - Automatic ECC May 07, 2018

AN98498 - S29GL-N to S29GL-P Migration May 07, 2018

AN98500 - S29GL-S Page Read Mode - Reducing System Startup Time May 07, 2018

AN200973 - Interfacing Cypress Flash to TI OMAP Processors Apr 26, 2018

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CUSTOMER SERVICE SUPPORT

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Hours:
8:00AM - 5:00PM (local time)
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INTERNATIONAL SUPPORT

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