# LT8392 <br> <br> 3V Minimum $\mathrm{V}_{\mathbb{I N}}, 12 \mathrm{~V}_{\text {OUT }}$ High Power Synchronous <br> <br> 3V Minimum $\mathrm{V}_{\mathbb{I N}}, 12 \mathrm{~V}_{\text {OUT }}$ High Power Synchronous 4-Switch Buck-Boost Regulator 

 4-Switch Buck-Boost Regulator}

## DESCRIPTIOn

Demonstration circuit 2626A is a 4 -switch synchronous buck-boost regulator that demonstrates the high power capability of the $\mathrm{LT}{ }^{\circledR} 8392$. The output is 12 V and the maximum output current is 12 A for up to 144 W power delivery. The switching frequency is 200 kHz and efficiency can exceed 97\%.

The steady-state operating input voltage range of DC2626A in which the temperature of the components is less than $90^{\circ} \mathrm{C}$ is from 9 V to 18 V . The transient operating input voltage range of DC2626A is from 3 V to 36 V . The output voltage and EN/UVLO are all programmed by resistor dividers. EN/UVLO is set so the circuit will turn off when the input voltage falls below 3 V and will turn on when the input voltage rises above 4V. The PCB has large copper planes and extensive vias for excellent high power thermal performance.

DC2626A features MOSFETs that complement the 5 V gate drive of the LT8392 to achieve high efficiency. 40 V AEC-Q101 MOSFETs are used on the input and output side of the four-switch topology. Ceramic capacitors are used at both the circuit input and output because of their small size and high ripple current capability. In addition to ceramic capacitors, there are bulk aluminum polymer capacitors on the input and output to make input and output stable during transient period.
The CTRL input is pulled up to the VREF pin through a $0 \Omega$ resistor to set the output current limit to its maximum, and an external voltage on CTRL can be used to lower the current limit if the resistor is removed. A capacitor at the SS pin programs soft-start.

To improve the EMI performance, the LT8392 has spread spectrum frequency modulation. With the SYNC/SPRD pin tied to INTVCC, LT8392 spreads its switching frequency $\pm 15 \%$ around the programmed oscillator frequency.
The $\overline{\mathrm{PGOOD}}$ status flag indicates when output voltage is within $\pm 10 \%$ of the final regulation voltage.

The LT8392's proprietary peak current mode buck-boost architecture ensures DC2626A runs either in discontinuous conduction mode (DCM) or pulse-skipping mode (PSM) without reverse inductor current. Both modes enhance the light load efficiency.

The demo circuit is designed to be easily reconfigured to suit other applications, including the example schematics in the data sheet. Consult the factory for assistance.

High power operation, 3V input voltage operation, 4-switch buck-boost topology, proprietary peak current mode architecture, fault protection and output current monitoring make the LT8392 attractive for high power voltage regulator circuits and also circuits whose input voltage drops to 3 V such as cold crank of car battery. It is also suitable for output current regulation such as battery chargers. The LT8392JFE is available in a thermally enhanced 28 lead TSSOP package. The LT8392 data sheet must be read in conjunction with this demo manual to properly use or modify demo circuit DC2626A.
Design files for this circuit board are available.
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## DEMO MANUAL DC2626A

PGRFORmANCG SUMmARY
Specifications are at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$

| PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage Range ( $\mathrm{V}_{\mathrm{IN}}$ ) | $\mathrm{V}_{\text {OUT }}=12 \mathrm{~V}$ | 3 |  | 36 | V |
| Full Load (12A) Input Voltage Range (V1) | Component Temperature $<90^{\circ} \mathrm{C}$ with no airflow | 9 |  | 18 | V |
| Output Voltage (V $\mathrm{V}_{\text {OUT }}$ ) | R7 $=110 \mathrm{k}, \mathrm{R} 8=10 \mathrm{k}$ | 11.5 | 12.0 | 12.5 | V |
| Output Voltage Ripple | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=10 \mathrm{~A}$ |  | 70 |  | $\mathrm{mV} \mathrm{VP}_{\text {P }}$ |
| Maximum Output Current | $9 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 18 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=12 \mathrm{~V}$ | 12 |  |  |  |
| Switching Frequency | R5 $=226 \mathrm{k}$ |  | 200 |  | kHz |
| Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=12 \mathrm{~A}$, SSFM On |  | 95.5 |  | \% |
| Input EN Voltage | R9 = 121k, R10 $=100 \mathrm{k}$, EXTV $_{\text {CC }}=\mathrm{V}_{\text {OUT }}, \mathrm{I}_{\text {OUT }}=6 \mathrm{~A}$ |  | 4 |  | V |
| Input UVLO Voltage | R9 = 121k, R10 $=100 \mathrm{k}, \mathrm{EXTV}_{\text {CC }}=\mathrm{V}_{\text {OUT }}, \mathrm{I}_{\text {OUT }}=6 \mathrm{~A}$ |  | 3 |  | V |
| Output Current Limit (IOUT) | $\mathrm{R} 3=3 \mathrm{~m} \Omega$ |  | 16.6 |  | A |
| Peak Switch Current Limit | $\mathrm{R} 1=1 \mathrm{~m} \Omega$ | 35 | 50 | 65 | A |
| $V_{\text {ISMON }}$ | $\mathrm{V}_{\text {OUT }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=12 \mathrm{~A}$ |  | 0.97 |  | V |

## PUICK START PROCEDURE

The DC2626A is easy to set up to evaluate the performance of the LT8392JFE. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below.

NOTE: Make sure that the voltage applied to VIN does not exceed 40 V , which is the voltage rating for input side MOSFETs.

1. Set JP1 at NO SSFM/SYNC to disable SSFM, at SSFM ON to enable SSFM, or at EXT SYNC and connect an external oscillator to EXT SYNC.
2. Connect the EN/UVLO terminal to ground with a clip-on lead. Connect the power supply (with power off), load, and meters as shown.
3. After all connections are made, turn on the input power and verify that the input voltage is between 9 V and 18 V .
4. Remove the clip-on lead from EN/UVLO. Verify that the output voltage is 12 V .
NOTE: If the output voltage is low, temporarily disconnect the load to make sure that it is not set too high.
5. Once the proper output voltage is established, adjust the input voltage and load within the operating ranges and observe the output voltage regulation, ripple voltage, efficiency and other parameters.

## DEMO MANUAL DC2626A

## PUICK START PROCEDURE



Figure 1. Test Procedure Setup Drawing for DC2626A

## TEST RESULTS



Figure 2. Efficiency vs $\mathrm{V}_{\mathrm{IN}}$ at Full Load (12A), SSFM On

## DEMO MANUAL DC2626A

## PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :---: | :---: | :---: |
| Required Circuit Components |  |  |  |  |
| 1 | 1 | C1 | CAP., 14F, X7S, 100V, 10\%, 0805 | MURATA, GCM21BC72A105KE36L |
| 2 | 1 | C2 | CAP, , 4.74F, X7S, 10V, 10\%, 0805 | MURATA, GCM21BCA475KA73L |
| 3 | 1 | C3 |  | MURATA, GCM188R71C474KA55D |
| 4 | 1 | C4 | CAP, $0.015 \mu \mathrm{~F}, \mathrm{X} 7 \mathrm{R}, 25 \mathrm{~V}, 10 \%, 0603$ | MURATA, GCJ188R71E153KA01 |
| 5 | 3 | C5, C7, C8 | CAP., 0.1 ¢F, X7R, $16 \mathrm{~V}, 10 \%, 0603$ | MURATA, GCJ188R71E104KA12 |
| 6 | 2 | C6, C25 | CAP, 1uF, X7R, 25V, 10\%, 0603 | MURATA, GCM188R71E105KA64D |
| 7 | 4 | C11, C12, C31, C32 | CAP., 10uF, X7S, 50V, 10\%, 1210 | MURATA, GCM32EC71H106KA03L |
| 8 | 4 | C13, C14, C33, C34 | CAP., 22 $2 \mathrm{~F}, \mathrm{X} 7 \mathrm{R}, 16 \mathrm{~V}, 20 \%, 1210$ | MURATA, GCM32ER71C226ME19L |
| 9 | 2 | C18, C20 | CAP, ALUM, 120 FF, $50 \mathrm{~V} 20 \% 10 \times 10.5 \mathrm{~mm}$ | PANASONIC, EEHZC1H121P |
| 10 | 2 | C21, 223 | CAP, ALUM, 470uF, 16V, 20\%, 10x10.3mm | CHEMI-CON, HHXA160ARA471MJAOG |
| 11 | 4 | C33, C34, C50, C51 | CAP., 10uF, X7R, 16V, 10\%, 1206 | MURATA, GCM31CR71C106KA64 |
| 12 | 2 | C37, C38 | CAP., 10uF, X7S, 50V, 10\%, 1210 | MURATA, GCM32EC71H106KA03L |
| 13 | 2 | C41, C53 | CAP, 4.7 7 F, X7R, 16V, $10 \%, 0805$ | MURATA, GCM21BR71C475KA73L |
| 14 | 1 | L1 | IND., 4.7 ${ }^{\text {H XAL } 1510}$ | COILCRAFT, XAL 1510-472MEB |
| 15 | 1 | L2 | IND., 4.7 $\mu \mathrm{H}, \mathrm{XAL} 1010$ | COILCRAFT, XAL 1010-472MEB |
| 16 | 4 | M1, M2, M3, M4 | XSTR., MOSFET, N-CH, 40V, TDSON-8 | INFINEON, IPC100N04S5L-1R1 |
| 17 | 2 | R1 | SENSE RES., 0.001 $\Omega$, 3W 2\% 1225 | SUSUMU, KRL6432E-M-R001-G-T1 |
| 18 | 2 | R3 | RES., SENSE, $0.003 \Omega 3 \mathrm{~W} 1 \% 1225$ | SUSUMU, KRL6432E-C-R003-F-T1 |
| 19 | 1 | R5 | RES., 226k, 1/10W, 1\%, 0603 | VISHAY, CRCW0603226KFKEA |
| 20 | 1 | R6 | RES., 15k, 1/10W, 1\%, 0603 | VISHAY, CRCW060315KOFKEA |
| 21 | 1 | R7 | RES., 110k, 1/10W, 1\%, 0603 | VISHAY, CRCW0603110KFKEA |
| 22 | 1 | R8 | RES., 10k, 1/10W, 1\%, 0603 | VISHAY, CRCW060310KOFKEA |
| 23 | 1 | R9 | RES., 121k, 1/10W, 1\%, 0603 | VISHAY, CRCW0603121KFKEA |
| 24 | 2 | R10, R11 | RES., 100k, 1/10W, 1\%, 0603 | VISHAY, CRCW0603100KFKEA |
| 25 | 2 | R12, R13 | RES., 102, 1/10W, 5\%, 0603 | VISHAY, CRCW060310ROJNEA |
| 26 | 6 | R14, R15, R16, R17, R18, R19 | RES., 0 2 , 1/10W, 0603 | VISHAY, CRCW06030000ZOEA |
| 27 | 4 | R14, R15, R16, R17 | RES., 3 3, 1/10W, 1\%, 0603 | VISHAY, CRCW06033R00FKEA |
| 28 | 1 | U1 | I.C., VOLTAGE REGULATOR, 28-TSSOP | ANALOG DEVICES, INC., LT8392JFE\#PBF |

Additional Demo Board Circuit Components

| 29 | 0 | C26, C27, C28 | CAP., OPTION, 0603 |  |
| :---: | :--- | :--- | :--- | :--- |
| 30 | 0 | C9, C10, C15, C16, C39, C40, C45, <br> C46, C47, C48, C50, C51 | CAP., OPTION, 1210 |  |
| 31 | 0 | C42, C44 | CAP., OPTION, ALUM., $10 \times 10.5 \mathrm{~mm}$ |  |
| 32 | 0 | C49, C52 | CAP., OPTION, ALUM, $10 \times 10.3 \mathrm{~mm}$ |  |
| 33 | 0 | R21, R22, R23, R24, R25, R26 | RES., OPTION, 0603 |  |
| 34 | 0 | R27 | RES., OPTION, 1225 |  |

## Hardware: For Demo Board Only

| 35 | 11 | E1-E11 | TESTPOINT, TURRET, 0.094" pbf | MILL-MAX, 2501-2-00-80-00-00-07-0 |
| ---: | :---: | :--- | :--- | :--- |
| 36 | 4 | J1, J2, J3, J4 | CONN., JACK, BANANA, 0.218" | KEYSTONE, 575-4 |
| 37 | 1 | JP1 | CONN., HEADER, 2X3, 2mm | WURTH ELEKTRONIK, 62000621121 |
| 38 | 1 | XJP1 | SHUNT, 2mm | WURTH ELEKTRONIK, 60800213421 |
| 39 | 4 | MH1-MH4 | STAND-OFF, NYLON 0.375" | WURTH ELEKTRONIK, 702933000 |

## SCHEMATIC DIAGRAM



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