|  |  |          |   |        |        |            |                       | F                      | REVISI                 | ONS                     |                         |             |                |              |               |             |       |        |      |       |
|--|--|----------|---|--------|--------|------------|-----------------------|------------------------|------------------------|-------------------------|-------------------------|-------------|----------------|--------------|---------------|-------------|-------|--------|------|-------|
| LTR  | DESCRIPTION  |          |   |        |        |            |                       | DA                     | TE (Y                  | R-MO-                   | ·DA)                    | APPROVED    |                | D            |               |             |       |        |      |       |
| А  | Update drawing boilerplate to most current format. |          |   |        |        |            |                       | 02-03-29               |                        |                         | Raymond Monnin          |             | nnin           |              |               |             |       |        |      |       |
| В  | Tab  | le II, a | idd not   | e to G | roup ( | C end-     | point t               | est pa                 | ramete                 | ers                     | gz                      |             |                | 09-0         | 06-16         |             | Jose  | eph D. | Rode | nbeck |
|  |  |          |   |        |        |            |                       |                        |                        |                         |                         |             |                |              |               |             |       |        |      |       |
|  |  |          | 1   |        |        |            |                       |                        |                        |                         |                         |             |                |              |               | ,.<br>T     |       |        |      | 1     |
|  |  |          |   |        |        | +          |                       |                        |                        |                         |                         |             |                |              |               |             |       |        |      |       |
| REV  |  |          |   |        |        | +          |                       |                        |                        |                         |                         |             |                |              |               |             |       |        |      |       |
| SHEET  |  |          |   |        |        |            |                       |                        |                        |                         |                         |             |                |              |               |             |       |        |      |       |
| REV STATUS   | 5<br>5   |          |   | RE     | V      |            | В                     | В                      | В                      | В                       | В                       | В           | В              | В            | В             | В           |       |        |      |       |
| OF SHEETS  |  |          |   | SHE    | EET    |            | 1                     | 2                      | 3                      | 4                       | 5                       | 6           | 7              | 8            | 9             | 10          |       |        |      |       |
| PMIC N/A     PREPARED BY<br>Donald R. Osborne       STANDARD<br>MICROCIRCUIT<br>DRAWING     CHECKED BY<br>Ray Monnin |  |          |   | -      | DI     | EFEN<br>CC | SE SI<br>DLUN<br>http | UPPL<br>IBUS<br>p://ww | Y CE<br>, OHI<br>ww.ds | NTER<br>O 432<br>scc.dl | R COL<br>218-3<br>a.mil | -UMB<br>990 | US             | 1            |               |             |       |        |      |       |
| APP<br>THIS DRAWING IS AVAILABLE<br>FOR USE BY ALL<br>DEPARTMENTS<br>AND AGENCIES OF THE<br>DEPARTMENT OF DEFENSE    |  |          | APPROVED BY<br>Michael A. Frye<br>DRAWING APPROVAL DATE<br>88-11-30 |        |        |            | MI(<br>SE             | CRO<br>TTLI            | CIRC<br>NG \           | UIT<br>/IDE             | , HY<br>O O             | BRID<br>PER | ), LIN<br>ATIC | IEAF<br>DNAL | R, FA<br>_ AM | .ST<br>PLIF | IER   |        |      |       |
| AM   | SC N/A   | ۱.       |   | REV    | ISION  | 1 LEVE     | EL<br>B               |                        |                        | SI                      | ZE<br>A                 | CA          | GE CO<br>67268 | DDE<br>B     |               | !           | 5962- | -8857  | '9   |       |
|  |  |          |   |        |        |            |                       |                        |                        | SHE                     | ET                      |             | 1              | OF           | 10            |             |       |        |      |       |

# 1. SCOPE

1.1 <u>Scope</u>. This drawing describes device requirements for class H hybrid microcircuits to be processed in accordance with MIL-PRF-38534 and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN).

1.2 <u>PIN</u>. The PIN shall be as shown in the following example:



# 2. APPLICABLE DOCUMENTS

2.1 <u>Government specification, standards, and handbooks</u>. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

#### DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

#### DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits. MIL-STD-1835 - Interface Standard for Electronic Component Case Outlines.

# DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at http://assist.daps.dla.mil/quicksearch/ or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

# 3. REQUIREMENTS

3.1 <u>Item requirements</u>. The individual item performance requirements for device class H shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 <u>Case outline</u>. The case outline shall be in accordance with 1.2.2 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

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3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 <u>Marking of device(s)</u>. Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 <u>Data</u>. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DSCC-VA) upon request.

3.7 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DSCC-VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

#### 4. VERIFICATION

4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 <u>Screening</u>. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
  - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
  - (2)  $T_A = +125^{\circ}C$ , minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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|  | Т                                | ABLE I. Electrical per   | formance cł | haracteristics.      |                |        |                    |      |
|--|----------------------------------|--|-------------|----------------------|----------------|--------|--------------------|------|
| Test   | Symbol                           | Symbol Conditions $-55^{\circ}C \leq T_{A} \leq +12t$ $V_{S} = \pm 15.0 \text{ V}$ |             | Group A<br>subgroups | Device<br>type | Limits |                    | Unit |
|  |                                  | unless otherwise sp  | ecified     |                      |                | Min    | Max                |      |
| Input offset voltage                               | V <sub>IO</sub>                  | $R_S = 100 \text{ k}\Omega, T_A = +2$  | 25°C        | 1                    | 01             |        | ±15.0              | mV   |
|  |                                  |  |             |                      | 02             |        | ±1.5<br><u>1</u> / |      |
|  |                                  | R <sub>S</sub> = 100 kΩ,   |             | 2,3                  | 01             |        | ±18.5              | mV   |
|  |                                  | $T_A = -55^{\circ}C \text{ to } +125^{\circ}C$                                     | С           |                      | 02             |        | ±5.0               |      |
| Temperature coefficient of<br>input offset voltage | $\frac{\Delta V_{IO}}{\Delta T}$ | T <sub>A</sub> = -55°C to +125°C   |             | 2,3                  | All            |        | ±35                | μV   |
| Input offset current                               | I <sub>IO</sub>                  | $T_A = +25^{\circ}C$   |             | 1                    | All            |        | ±0.1               | nA   |
|  |                                  | T <sub>A</sub> = -55°C to +125°  | С           | 2,3                  |                |        | ±25                |      |
| Input bias current $I_{IB}$ $T_A = +25^{\circ}C$   |                                  |  | 1           | All                  |                | ±2.0   | nA                 |      |
|  |                                  | T <sub>A</sub> = -55°C to +125°  | с           | 2,3                  |                |        | ±50                |      |
| Supply current                                     | lcc                              | $T_A = +25^{\circ}C$   |             | 1                    | All            |        | 25                 | mA   |
|  |                                  | T <sub>A</sub> = -55°C to +125°  | с           | 2,3                  |                |        | 35                 |      |
| Supply voltage rejection ratio                     | -SVRR                            | V- = -12 V, (V+ = +1<br>V- = -18 V   | 5 V)        | 4,5,6                | All            | 50     |                    | dB   |
|  | +SVRR                            | V+ = +12 V, (V- = -1<br>V+ = +18 V   | 5 V)        | 4,5,6                | All            | 50     |                    | dB   |
| Large signal voltage gain                          | AVS(±)                           | $V_{OUT} = \pm 10 \text{ V}, \text{ R}_L = 2$                                      | 200Ω        | 4,5,6                | All            | 80     |                    | dB   |
| Input voltage common mode                          | CMRR                             | V <sub>IN</sub> = 10 V, T <sub>A</sub> = +25                                       | 5°C         | 4                    | All            | 60     |                    | dB   |
|  |                                  | V <sub>IN</sub> = 10 V, T <sub>A</sub> = -55°<br>+125°C                            | °C to       | 5,6                  |                | 55     |                    |      |
| See footnotes at end of table.                     |                                  |  |             |                      |                |        |                    |      |
|  |                                  |  |             |                      |                |        |                    |      |
|  |                                  |  |             |                      |                |        |                    |      |
| STAN<br>MICROCIRCU                                 | DARD<br>JIT DRAV                 | VING   | SIZE<br>A   | SIZE 4               |                | 5962-  | 88579              |      |
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В

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| Test                                 | Cumbol           | Canditiona  |           | Davias |     |      | Linit |
|--------------------------------------|------------------|---|-----------|--------|-----|------|-------|
| Test                                 | Sympol           | $-55^{\circ}C \leq T_{A} \leq +125^{\circ}C$  | subgroups | type   |     | nits | Unit  |
|                                      |                  | $V_S = \pm 10.0 v$<br>unless otherwise specified  |           |        | Min | Max  | ]     |
| Maximum output voltage<br>swing      | V <sub>OP</sub>  | R <sub>L</sub> = 200Ω   | 1,2,3     | All    | ±10 |      | V     |
| Voltage gain                         | AV               | R <sub>L</sub> = 200Ω, f = 1.0 kHz  | 4,5,6     | All    | 80  |      | dB    |
| Slew rate                            | SR               | $R_L = 200\Omega, A_V = 2 \text{ or } -1,$<br>$T_A = +25^{\circ}C$                          | 4         | All    | 230 |      | V/µs  |
|                                      |                  | $R_L = 200\Omega, A_V = -1$<br>$T_A = -55^{\circ}C \text{ to } +125^{\circ}C$               | 5,6       | All    | 210 |      | V/µs  |
| Settling time to 0.1% of final value | t <sub>SET</sub> | $\begin{array}{l} A_{V}=\text{-1, }R_{L}=200\Omega\\ T_{A}=\text{+}25^{\circ}C \end{array}$ | 9         | All    |     | 100  | ns    |
|                                      |                  | $A_V = -1, R_L = 200\Omega$<br>$T_A = -55^{\circ}C$ to +125°C                               | 10,11     | All    |     | 120  | ns    |
|                                      |                  |   |           |        |     |      |       |

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Case outline X.



| Symbol         | Inc       | hes  | Millim | Millimeters |     |  |
|----------------|-----------|------|--------|-------------|-----|--|
|                | Min       | Max  | Min    | Min         |     |  |
| А              | .148      | .181 | 3.76   | 4.60        |     |  |
| Øb             | .016      | .019 | .41    | .48         | 1,5 |  |
| Øb₁            | .016      | .021 | .41    | .53         | 1,5 |  |
| ØD             | .595      | .605 | 15.11  | 15.37       |     |  |
| ØD1            | .545      | .555 | 13.84  | 14.10       |     |  |
| е              | .400 BSC  |      | 10.16  | 3           |     |  |
| e1             | .200 BSC  |      | 5.08   | 3           |     |  |
| e <sub>2</sub> | .100      | BSC  | 2.54   | 3           |     |  |
| F              | .022      | .030 | .56    | .76         |     |  |
| k              | .026      | .036 | .66    | .91         |     |  |
| <b>k</b> 1     | .026      | .036 | .66    | .91         | 2   |  |
| L              | .500 .560 |      | 12.70  | 14.22       | 1   |  |
| L <sub>1</sub> |           | .050 |        | 1.27        | 1   |  |
| $L_2$          | .250      |      | 6.35   |             | 1   |  |
| α              | 45°       | BSC  | 45°    | 3           |     |  |

# NOTES:

- 1. All leads  $\varnothing$ b applies between L<sub>1</sub> and L<sub>2</sub>.  $\varnothing$ b<sub>1</sub> applies between L<sub>2</sub> and 0.500 (12.70 mm) from the reference plane. Diameter is uncontrolled in L<sub>1</sub> and beyond 0.500 (12.70 mm) from the reference plane.
- 2. Measured from the maximum diameter of the product.
- Leads having a maximum diameter 0.019 (0.48 mm) measured in gauging plane 0.054 (1.37 mm) + 0.001 (0.03 mm)
   0.000 (0.00 mm) below the base plane of the product shall be within 0.007 (0.18 mm) of their true position relative to a maximum tab width.
- 4. The product may be measured by direct methods or by gauge.
- 5. All leads: Increase maximum limit by 0.003 (0.08 mm) when lead finish A is applied.

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| Device types    | All                 |
|-----------------|---------------------|
| Case outline    | Х                   |
| Terminal number | Terminal symbol     |
| 1               | V+                  |
| 2               | Ground              |
| 3               | Offset adjust*      |
| 4               | Offset adjust*      |
| 5               | Inverting input     |
| 6               | Non-inverting input |
| 7               | No connection       |
| 8               | Ground              |
| 9               | V-                  |
| 10              | V-                  |
| 11              | Output              |
| 12              | V+                  |

\*These pins are for connecting an optional offset adjust potentiometer. Recommended value is 10 k $\Omega$  with center arm connected to +15 V.

FIGURE 2. Terminal connections.

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| MIL-PRF-38534 test requirements  | Subgroups<br>(in accordance with<br>MIL-PRF-38534, group A<br>test table) |
|--|---|
| Interim electrical parameters  |   |
| Final electrical parameters  | 1*, 2, 3, 4, 5, 6, 9  |
| Group A test requirements  | 1, 2, 3, 4, 5, 6, 9, 10**, 11**   |
| Group C end-point electrical parameters <u>1</u> /                                   | 1,2,3   |
| End-point electrical parameters<br>for radiation hardness assurance<br>(RHA) devices | Not applicable  |

TABLE II. Electrical test requirements.

\* PDA applies to subgroup 1.

\*\* If not tested, shall be guaranteed to the specified limits of table I.

1/ As a minimum, for all Group C testing performed after June 16, 2009, manufacturers shall perform subgroups 1, 2, and 3 from the Group A electrical test table (Table C-Xa of MIL-PRF-38534).

4.3 <u>Conformance and periodic inspections</u>. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

- 4.3.1 <u>Group A inspection (CI)</u>. Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:
  - a. Tests shall be as specified in table II herein.
  - b. Subgroups 7 and 8 shall be omitted.
- 4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.
- 4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:
  - a. End-point electrical parameters shall be as specified in table II herein.
  - b. Steady-state life test, method 1005 of MIL-STD-883.
    - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
    - (2)  $T_A = +125^{\circ}C$ , minimum.
    - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

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4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.

5. PACKAGING

5.1 <u>Packaging requirements</u>. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractorprepared specification or drawing.

6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Supply Center Columbus (DSCC) when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.5 <u>Comments</u>. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 <u>Sources of supply</u>. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DSCC-VA and have agreed to this drawing.

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# STANDARD MICROCIRCUIT DRAWING BULLETIN

#### DATE: 09-06-16

Approved sources of supply for SMD 5962-88579 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This information bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DSCC maintains an online database of all current sources of supply at http://www.dscc.dla.mil/Programs/Smcr/.

| Standard<br>microcircuit drawing<br>PIN <u>1</u> / | Vendor<br>CAGE<br>number | Vendor<br>similar<br>PIN <u>2</u> / |
|--|--------------------------|-------------------------------------|
| 5962-8857901XC                                     | <u>3</u> /               | HOS-050A/883B                       |
| 5962-8857902XC                                     | <u>3</u> /               | HOS-060SH/883B                      |

- 1/ The lead finish shown for each PIN, representing a hermetic package, is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine availability.
- 2/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply. The last known supplier is listed below.

Vendor CAGE <u>number</u> Vendor name and address

34031

Analog Devices 7910 Triad Center Drive Greensboro, NC 27409-9605

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