

REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Drawing updated to reflect current requirements. - gt	02-04-24	R. MONNIN
B	Add radiation hardness assurance requirements. -rrp	02-07-29	R. MONNIN

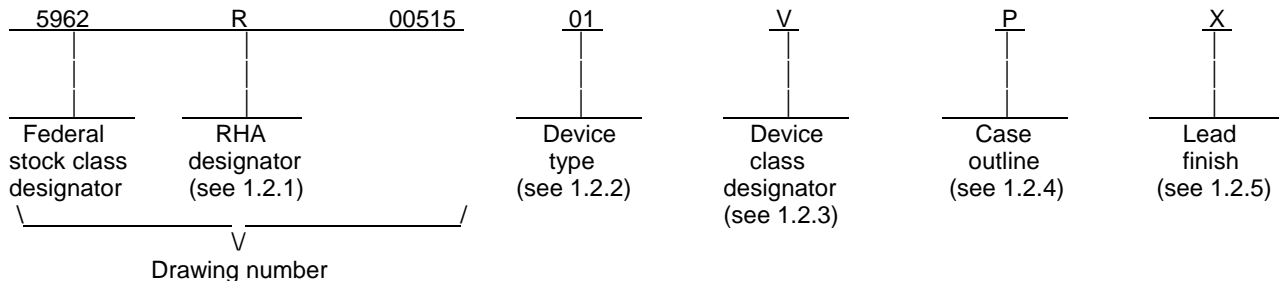


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OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10	11								
PMIC N/A	PREPARED BY Rajesh Pithadia	<p align="center">DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216 http://www.dscc.dla.mil</p> <p align="center">MICROCIRCUIT, LINEAR, SINGLE, ULTRAFAST COMPARATOR, MONOLITHIC SILICON</p>																		
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS</p> <p>AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY Rajesh Pithadia																			
	APPROVED BY Raymond Monnin																			
	DRAWING APPROVAL DATE 00-04-06																			
	REVISION LEVEL B	SIZE A	CAGE CODE 67268	5962-00515																
		SHEET 1 OF 11																		

1. SCOPE

1.1 Scope. This drawing documents two product assurance class levels consisting of high reliability (device classes Q and M) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels are reflected in the PIN.

1.2 PIN. The PIN is as shown in the following example:



1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. Device class M RHA marked devices meet the MIL-PRF-38535, appendix A specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	AD8561A	Ultrafast single supply comparator

1.2.3 Device class designator. The device class designator is a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
M	Vendor self-certification to the requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A
Q or V	Certification and qualification to MIL-PRF-38535

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line
H	GDFP1-F10 or CDFP2-F10	10	Flat pack

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V or MIL-PRF-38535, appendix A for device class M.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 2

1.3 Absolute maximum ratings. 1/

Total Analog Supply Voltage	+14 V
Digital Supply Voltage	+14 V
Analog Positive Supply -Digital Positive Supply	-600 mV
Input Voltage	±7 V
Differential Input Voltage	±8 V
Output Short Circuit Duration to GND	Indefinite
Storage Temperature Range	-65°C to +150°C
Operating Temperature Range	-55°C to +125°C
Junction Temperature Range	-65°C to +150°C
Lead Temperature Range (Soldering, 10 sec.)	+300°C
Power dissipation (P _D)	500 mW
Thermal resistance, junction-to-case (θ _{JC})	See MIL-STD-1835
Thermal resistance, junction-to-ambient (θ _{JA}):	
Case P	148°C/W
Case H	180°C/W

1.4 Recommended operating conditions.

Single supply voltage operation	+3.0 V to +5.0 V
Dual supply voltage operation	±5 V
Ambient operating temperature (T _A)	-55°C to +125°C

1.5 Radiation features:

Maximum total dose available (dose rate = 50 – 300 rads (Si)/s) 100 Krads(Si) 2/

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

HANDBOOKS

DEPARTMENT OF DEFENSE

MIL-HDBK-103 - List of Standard Microcircuit Drawings (SMD's).
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

- 1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- 2/ These parts may be dose rate sensitive in a space environment and may demonstrate enhanced low dose rate effects. Radiation end point limits for the noted parameters are guaranteed only for the conditions specified in MIL-STD-883, method 1019, condition A.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 3

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein. The individual item requirements for device class M shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V or MIL-PRF-38535, appendix A and herein for device class M.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Radiation exposure circuit. The radiation exposure circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing and acquiring activity upon request.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table IIA. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-HDBK-103. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535. Marking for device class M shall be in accordance with MIL-PRF-38535, appendix A.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535. The compliance mark for device class M shall be a "C" as required in MIL-PRF-38535, appendix A.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). For device class M, a certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6.2 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein or for device class M, the requirements of MIL-PRF-38535, appendix A and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 or for device class M in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change for device class M. For device class M, notification to DSCC-VA of change of product (see 6.2 herein) involving devices acquired to this drawing is required for any change as defined in MIL-PRF-38535, appendix A.

3.9 Verification and review for device class M. For device class M, DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

3.10 Microcircuit group assignment for device class M. Device class M devices covered by this drawing shall be in microcircuit group number 50 (see MIL-PRF-38535, appendix A).

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-0015
		REVISION LEVEL B	SHEET 4

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions <u>1/2/</u> -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
V ₊ = +5 V, V ₋ = V _{GND} = 0 V section							
Input Offset Voltage <u>3/</u>	V _{OS}		1	01		7	mV
			2, 3			8	
Input Bias Current <u>3/</u>	I _B	V _{CM} = 0 V	1		-6		μA
			2, 3		-7		
Input offset current <u>3/</u>	I _{OS}	V _{CM} = 0 V	1, 2, 3			+4	μA
Input voltage range <u>3/</u>	I _{VR}	V ₊ = 5 V, V ₋ = 0 V	1		0	+3	V
Common mode rejection <u>3/</u> ratio	CMRR	V _{CM} = I _{VR}	1		65		dB
Logic "1" input voltage <u>3/</u>	V _{IH}		1		2		V
Logic "0" input voltage <u>3/</u>	V _{IL}		1			0.8	V
Logic "1" current <u>3/</u>	I _{IH}	V _{IH} = 3 V	1		-1		μA
			2, 3				
Logic "0" input current <u>3/</u>	I _{IL}	V _{IL} = 0.3 V	1		-4		μA
			2, 3				
Logic "1" output voltage <u>3/</u>	V _{OH}	I _{OH} = -50 μA, ΔV _{IN} > 250 mV,	1		3.5		V
		I _{OH} = -3.2 mA, ΔV _{IN} > 250 mV	1		2.4		
Logic "0" output voltage <u>3/</u>	V _{OL}	I _{OL} = 3.2 mA, ΔV _{IN} > 250 mV	1			0.4	V
Power supply rejection ratio <u>3/</u>	PSRR	V ₊ = 4.5 V to +5.5 V	1, 2, 3		50		dB
Positive Supply Current <u>3/</u>	I ₊		1			6.0	mA
			2, 3			7.5	
Ground Supply Current <u>3/</u>	I _{GND}	V _{OUT} = 0 V, R _L = ∞	1			3.3	mA
			2, 3			3.8	
Analog Supply Current <u>3/</u>	I ₋		1			4.5	mA
			2, 3			5.5	
Propagation delay <u>3/</u>	t _p	200 mV step with 100 mV overdrive	9			9.8	ns

V₊ = +5 V, V₋ = -5 V, V_{GND} = 0 V section

Input Offset Voltage	V _{OS}		1	01		7	mV
			2, 3			8	
			M,D,P,L,R		1		

See footnotes at end of table.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 5

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions <u>1/2/</u> -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
V ₊ = +5 V, V ₋ = -5 V, V _{GND} = 0 V section - Continued							
Input Bias Current	I _B	V _{CM} = 0 V	1	01	-6		μA
			2, 3		-7		
			M,D,P,L,R		-7		
Input offset current	I _{OS}	V _{CM} = 0 V	1, 2, 3		+4	μA	
			M,D,P,L,R	1	+4		
Input voltage range <u>3/</u>	I _{VR}		1		-5.0	+3.0	V
Common mode rejection <u>3/</u> ratio	CMRR	V _{CM} = I _{VR}	1		65		dB
Logic "1" input voltage <u>3/</u>	V _{IH}		1		2.0		V
Logic "0" input voltage <u>3/</u>	V _{IL}		1			0.8	V
Logic "1" current <u>3/</u>	I _{IH}	V _{IH} = 3 V	1		-1	20	μA
Logic "0" input current <u>3/</u>	I _{IL}	V _{IL} = 0.3 V	1		-4	20	μA
Logic "1" output voltage <u>3/</u>	V _{OH}	I _{OH} = -3.2 mA, ΔV _{IN} > 250 mV	1		2.6		V
Logic "0" output voltage <u>3/</u>	V _{OL}	I _{OL} = 3.2 mA, ΔV _{IN} > 250 mV	1			0.3	V
Power supply rejection ratio <u>3/</u>	PSRR	V _S = ±4.5 V to ±5.5 V	1		55		dB
Positive Supply Current	I ₊		1			6.5	mA
			2, 3		7.5		
			M,D,P,L,R	1	7.0		
Ground Supply Current	I _{GND}	V _{OUT} = 0 V, R _L = ∞	1			3.3	mA
			2, 3		3.8		
			M,D,P,L,R	1	3.5		
Analog Supply Current	I ₋		1			4.5	mA
			2, 3		5.5		
			M,D,P,L,R	1	5.0		
Propagation delay <u>3/</u>	t _p	200 mV step with 100 mV overdrive	9			9.8	ns

See footnotes at end of table.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 6

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions <u>1/ 2/</u> -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
V ₊ = 3 V, V ₋ = V _{GND} = 0 V section							
Input Offset Voltage <u>3/</u>	V _{OS}		1	01		7	mV
Input Bias Current <u>3/</u>	I _B	V _{CM} = 0 V	1		-6		μA
			2, 3		-7		
Input Voltage Range <u>3/</u>	I _{VR}		1		0	1.5	V
Common Mode Rejection Ratio <u>3/</u>	CMRR	0.1 V ≤ V _{CM} ≤ 1.5 V	1		60		dB
Logic "1" output voltage <u>3/ 4/</u>	V _{OH}	I _{OH} = -3.2 mA, ΔV _{IN} > 250 mV	1		1.2		V
Logic "0" output voltage <u>3/</u>	V _{OL}	I _{OL} = 3.2 mA, ΔV _{IN} > 250 mV	1			0.3	V
Positive Supply Current <u>3/</u>	I ₊		1			4.5	mA
			2, 3			5.5	
Ground Supply Current <u>3/</u>	I _{GND}	V _{OUT} = 0 V, R _L = ∞	1			2.5	mA
			2, 3		3.0		
Analog Supply Current <u>3/</u>	I ₋		1		3.3	mA	
			2, 3		3.8		

1/ Devices supplied to this drawing have been characterized through all levels M, D, P, L, R of irradiation. However, this device is only tested at the "R" level. Pre and Post irradiation values are identical unless otherwise specified in table I. When performing post irradiation electrical measurement for any RHA level, T_A = +25°C.

2/ These parts may be dose rate sensitive in a space environment and may demonstrate enhanced low dose rate effects. Radiation end point limits for the noted parameters are guaranteed only for the conditions specified in MIL-STD-883, method 1019, condition A.

3/ This parameter is not tested post irradiation.

4/ Output high voltage without pull-up resistor. It may be useful to have a pull-up resistor to V₊ for 3V operation.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein. For device class M, sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection. For device class M, screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 7

Device type	01	
Case outlines	P	H
Terminal number	Terminal symbol	
1	V+	NC
2	+IN	V+
3	-IN	+IN
4	V-	-IN
5	LATCH	V-
6	GND	LATCH
7	OUT	GND
8	$\overline{\text{OUT}}$	OUT
9	----	$\overline{\text{OUT}}$
10	----	NC

NC = No connect

FIGURE 1. Terminal connections.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 8

TABLE IIA. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class M	Device class Q	Device class V
Interim electrical parameters (see 4.2)	1	1	1
Final electrical parameters (see 4.2)	1, 2, 3, 9 <u>1/</u>	1, 2, 3, 9 <u>1/</u>	1, 2, 3, 9 <u>1/ 2/</u>
Group A test requirements (see 4.4)	1, 2, 3, 9	1, 2, 3, 9	1, 2, 3, 9
Group C end-point electrical parameters (see 4.4)	1	1	1 <u>2/</u>
Group D end-point electrical parameters (see 4.4)	1	1	1
Group E end-point electrical parameters (see 4.4)	1	1	1

1/ PDA applies to subgroup 1.

2/ Delta limits as specified in table IIB shall be required where specified, and the delta limits shall be computed with reference to the previous endpoint electrical parameters.

TABLE IIB. 240 hour burn-in and group C end-point electrical parameters.

Parameter	Limit		Delta <u>1/</u>		Unit
	Min	Max	Min	Max	
V _{OS}	-7	7	-1	1	mV
I _{OS}	-4	+4	-1	1	μA

1/ Delta limits apply to ±5 V condition.

4.2.1 Additional criteria for device class M.

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015.

(2) T_A = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table IIA herein.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 9

4.2.2 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table IIA herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections and as specified herein. Quality conformance inspection for device class M shall be in accordance with MIL-PRF-38535, appendix A and as specified herein. Inspections to be performed for device class M shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection.

- a. Tests shall be as specified in table IIA herein.
- b. Subgroups 4, 5, 6, 7, 8, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table IIA herein.

4.4.2.1 Additional criteria for device class M. Steady-state life test conditions, method 1005 of MIL-STD-883:

- a. Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- b. $T_A = +125^\circ\text{C}$, minimum.
- c. Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.4.2.2 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table IIA herein.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes M, Q, and V shall be as specified in MIL-PRF-38535. End point electrical parameters shall be as specified in table IIA herein.

4.4.4.1 Total dose irradiation testing. Total dose irradiation testing shall be performed in accordance with MIL-STD-883 method 1019, condition A, and as specified herein.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V or MIL-PRF-38535, appendix A for device class M.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-0015
		REVISION LEVEL B	SHEET 10

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.1.2 Substitutability. Device class Q devices will replace device class M devices.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.3 Record of users. Military and industrial users should inform Defense Supply Center Columbus when a system application requires configuration control and which SMD's are applicable to that system. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.4 Comments. Comments on this drawing should be directed to DSCC-VA , Columbus, Ohio 43216-5000, or telephone (614) 692-0547.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in QML-38535. The vendors listed in QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DSCC-VA and have agreed to this drawing.

6.6.2 Approved sources of supply for device class M. Approved sources of supply for class M are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-00515
		REVISION LEVEL B	SHEET 11

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 02-07-29

Approved sources of supply for SMD 5962-00515 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-0051501VPA	24355	AD8561AQ/QMLV
5962-0051501VHA	24355	AD8561AL/QMLV
5962R0051501VPA	24355	AD8561AQ/QMLR
5962R0051501VHA	24355	AD8561AL/QMLR

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.

2/ **Caution.** Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

Vendor name and address

24355

Analog Devices
 RT 1 Industrial Park
 PO Box 9106
 Norwood, MA 02062
 Point of contact: 1500 Space Park Drive
 PO Box 58020
 Santa Clara, CA 95052-8020

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.