

## **Features**

- RoHS compliant\*
- ESD protection >25 kV
- Surge protection
- Low capacitance

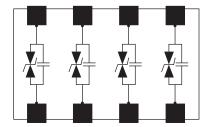


# ChipGuard® CG0805MLA Series - ESD Protector Array

## **General Information**

The ChipGuard® CG0805MLA series has been designed to provide a competitive solution for applications where capacitance of the ESD protector is less critical. General ESD applications in end products such as computers, analog modems and PDAs include IC supply lines, MOSFET gate protection, and low frequency digital and control lines.

The ChipGuard® CG0805MLA quad device is designed to protect equipment to IEC61000-4-2, Level 4 (±8 kV Contact / ±15 kV Air Discharge) ESD specification.



## Electrical & Thermal Characteristics @ TA = 25 °C (unless otherwise noted)

Parameter	Symbol	CG0805MLA-5.5ME	CG0805MLA-18KE	Unit
Maximum AC Working Voltage @ 10 μA Max.	V <sub>RMS</sub>	4	14	V
Maximum DC Working Voltage @ 10 µA Max.	$V_{DC}$	5.5	18	V
Minimum Resistance @ 3.3 VDC	I <sub>R</sub>	10		Megohms
Maximum Peak Current @ 8/20 µS Pulse	IS	10	5	Α
Maximum Transient Energy @ 10/1000 μS Pulse	W <sub>Max</sub>	0.01		Joules
Nominal Breakdown Voltage @ 1 mA	V <sub>BR</sub>	12	28	V
Breakdown Voltage Tolerance	%_ VBR	20	10	%
Maximum Clamping Voltage @ 1 A 8/20 μS Pulse	VC	28	58	V
Typical Capacitance @ 1 VRMS 1 kHz	C <sub>O1</sub>	36	17	pF
Typical Capacitance @ 1 VRMS 1 MHz	C <sub>O2</sub>	33	15	pF
Maximum Response Time	R <sub>T</sub>	1		ns
ESD Protection: Per IEC 61000-4-2 Min. Contact Discharge (>100 Reps) Min. Air Discharge (>100 Reps)		±	kV kV	
Operating Temperature	T <sub>OPR</sub>	-40 t	°C	
Storage Temperature	TSTG	-40 t	°C	



WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

\*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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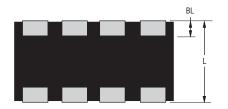
## **Applications**

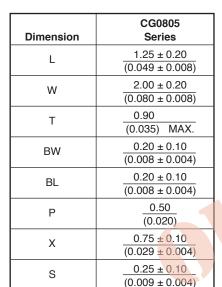
- IC supply line protection
- MOSFET gate protection
- Control line protection
- Low frequency I/O line protection

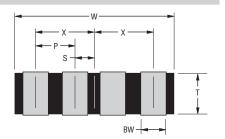
# ChipGuard® CG0805MLA Series - ESD Protector Array

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## **Product Dimensions**





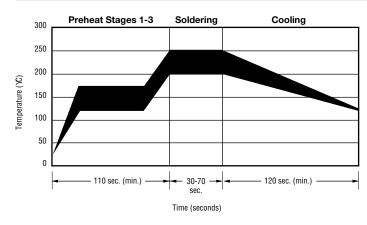


DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

# Recommended Pad Layout

Dimension	CG0805 Series
А	<u>0.64</u> (0.025)
В	<u>1.27</u> (0.050)
С	1.91 (0.075)
D	<u>0.28</u> (0.011)
E	<u>0.51</u> (0.020)

## **Solder Reflow Recommendations**



Α	Stage 1 Preheat	Ambient to Preheating Temperature	30 s to 60 s
В	Stage 2 Preheat	140 °C to 160 °C	60 s to 120 s
O	Stage 3 Preheat	Preheat to 200 °C	20 s to 40 s
D	Main Heating	200 °C 210 °C 220 °C 230 °C 240 °C	60 s to 70 s 55 s to 65 s 50 s to 60 s 40 s to 50 s 30 s to 40 s
Е	Cooling	200 °C to 100 °C	1 °C/s to 4 °C/s

- · This product can be damaged by rapid heating, cooling or localized heating.
- Heat shocks should be avoided. Preheating and gradual cooling recommended.
- Excessive solder can damage the device. Print solder thickness of 150 to 200 um recommended.
- Solder gun tip temperature should be kept below 280 °C and should not touch the device directly. Contact should be less than 3 seconds.
   A solder gun under 30 watts is recommended.

Specifications are subject to change without notice.

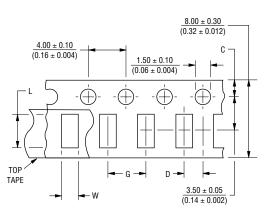
Users should verify actual device performance in their specific applications.

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# ChipGuard® CG0805MLA Series - ESD Protector Array

DIMENSIONS:

## **Packaging Dimensions**



NOTES: TAPE MATERIAL IS PAPER. TAPE THICKNESS IS  $\frac{0.60 \pm 0.05}{(0.024 \pm 0.002)}$ COVER TAPE ADHESION IS 40  $\pm$  15 GRAMS.

 $\frac{13.0 \pm 1.0}{(0.52 \pm 0.04)}$  $62.0 \pm 1.50$  $2.0 \pm 0.50$  $(2.48 \pm 0.06)$  $(0.08 \pm 0.02)$  $13.0 \pm 0.50$  $\overline{(0.52\pm0.02)}$ 21.0 ± 0.80  $\frac{180.8 \pm 2.0}{(7.12 \pm 0.08)}$  $(0.84 \pm 0.032)$ MM (INCHES)  $\frac{9.0 \pm 0.50}{(0.36 \pm 0.02)}$ 

Dimension	CG0805 Series
С	$\frac{1.75 \pm 0.10}{(0.069 \pm 0.004)}$
D	$\frac{2.00 \pm 0.05}{(0.080 \pm 0.002)}$
L	$\frac{2.40 \pm 0.20}{(0.094 \pm 0.008)}$
W	$\frac{1.65 \pm 0.20}{(0.065 \pm 0.008)}$
G	$\frac{4.00 \pm 0.10}{(0.16 \pm 0.004)}$

How to Orde	er					
	CG 080	05 - MI	A n	.n	x E	Ξ
ChipGuard® Product Designato	or _					
Package Option _ 0805 = 0805 F	Package					
Model Series — MLA = Standa	rd Capaci	tance				
Working Peak Volt 18 = 18 V 5.5 = 5.5 V	tage** —					
Tolerance ——— K = 10 % M = 20 %						
Tape & Reel Pack E = 4,000 pcs.						

Only models lower than 10 volts require decimal point.

REV. F 01/21

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