Chip Resistor Array						
Type: <b>EXB1:0201x2</b>	м	н	н	-	-	-
EXB2:0402x2, 0402x4, 0402x8	н	н	м	юн	юн	юн
EXB3:0603x2, 0603x4 EXBV:0603x2, 0603x4	м	н	м			10200
EXBS:0805x4	潮	驧	喇	100	190	100
<ul> <li>Features</li> <li>High density</li> <li>2 resistors in 0.8 mm × 0.6 mm size</li> <li>2 resistors in 1.0 mm × 1.0 mm size</li> <li>2 resistors in 1.6 mm × 1.6 mm size</li> <li>2 resistors in 1.6 mm × 1.6 mm size</li> <li>2 resistors in 1.6 mm × 1.6 mm size</li> <li>2 resistors in 2.0 mm × 1.0 mm size</li> <li>4 resistors in 3.2 mm × 1.6 mm size</li> <li>4 resistors in 3.2 mm × 1.6 mm size</li> <li>4 resistors in 5.08 mm × 2.2 mm size</li> <li>8 resistors in 3.8 mm × 1.6 mm size</li> <li>(2HV, Concave T</li> <li>8 resistors in 3.8 mm × 1.6 mm size</li> <li>(2HV, Concave T</li> <li>9 Improvement of placement efficiency</li> <li>Placement efficiency of Chip Resistor Array is two, four</li> </ul>	rminal) rminal) rminal) rminal) rminal) rerminal) erminal) erminal) ur or eight	times of t	he flat <sup>.</sup>	type chip re	sistor	
Product Code       Chip Resistor Array         Thick Film       Chip Resistor Array         Chip Resistor       1         0603×2,0603×4       8         V       0603×2,0603×4         S       0805×4	7 4 Schematics V Isolated type	8 <b>Resistance</b> The first two significant fig resistance va the third one the number of following. Jur expressed by Example: 222	9 2 2 2 4 Value digits are ures of lue and denotes f zeroes mper is r R00 2: 2.2 kW	10 1 <b>J</b> Resistance Tolerance <u>G</u> ±2 % J ±5 % 0 Jumper	1 Packaging Code Packa Embo Nil Embo taping (Only V Punchec 4 mm Punchec 2 mm X (Only 14)	Methods ging ssed S8V type) I (Paper) taping pitch I (Paper) taping pitch V 24V 28V type)
Construction (Example : EXBV8V)	Scl	nematics		L	(Only 14	*, 24¥, 20¥ (jpc)
Resistive element Protective coating	• Isol	4V, 34V, V resistors	4V 2HV sistors	28V, 38 4 ru	V, V8V, S esistors	5  4
			4 5 6 7		D	ον 02/0 <i>1</i>
					Г	01.02/04

Dimensions in mm (not to scale)





28V

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14V, 34V

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Ratings

Iter	n	Specifications
Resistance Range		10 $\Omega$ to 1 M $\Omega$ : E24 series
Resistance Tolerar	ice	G: ±2 %, J: ±5 %
	14V,24V,V4V,34V	4 terminal
Number of Terminal	28V,38V,V8V,S8V	8 terminal
	2HV	16 terminal
	14V,24V,V4V,34V	2 resistors
Number of Resistors	28V,38V,V8V,S8V	4 resistors
	2HV	8 resistors
	14V,28V	0.031 W/element
Power Pating at 70 °C	24V,V4V,34V,V8V,38V	0.063 W/element
Fower hating at 70 C	S8V	0.1 W/element
	2HV	0.063 W/element (0.25 W/package)

	Ite	m	Specifications
		14V	12.5 V
LIMI	ting Element Voltage(1)	2HV	25 V
	ax. Rated Continuous	24V,28V,38V,34V,V4V,V8V	50 V
<b>`</b> WC	orking Voltage	S8V	100 V
		14V	25 V
May	Over land Veltage(2)	2HV	50 V
wax	. Over-load voltage <sup>(2)</sup>	24V,28V,38V,34V,V4V,V8V	100 V
		S8V	200 V
T.C	:.R.		±200 ×10-6/°C(ppm/°C)
Cat	tegory Temperatur	e Range	
(Op	perating Temperati	ure Range)	-55 C to 125 C
		14V	0.5 A
ray	Rated Current	2HV,24V,28V,38V,34V,V4V,V8V	1 A
Ą		S8V	2 A
Ibel		14V	1 A
Jun	Max. Overload Current	2HV,24V,28V,38V,34V,V4V,V8V	2 A
ر 		S8V	4 A

(1) Rated Continuous Working Voltage (RCWV) should be determined from RCWV= $\sqrt{Power Rating \times Resistance Value}$ , or Limiting Element Voltage (max. RCWV) listed above, whichever is less.

(2) Overload (Short-time Overload) Test Voltage (SOTV) should be determined from SOTV=2.5 × Power Rating or max. Overload (Voltage) listed above whichever is less.

### Power Derating Curve

For resistors operating in ambient temperature above 70 °C, power rating should be derated in accordance with the right figure.



### Packaging Specifications

• Standard Quantity

Type (inches)	Thickness (mm)	Weight/1000 pcs. (g)	Punched (Paper) Taping	Embossed Taping
EXB14V (0201×2)	0.35	14V: 0.5	10000 pcs./reel	_
EXB24V, 28V (0402×2, 0402×4)	0.35	24V: 1.2 28V: 2	10000 pcs./reel	_
EXBV4V, V8V (0603×2, 0603×4)	0.6	V4V: 5 V8V: 10	5000 pcs./reel	—
EXB34V, 38V (0603×2, 0603×4)	0.5	34V: 3.5 38V: 7	5000 pcs./reel	_
EXBS8V (0805×4)	0.7	S8V: 30		2500 pcs./reel
EXB2HV (0402×8)	0.45	2HV: 9	5000 pcs./reel	

• Punched (Paper) Taping Reel



	туре	φΑ	φι	>	$\psi \cup$
Dimensions (mm)	14V, 2HV 24V, 28V V4V, 34V V8V, 38V	180.0 <sub>-3.0</sub>	60 r	nin.	13.0 <sup>±1.0</sup>
	Туре	W			Т
Dimensions (mm)	14V, 2HV 24V, 28V V4V, 34V V8V, 38V	9.0 <sup>±1.0</sup>			11.4 <sup>±2.0</sup>

	¢					M
	ip com	iponent	P <sub>1</sub> P <sub>2</sub>			direction
	Туре	A	В	W	F	E
	14V	$0.70^{\pm 0.05}$	0.90 <sup>±0.05</sup>			
	24V	1 00+0.05	1.20 <sup>±0.05</sup>			
Dimonoiono	28V	1.20-0.00	2.20 <sup>±0.10</sup>	]		
(mm)	V4V	1 05+0.15	1 05+0.20	0,00+0.20	2 50+0.05	<b>1</b> 75+0.10
(11111)	34V	1.95	1.90	0.00	3.50=	1.75=
	V8V	2 00±0.15	2 60±0.20	1		
	38V	2.00	3.00			
	2HV	1.90 <sup>±0.15</sup>	4.10 <sup>±0.15</sup>			
	Туре	P <sub>1</sub>	P <sub>2</sub>	P <sub>0</sub>	φD₀	Т
	14V					
	24V	2.00 <sup>±0.10</sup>				$0.52^{\pm 0.05}$
Dimension	28V					
Dimensions	V4V		0,00+0.05	4 00+0 10	1 50+0 10	0.84 <sup>±0.05</sup>
(11111)	34V		2.00	4.00-0.10	1.501010	$0.70^{\pm 0.05}$
	V8V	4.00 <sup>±0.10</sup>				0.84 <sup>±0.05</sup>
	38V					0.70+0.05
	2HV					0.70-0.00

## • Embossed Taping Reel



13.0<sup>±1.0</sup>

## Embossed Taping

• Punched (Paper) Taping



### Land pattern design

S8V

(mm)

Recommended land pattern design for Network chip is shown below.

15.4<sup>±2.0</sup>



				(No	ot to scale)
-		[	Dimensions		Unit (mm)
lype	а	b	С	р	f
14V	0.3	0.3	0.3	0.50	0.9
24V	0.5	0.35 to 0.40	0.35 to 0.40	0.65	1.4 to 1.5
28V	0.4	0.525	0.25	0.50	1.4
V4V,V8V	0.7 to 0.9	0.4 to 0.45	0.4 to 0.45	0.80	2 to 2.4
34V,38V	0.7 to 0.9	0.4 to 0.5	0.4 to 0.5	0.80	2.2 to 2.6
S8V	1 to 1.2	0.5 to 0.75	0.5 to 0.75	1.27	3.2 to 3.8
2HV	1	0.425	0.25	0.50	2

### Recommended Soldering Conditions

Recommendations and precautions are described below.

• Recommended soldering conditions for reflow

•Reflow soldering should be a maximum of two times •Please contact us for additional information when used in conditions other than those specified. •Please measure the temperature of the terminations and study every type of the printed circuit board for solderability, before actual use.



	Temperature	Time
Preheating	140 °C to 160 °C	60 s to 120 s
Main heating	Above 200 °C	30 s to 40 s
Peak	235 + 5 °C	may 10 s
	200 2 0 0	11107. 10.3
For lead-free solo	der (Example : Sn/A	.g/Cu)
For lead-free solo	der (Example : Sn/A	ug/Cu) Time
For lead-free solo Preheating	der (Example : Sn/A Temperature 150 °C to 180 °C	g/Cu) Time 60 s to 120 s
For lead-free solo Preheating Main heating	der (Example : Sn/A Temperature 150 °C to 180 °C Above 230 °C	g/Cu) Time 60 s to 120 s 30 s to 40 s

Flow soldering

·Flow soldering is not recommended because solder bridge may occur.

## ▲ Safety Precautions

### 1. Component Placement

- ① Take measures against mechanical stress during and after mounting, so as not to damage the termination and protective coating.
- ② Misplacement of components on the land pattern may cause solder bridge problem.
- 2. If transient load (heavy load in a short time) like pulse is expected to be applied, carry out evaluation and confirmation test with the resistors actually mounted on the board before using in production.
- When the load of more than rated power is applied under the load condition at steady state, it may impair performance and/or reliability of resistor. Never exceed the rated power.
- 3. Chlorine type or other high-activity flux is not recommeded as the residue may affect performance or reliability of resistors.
- 4. When soldering with soldering iron, never touch the body of the chip resistor with the tip of the soldering iron. When using a soldering iron with a tip at high temperature, solder for as short a time as possible (three seconds or less up to 350 °C).
- **5.** Avoid physical shock to the resistor and nipping of the resistor with hard tool (pliers or tweezers) as it may damage protective film or the body of resistor and may affect resistor's performance.
- 6. Do not use the product in high humidity atmospheres.