LT3825

DESCRIPTION

Demonstration circuit 894A-B is a 36V-72Vin, synchronous flyback converter featuring the LT3825. This circuit was designed specifically to attain a high current, low ripple, synchronously rectified flyback to efficiently power 5.0V loads at up to 8A from a typical telecom input voltage range. This circuit features synchronous rectifier drive outputs, output voltage regulation without the need of an optocoupler, selfstarting architecture and input undervoltage lockout.

Design files for this circuit board are available. Call the LTC factory.

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PARAMETER	CONDITION	VALUE	
Minimum Input Voltage		36V	
Maximum Input Voltage		72V	
Output Voltage V _{OUT}	V _{IN} = 36V to 72V, I _{OUT} = 0A to 8A	5.0V	
Maximum Output Current		8A	
Output Ripple V _{OUT}	V _{IN} = 48V, I _{OUT} = 8A	< 20mV _{P–P} (typical)	
Output Regulation	Over All Input Voltages and Output Currents	±1.0% (typical)	
Load Transiant Deepenan	Peak Deviation with Load Step of 6A to 8A (10A/us)	±100mV (< ± 2%)	
Load Transient Response	Settling Time	100us	
Nominal Switching Frequency		200kHz	
Efficiency	V _{IN} = 48V, I _{OUT} = 8A	91% (typical)	

Table 1. Performance Summary $(T_A = 25^{\circ}C)$

OPERATING PRINCIPLES

The LT3825 controller exhibits a self-starting capability. When an input voltage is applied, a trickle charge resistor, R8, charges C10 (See Figure 9) to power Vcc. Then, the IC begins a controlled soft-start of the output voltage. As this voltage begins to rise, Vcc power is quickly taken over by T1, D2, and R7. When the soft-start period is over, the LT3825 then regulates output voltage by observing the pulses across the auxiliary winding of T1 during the flyback time. The Primary Gate drive (PG) and Synchronous Gate (SG) drive is then Pulse Width Modulated (PWM) in order to keep the output voltage constant. The synchronous gate drive signal is transmitted to the secondary via the small signal transformer, T2. The output of T2 then drives a discreet gate drive buffer, R26, Q12, and Q13 in order to achieve fast gate transition times, hence a higher efficiency.

The two-stage input filter, C25, L1, and C30 and output filter, C1, C2, C5, L2, and C29 are the reasons that this flyback has exceptionally low conducted emissions.



QUICK START PROCEDURE

Demonstration circuit 894A-B is easy to set up to evaluate the performance of the LT3825. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the output (or input) voltage ripple by touching the probe tip and probe ground directly across the +Vout and –Vout (or +Vin and –Vin) terminals. See Figure 2 for proper scope probe technique.

- Set an input power supply that is capable of 36V to 72V at a current of at least 2A to a voltage of 36V. Then, turn off the supply.
- 2. With power off, connect the supply to the input terminals +Vin and –Vin.
 - a. Input voltages lower than 36V can keep the converter from turning on due to the undervoltage lockout feature of the LT3825.
 - b. If efficiency measurements are desired, an ammeter capable of measuring 2Adc can be put in series with the input supply in order to measure the DC894A-B's input current.
 - c. A voltmeter with a capability of measuring at least 72V can be placed across the input terminals in order to get an accurate input voltage measurement.

3. Turn on the power at the input.

NOTE: Make sure that the input voltage never exceeds 72V.

- 4. Check for the proper output voltage of 5.0V
- 5. Turn off the power at the input.
- Once the proper output voltages are established, connect a variable load capable of sinking 8A at 5.0V to the output terminals +Vout and -Vout. Set the current for OA.
 - a. If efficiency measurements are desired, an ammeter or a resistor current shunt that is capable of handling at least 8Adc can be put in series with the output load in order to measure the DC894A-B's output current.
 - b. A voltmeter with a capability of measuring at least 5.0V can be placed across the output terminals in order to get an accurate output voltage measurement.
- 7. Turn on the power at the input.

NOTE: If there is no output, temporarily disconnect the load to make sure that the load is not set too high.

 Once the proper output voltage is established, adjust the load within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other desired parameters.



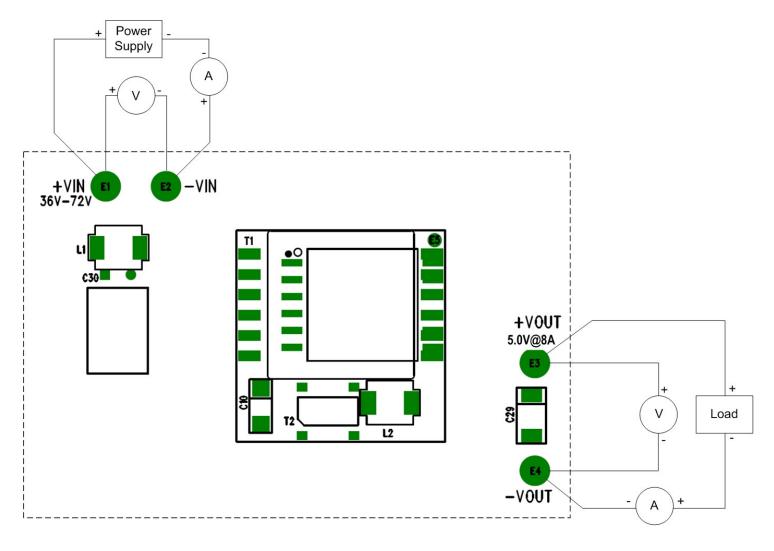


Figure 1. Proper Measurement Equipment Setup

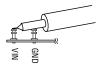


Figure 2. Measuring Input or Output Ripple



MEASURED DATA

Figures 3 through 10 are measured data for a typical DC894A-B. Figures 11 through 13 are schematics and bill of materials.

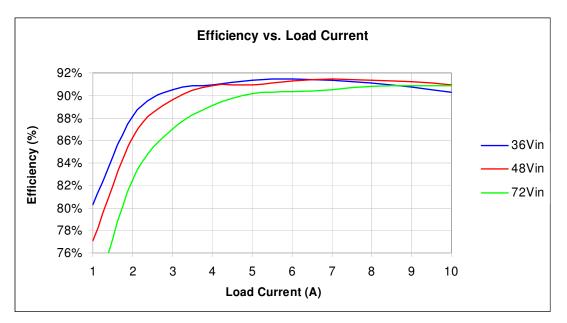


Figure 3. Efficiency (no airflow)

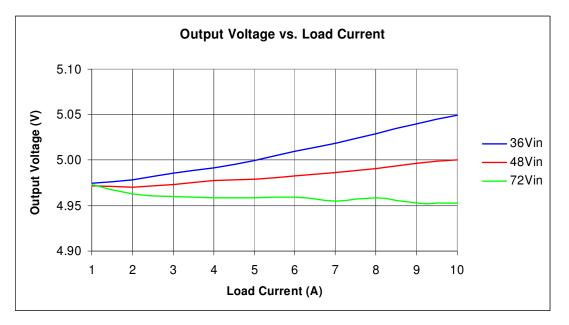


Figure 4. Regulation (no airflow)



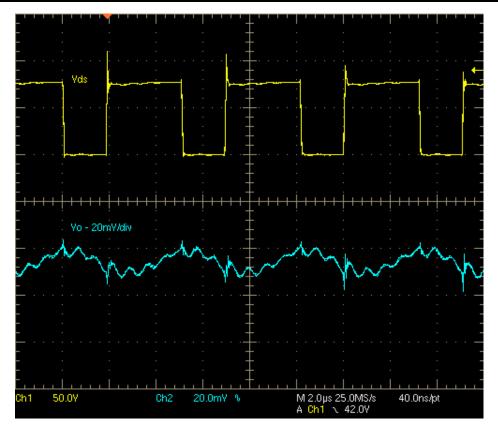


Figure 5. Output Voltage Ripple (48Vin 8A)

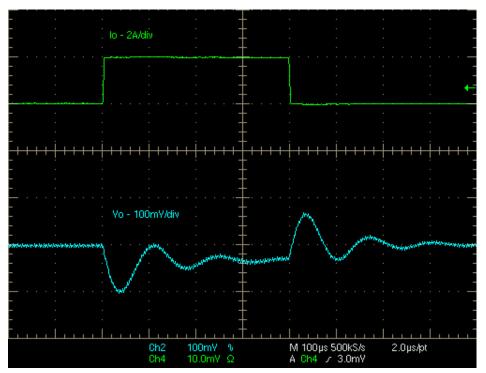
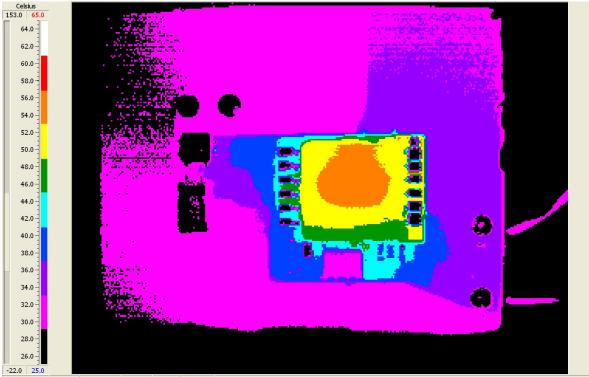


Figure 6. Load Transient Response (10A/us)





Thermoteknix TVS-700 12:53:21 PM 8/3/2006 e : 0.95 Bg : 0.0°C

Figure 7. Temp Data (48Vin, 8A, 100LFM airflow - front)

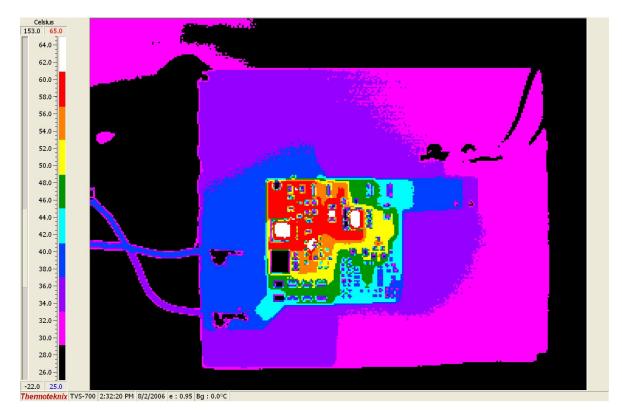


Figure 8. Temp Data (48Vin, 8A, 100LFM airflow - back)





Figure 9. Temp Data (48Vin, 8A, no airflow - front)

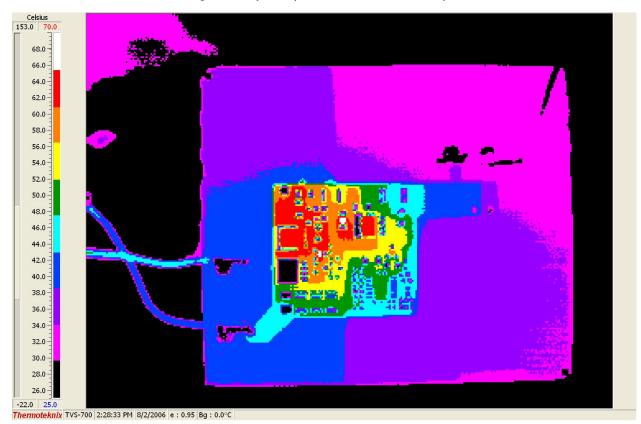
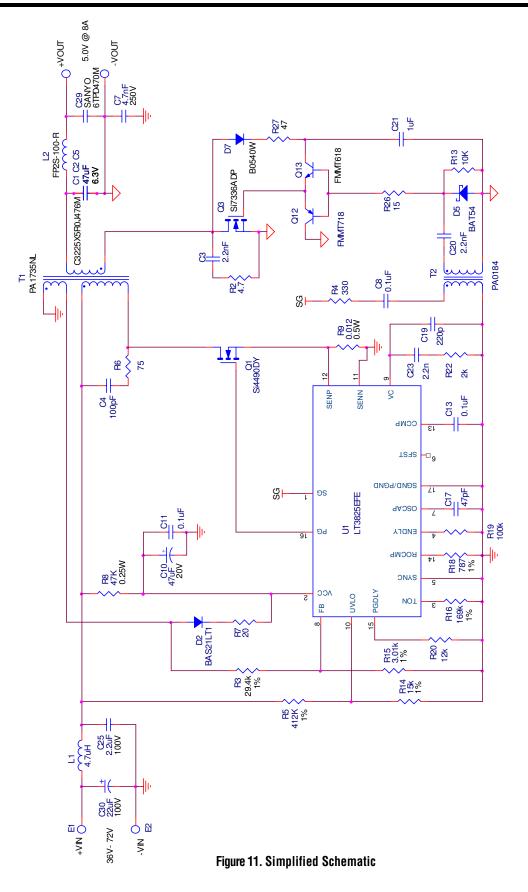
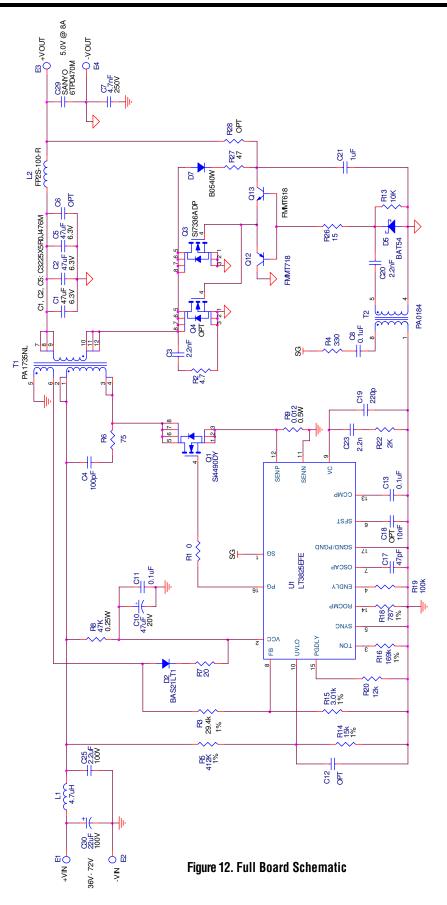


Figure 10. Temp Data (48Vin, 8A, no airflow - back)











ltem	Qty	Ref-Des	Description	Manufacturer's Part Number
REG	UIRE	D CIRCUIT CO	MPONENTS ¹	
REC 2 1 2 3 4 2 5 3 6 7 4 8 5 9 10 6 7	UIREI 1 3 1 3 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1	D CIRCUIT CO C3 C4 C5,C2,C1 C7 C8,C11,C13 C10 C12 C17 C19 C20 C21 C21 C23 C25 C29 D2 D5 D7 L2	CAP, 1206 2.2nF 20% 100V X7R CAP, 1206 100pF 10% 630V COG CAP, 1210 47uF 20% 6.3V X5R CAP, 4.7nF 10% 250V X7R	AVX 12061C222MAT TDK C3216COG2J101K TDK C3225X5R0J476MT MURATA GA343DR7GD472KW01L TDK C1608X7R1H104K AVX TAJC476M020R AVX 06033A471KAT2A OPTION AVX 06033A470KAT2A AVX 06035A221JAT AVX 06035C222JAT AVX 06035C222JAT AVX 12063C105MAT2A AVX 06035C222JAT TDK C4532X7R2A225K SANYO 6TPD470M DIODES INC. BAS21-7-F DIODES INC. BAT54-7 DIODES INC. B0540W-7-F COILTRONICS FP2S-100-R
8 9 11 12 10 11 13 14 12 13 15 14 16 17 18 19 15 20 21 16 22 17 18 23 24	- 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Q1 Q3 Q12 Q13 R2 R3 R4 R5 R6 R7 R8 R9 R13 R14 R15 R16 R18 R19 R20 R22 R26 R27 T1 T2 U1	XSTR, MOSFET N-CHANNEL XSTR, MOSFET N-CHANNEL XSTR, PNP XSTR, NPN RES, 1206 4.7 OHMS 5% 1/4W RES, 0603 29.4K OHMS 1% 1/10W RES, 0603 330 OHMS 5% 1/10W RES, 0603 412K OHMS 1% 1/8W RES, 1206 75 OHMS 5% 1/4W RES, 1206 75 OHMS 5% 1/4W RES, 1206 47K OHMS 5% 1/4W RES, 1206 0.012 OHMS 5% 1/4W RES, 1206 0.012 OHMS 1% 0.5W RES, 0603 10K OHMS 5% 1/10W RES, 0603 15K OHMS 1% 1/10W RES, 0603 169K OHMS 1% 1/10W RES, 0603 169K OHMS 1% 1/10W RES, 0603 12K OHMS 5% 1/10W RES, 0603 12K OHMS 5% 1/10W RES, 0603 15 OHMS 5% 1/10W RES, 0805 47 OHMS 5% 1/8W XFMR, PA1735NL XFMR, 1.4mH MIN, 50KHz IC, LT3825EFE	VISHAY SILICONIX Si4490DY VISHAY SILICONIX Si7336ADP ZETEX FMMT718TA ZETEX FMMT618TA AAC CR18-4R7JM AAC CR16-2942FM AAC CR16-331JM AAC CR16-4123FM AAC CR16-4123FM AAC CR18-750JM AAC CR18-750JM AAC CR18-750JM AAC CR18-473JM IRC LRC-LRF1206-01-R012-F AAC CR16-103JM AAC CR16-103JM AAC CR16-1502FM AAC CR16-1693FM AAC CR16-1693FM AAC CR16-1693FM AAC CR16-104JM AAC CR16-104JM AAC CR16-123JM AAC CR16-123JM AAC CR16-150JM VISHAY CRCW0805470JRT6 PULSE PA1735NL PULSE PA0184 LINEAR TECH LT3825EFE

ADD	ADDITIONAL DEMO BOARD CIRCUIT COMPONENTS ²				
1	0	C6	CAP, 1210 47uF 20% 6.3V X5R OPTION	TDK C3225X5R0J476MT OPTION	
3	0	C18	CAP, 0603 10nF 10% 25V X7R OPTION	AVX 06033C103KAT OPTION	



4	1	C30	CAP, 22uF 100V
5	1	L1	IND, 4.7uH
6	0	Q4	XSTR, MOSFET N-CHANNEL OPTION
7	1	R1	RES, 0603 0 OHM JUMPER
1	0	R28	RES, 0805 OPTION

SANYO 100ME22AXTS VISHAY IHPL2525CZER4R7M01 VISHAY SILICONIX Si7336ADP OPTION VISHAY CRCW0603000ZRT6 OPTION

Notes:

1. Required Circuit Components are those parts that are required to implement the circuit function

2. Additional Demo Board Circuit Components are those parts that provide added functionality for the demo board but are or may not be required in the actual circuit.

Figure 13. Bill of Materials

