

Features

- RoHS compliant*
- Low capacitance 1 pF
- ESD protection >15 kV
- Protects 4 I/O and 1 V_{DD} line

Applications

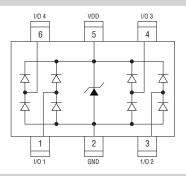
- HDMI 1.3 version
- PDAs and notebooks
- Consumer electronics
- Display port interface
- USB 2.0 up to 480 Mb/s

CDS0T236-0504C - TVS/Steering Diode Array

General Information

The CDSOT236-0504C device provides ESD, EFT and Surge protection for high speed data ports meeting IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array offers a Working Peak Reverse Voltage of 5 V and Minimum Breakdown Voltage of 6 V.

The SOT23-6 packaged device will mount directly onto the industry standard SOT23-6 footprint. Bourns® Chip Diodes are easy to handle with standard pick and place equipment and their flat configuration minimizes roll away.



Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDSOT236-0504C	Unit
Peak Pulse Current (tp = 8/20 µs)	I _{PP}	5.5	A
Storage Temperature	T _{STG}	-55 to +150	°C
Operating Temperature	T _{OPR}	-55 to +85	°C
Operating Supply Voltage	V _{DC}	6	V
ESD per IEC 61000-4-2 (Air) (I/O Pins) ESD per IEC 61000-4-2 (Contact) (I/O Pins)	V _{ESD_IO}	15 8	kV
ESD per IEC 61000-4-2 (Air) (V _{CC} to GND) ESD per IEC 61000-4-2 (Contact) (VCC to GND)	V _{ESD_} VCC	30 30	kV
DC Voltage at any I/O Pin	V _{IO}	(GND-0.5) to (V _{CC} +0.5)	V

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDSOT236-0504C	Unit
Maximum Reverse Standoff Voltage ¹	V _{RWM}	5.0	V
Maximum Leakage Current ¹ @ V _{RWM}	ΙL	2.0	μΑ
Maximum Channel Leakage Current @ VRWM	I _{CD}	1.0	μΑ
Minimum Reverse Breakdown Voltage ¹ @ I _{BV} = 1 mA	V_{BR}	6.0	V
Maximum Forward Voltage ⁴ @ I _F = 15 mA	V _F	1.2	V
Maximum Clamping Voltage ² @ 5 A 8/20 μs	V _C	10	V
Typical ESD Clamping Voltage - I/O ²	V _{clamp_io}	14	V
Maximum Channel Input Capacitance ² @ VPIN5 = 5 V, VPIN2 = 0 V, VIN = 2.5 V, f = 1 MHz	C _{IN}	1.2	pF
Maximum Channel to Channel Input Capacitance ³ @ V _{PIN5} = 5 V, V _{PIN2} = 0 V, V _{IN} = 2.5 V, f = 1 MHz	CCROSS	0.12	pF
Maximum Variation of Channel Input Capacitance @ V _{PIN5} = 5 V, V _{PIN2} = 0 V, V _{IN} = 2.5 V, f = 1 MHz (I/O Pin to GND)	ΔC _{IN}	0.05	pF

NOTES:

- 1. Pin 5 to Pin 2 (GND)
- 3. Between any two of Pins 1,3,4,6
- 2. Pin 1,3,4 or 6 to Pin 2 (GND)
- 4. Pin 2 (GND) to Pin 5



*RoHS Directive 2015/863, Mar 31, 2015 and Annex.

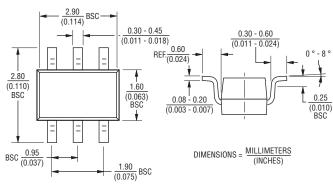
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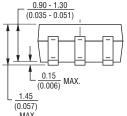
CDS0T236-0504C - TVS/Steering Diode Array

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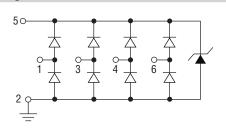
Product Dimensions

This is a molded SOT23-6L package with lead free 100 % Matte Sn on the lead frame. It weighs approximately 3 mg and has a flammability rating of UL 94V-0.

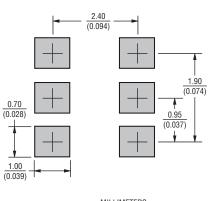




Circuit Diagram



Recommended Footprint

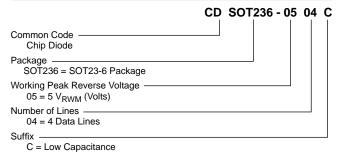


 $DIMENSIONS = \frac{MILLIMETERS}{(INCHES)}$

Typical Part Marking

CDSOT236-0504C54C

How to Order

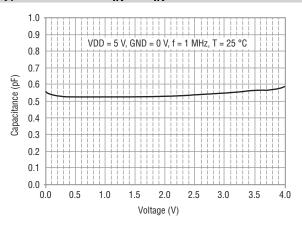


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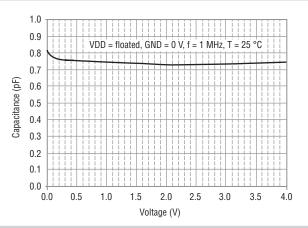
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Typical Characteristics

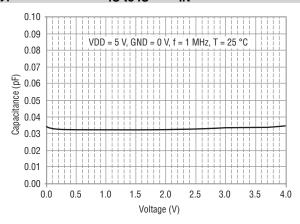
Typical Variation of CIN vs. VIN



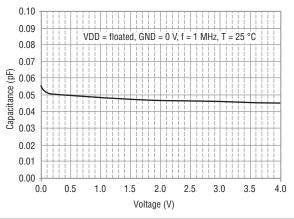
Typical Variation of CIN vs. VIN



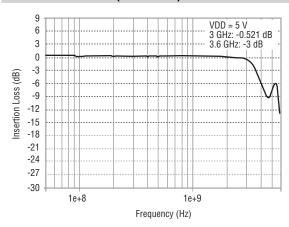
Typical Variation of CIO to IO vs. VIN



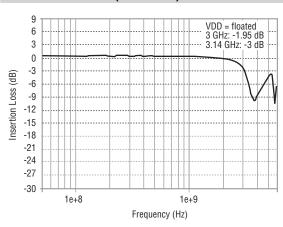
Typical Variation of CIO to IO vs. VIN



Insertion Loss S21 (I/O to GND)



Insertion Loss S21 (I/O to GND)



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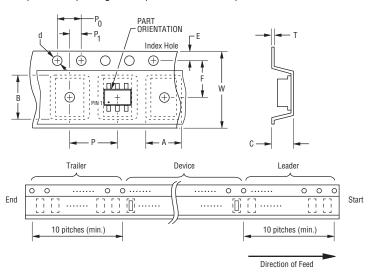
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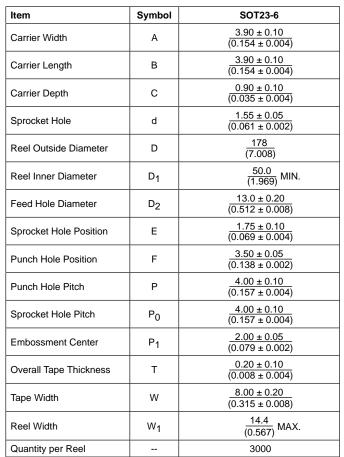
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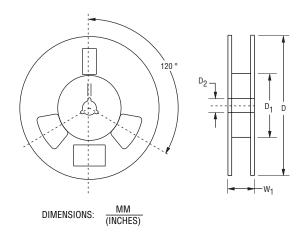
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Packaging Information

The product is packaged in tape and reel format per EIA-481 standard.







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