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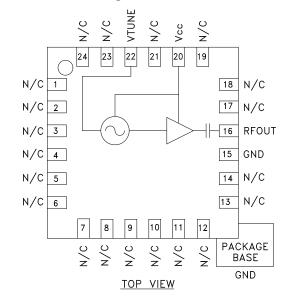


Typical Applications

Low noise MMIC VCO w/Buffer Amplifier for:

- Wireless Infrastructure
- Industrial Controls
- Test Equipment
- Military

Functional Diagram



HMC384LP4 / 384LP4E

MMIC VCO w/ BUFFER AMPLIFIER, 2.05 - 2.25 GHz

Features

Pout: +3.5 dBm Phase Noise: -112 dBc/Hz @100 KHz No External Resonator Needed Single Supply: 3V @ 35 mA QFN Leadless SMT Package, 16 mm²

General Description

The HMC384LP4 & HM384LP4E are GaAs InGaP Heterojunction Bipolar Transistor (HBT) MMIC VCOs with integrated resonators, negative resistance devices, varactor diodes, and buffer amplifiers. The VCO's phase noise performance is excellent over temperature, shock, vibration and process due to the oscillator's monolithic structure. Power output is 3.5 dBm typical from a 3V supply voltage. The voltage controlled oscillator is packaged in a low cost leadless QFN 4 x 4 mm surface mount package.

Electrical Specifications, $T_{A} = +25^{\circ}$ C, Vcc = +3V

Parameter	Min.	Тур.	Max.	Units
Frequency Range	2.05 - 2.25		GHz	
Power Output	0.5	3.5		dBm
SSB Phase Noise @ 100 kHz Offset, Vtune= +5V @ RF Output		-112		dBc/Hz
Tune Voltage (Vtune)	0		10	V
Supply Current (Icc) (Vcc = +3.0V)		35		mA
Tune Port Leakage Current			10	μA
Output Return Loss		6		dB
Harmonics 2nd 3rd		-7 -23		dBc dBc
Pulling (into a 2.0:1 VSWR)		2.5		MHz pp
Pushing @ Vtune= +5V		5		MHz/V
Frequency Drift Rate		0.25		MHz/°C

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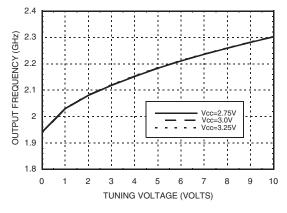
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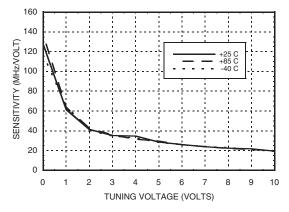
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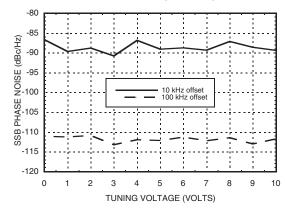
Frequency vs. Tuning Voltage, T= 25°C



Sensitivity vs. Tuning Voltage, Vcc= +3V

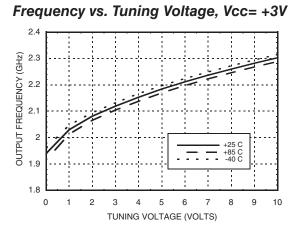


Phase Noise vs. Tuning Voltage

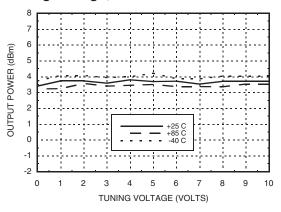




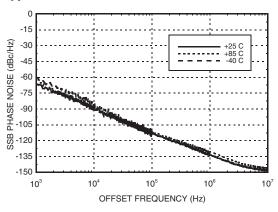
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Output Power vs. Tuning Voltage, Vcc= +3V



Typical SSB Phase Noise @ Vtune= +5V



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MMIC VCO w/ BUFFER

Typical Supply Current vs. Vcc

Note: VCO will operate over full voltage range shown above.

Vcc (V)

2.75

3.0

3.25

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AMPLIFIER, 2.05 - 2.25 GHz

Icc (mA)

28

35

41

Absolute Maximum Ratings

Vcc	+3.5 Vdc
Vtune	0 to +11V
Channel Temperature	135 °C
Continuous Pdiss (T = 85°C) (derate 6.28 mW/°C above 85°C)	565 W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C



ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS**

Outline Drawing

BOTTOM VIEW 4.10 3.90 PIN 24 ⊢.016 [0.40] REF .161 .012 .007 0.30 -.008 [0.20] MIN 19 24 L PIN 1 18 2.56 HNNN XXXX 101 25 6 13 12 EXPOSED GROUND PADDLE 7 LOT NUMBER .116 MUST BE CONNECTED TO 2.65 RF/DC GROUND 1.00 SQUARE .002 .000 0.05 NOTES: 1. LEADFRAME MATERIAL: COPPER ALLOY SEATING 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]. PLANE 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE .003[0.08]|C 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. -C-

PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.

5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.

6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLERED TO PCB RF GROUND.

7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED PCB LAND PATTERN

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC384LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	H384 XXXX
HMC384LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>H384</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1- 14, 17 - 19, 21, 23, 24	N/C	No Connection	
15	GND	This pin must be connected to RF & DC ground.	
16	RFOUT	RF output (AC coupled)	
20	Vcc	Supply Voltage Vcc= 3V	VccO
22	VTUNE	Control Voltage Input. Modulation port bandwidth dependent on drive source impedance.	$\begin{array}{c} 7.5 \text{nH} \\ 150 \text{o} \\ 2.4 \text{pF} \\ = \\ \end{array} \begin{array}{c} C;= \\ 3.6 \text{pF} \\ = \\ \end{array}$
	GND	Package bottom has an exposed metal paddle that must be RF & DC grounded.	⊖ GND

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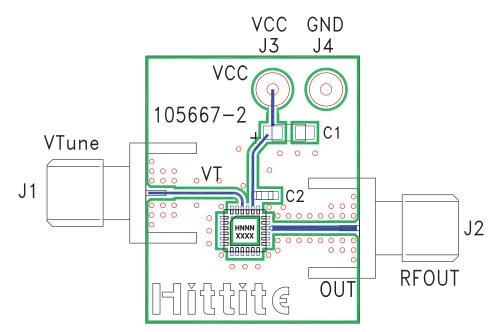
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Evaluation PCB



List of Materials for Evaluation PCB 105706^[1]

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3 - J4	DC Pin
C1	4.7 µF Tantalum Capacitor
C2	10,000 pF Capacitor, 0603 Pkg.
U1	HMC384LP4 / HMC384LP4E VCO
PCB [2]	105667 Eval Board

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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ROHS V

Notes:

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