



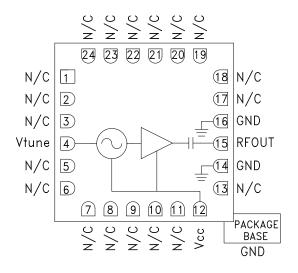
WIDEBAND MMIC VCO w/ BUFFER AMPLIFIER, 10 - 20 GHz

Typical Applications

Low Noise wideband MMIC VCO is ideal for:

- Industrial/Medical Equipment
- Test & Measurement Equipment
- Military Radar, EW & ECM

Functional Diagram



Features

Wide Tuning Bandwidth

Pout: +3 dBm

Low SSB Phase Noise: -90 dBc/Hz @100 kHz

No External Resonator Needed

Single Positive Supply: +5V @ 70 mA RoHS Compliant 4 x 4 mm SMT Package

General Description

The HMC733LC4B is a wideband MMIC Voltage Controlled Oscillator which incorporates the resonator, negative resistance device, and varactor diode. Output power and phase noise performance are excellent over temperature due to the oscillator's monolithic construction. The Vtune port accepts an analog tuning voltage from 0 to +22V. The HMC733LC4B VCO operates from a single +5V supply, consumes only 70 mA of current, and is housed in a RoHS compliant SMT package. This wideband VCO uniquely combines the attributes of ultra small size, low phase noise, low power consumption, and wide tuning range.

Electrical Specifications, $T_A = +25^{\circ}$ C, Vcc = +5V

| Parameter | Min. | Тур. | Max. | Units |
|--|---------|-------|------|--------|
| Frequency Range | 10 - 20 | | GHz | |
| Power Output | | 3 | | dBm |
| SSB Phase Noise @ 10 kHz Offset | | -60 | | dBc/Hz |
| SSB Phase Noise @ 100 kHz Offset | | -90 | | dBc/Hz |
| Tune Voltage (Vtune) | -0.25 | | 23 | V |
| Supply Current (Icc) (Vcc = +5V) | | 70 | | mA |
| Tune Port Leakage Current (Vtune = +23V) | | 25 | | μΑ |
| Output Return Loss | | 10 | | dB |
| 2nd Harmonic | | -20 | | dBc |
| Pulling (into a 2.0:1 VSWR) | | 15 | | MHz pp |
| Vcc Pushing, Vtune = +20V, F = 20 GHz | | -90 | | MHz/V |
| Frequency Drift Rate @ 10 GHz | | -0.25 | | MHz/°C |
| Frequency Drift Rate @ 20 GHz | | -0.80 | | MHz/°C |

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HMC733* PRODUCT PAGE QUICK LINKS

Last Content Update: 02/23/2017

COMPARABLE PARTS -

View a parametric search of comparable parts.

EVALUATION KITS

• HMC733LC4B Evaluation Board

DOCUMENTATION

Data Sheet

• HMC733 Data Sheet

REFERENCE MATERIALS 🖵

Quality Documentation

- Package/Assembly Qualification Test Report: LC4, LC4B (QTR: 2014-00380 REV: 01)
- Semiconductor Qualification Test Report: GaAs HBT-A (QTR: 2013-00228)

DESIGN RESOURCES

- HMC733 Material Declaration
- PCN-PDN Information
- · Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all HMC733 EngineerZone Discussions.

SAMPLE AND BUY

Visit the product page to see pricing options.

TECHNICAL SUPPORT 🖳

Submit a technical question or find your regional support number.

DOCUMENT FEEDBACK 🖳

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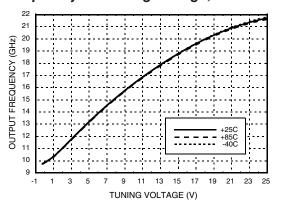
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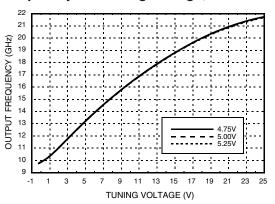


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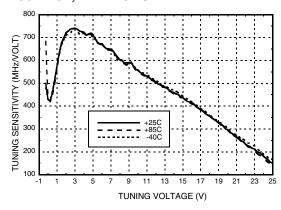
Frequency vs. Tuning Voltage, Vcc = +5V



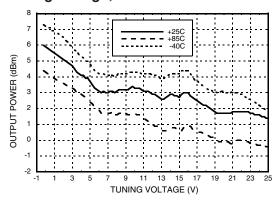
Frequency vs. Tuning Voltage, T = +25 °C



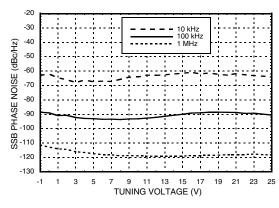
Sensitivity vs. Tuning Voltage, Vcc= +5V, T = +25 °C



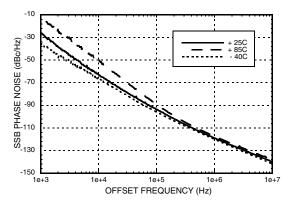
Output Power vs.
Tuning Voltage, Vcc= +5V



SSB Phase Noise vs. Tuning Voltage, T = +25 °C



Typical SSB Phase Noise vs. Temperature Vtune = +10V

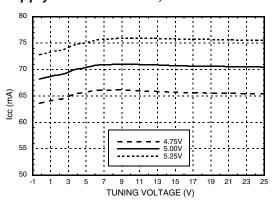


WIDEBAND VCOS - SMI

v04.0514

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Supply Current vs. Vcc, T = +25 °C





Absolute Maximum Ratings

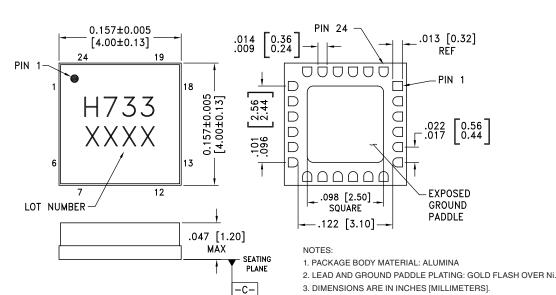
| Vcc | +5.5 Vdc |
|-----------------------|----------------|
| Vtune | -1.0 to +25V |
| Storage Temperature | -65 to +150 °C |
| ESD Sensitivity (HBM) | Class 1A |

Reliability Information

| Junction Temperature To Maintain 1 Million Hour MTTF | 135 °C |
|---|------------------|
| Nominal Junction Temperature (T = 85 °C) | 119 °C |
| Thermal Resistance (Junction to GND paddle, 5V supply) | 97 °C/W |
| Operating Temperature | -40 °C to +85 °C |

Outline Drawing

BOTTOM VIEW



Package Information

| Part Number | Package Body Material | Lead Finish | MSL Rating | Package Marking [2] |
|-------------|-----------------------|------------------|---------------------|---------------------|
| HMC733LC4B | Alumina, White | Gold over Nickel | MSL3 ^[1] | H733 XXXX |

[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX

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4. LEAD SPACING TOLERANCE IS NON-CUMULATIVE. 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm DATUM -C-6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED

TO PCB RF GROUND.





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Pin Descriptions

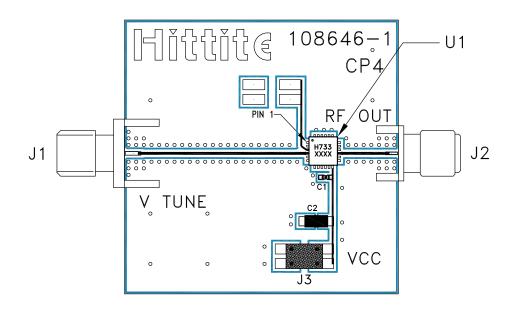
| Pin Number | Function | Description | Interface Schematic |
|-------------------------------|----------|---|---------------------|
| 1 - 3, 5 - 11, 13, 17 - 24 | N/C | No Connection. These pins may be connected to RF/DC ground. Performance will not be affected. | |
| 4 | Vtune | Control Voltage and Modulation Input. Modulation bandwidth dependent on drive source impedance. | Vtune 0 50 1.4 pF |
| 12 | Vcc | Supply Voltage Vcc= +5V | Vcc ○ |
| 14, 16 | GND | Package bottom has an exposed metal paddle that must also be RF & DC grounded. | ⊖ GND = |
| 15 | RFOUT | RF output (AC coupled) | RFOUT |





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Evaluation PCB



List of Materials for Evaluation PCB 108648 [1]

| Item | Description |
|--------------------|-------------------------------------|
| J1 | PCB Mount SMA RF Connector, Johnson |
| J2 | PCB Mount SMA Connector, SRI |
| J3 | DC Header |
| C1 | 1000 pF Capacitor, 0402 Pkg. |
| C2 | 4.7 μF Capacitor, Tantalum |
| U1 | HMC733LC4B VCO |
| PCB ^[2] | 108646 Eval Board |

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed ground paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.







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