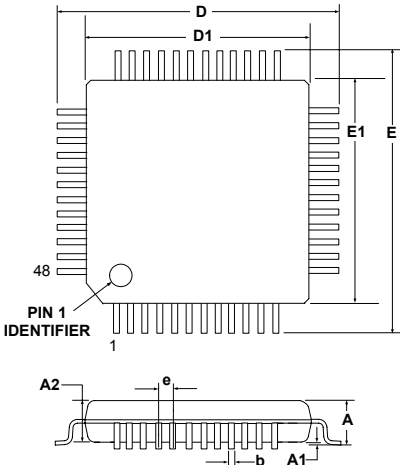


SELECTED ELECTRICAL SPECIFICATIONS TA = -40°C to +85°C unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GLOBAL CHARACTERISTICS					
Digital Supply Voltage		2.7		3.6	V
Digital Supply Current with CPU active	Clock=25MHz Clock=1MHz Clock=32kHz; VDD Monitor Disabled		9 0.4 11		mA mA μA
Digital Supply Current (shutdown)	Oscillator not running; VDD Monitor Enabled Oscillator not running; VDD Monitor Disabled		7 0.1		μA μA
Digital Supply RAM Data Retention Voltage			1.5		V
CPU & DIGITAL I/O PORTS					
Clock Frequency Range		DC		25	MHz
Port Output High Voltage	I _{OH} = -3mA, Port I/O push-pull	VDD - 0.7			V
Port Output Low Voltage	I _{OL} = 8.5mA			0.6	V
Input High Voltage		0.7 x VDD			V
Input Low Voltage				0.3 x VDD	V
SPI Bus Clock Frequency	fCLK=MCU Clock; SPI in Master Mode			fCLK/2	MHz
A/D CONVERTER					
Resolution			8		bits
Integral Nonlinearity				±1/2	LSB
Differential Nonlinearity	Guaranteed Monotonic			±1/4	LSB
Signal to Noise Ratio			49		dB
Throughput Rate				100	ksps
Input Voltage Range		0		VREF	V
COMPARATORS					
Response Time	CP+ - CP- = 100mV		4		μs
Input Voltage Range		-0.25		VDD + 0.25	V
Input Bias Current		-5	0.001	+5	nA
Input Offset Voltage		-10		+10	mV

PACKAGE INFORMATION



	MIN (mm)	NOM (mm)	MAX (mm)
A	-	-	1.20
A1	0.05	-	0.15
A2	0.95	1.00	1.05
b	0.17	0.22	0.27
D	-	9.00	-
D1	-	7.00	-
e	-	0.50	-
E	-	9.00	-
E1	-	7.00	-

