# DEMO MANUAL DC2754A

### LTC4331 I<sup>2</sup>C Device Extender Over Rugged Differential Link

### DESCRIPTION

Demonstration circuit 2754A extends an I<sup>2</sup>C based QuikEval<sup>™</sup> interface to up to 450 feet and features the LTC<sup>®</sup>4331. In order to realize the QuikEval interface, this demo circuit includes two complete I<sup>2</sup>C extenders and a remote side power supply formed from an LT<sup>®</sup>3461 boost converter and two LT1761 LDOs.

The LTC4331 is a SMBus compatible  $I^2C$  slave device extender designed for operation in high noise industrial environments. The  $I^2C/SMBus$  is extended over a single twisted pair by a ±60V fault protected differential transceiver. The LTC4331's extended 15V common mode voltage range allows it to bridge across different ground potentials.

When used as a QuikEval extender, the DC2754A requires the use of the DC2026 USB serial adapter. The DC2754A is NOT compatible with the DC590 USB serial adapter because the DC590 does not support clock stretching.

The DC2754A's jumpers and turrets allow easy re-configuration for use in other applications and/or demonstrating other application circuits. The circuit board is fabricated with a score between the local and remote sides to allow physical separation. The link cable connections are made using either pluggable terminal blocks or an Ethernet cable.

Two complete I<sup>2</sup>C extenders are present. The first extender has a full set of diagnostic LEDs, jumpers, turrets and large (0805) resistors for simple user reconfiguration and connection for customer specific application(s). The second extender is configured specifically for the QuikEval EEPROM with no LEDs; however, it may be reconfigured and used for customer specific application(s) via 0603 jumpers and resistors and unpopulated test points on 0.1" centers.

The LTC4331 data sheet gives a complete description of the part, its operation and application information. The data sheet should be referred to when reading this demo manual. The LTC4331 is available in a 20-lead QFN package and three temperature grades (commercial 0°C to 70°C, industrial –40°C to 85°C, and high temperature –40°C to 125°C).

#### Design files for this circuit board are available.

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### **DC2754A CONNECTION DIAGRAM**

### **PERFORMANCE SUMMARY** Specifications are at $T_A = 25^{\circ}C$

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNITS
V <sub>IN</sub>	Input Supply Range		2.6		5.5	V
I <sub>IN</sub>	Input Supply Current (V <sub>CC</sub> + V <sub>L</sub> )	$V_{CC} = V_L = V_{L2} = 5V, I_{(VL2)} = 15mA$		70		mA

## **QUICK START PROCEDURE**

Demonstration circuit 2754A is easy to set up to evaluate the performance of the LTC4331 using a DC2026 USB serial controller and an  $I^2C$  interfaced demo circuit. Refer to Figure 1 for proper equipment setup and follow the procedure below:

- 1. Connect the DC2754A's QuikEval input, J3 to a DC2026 USB serial controller using the 14-conductor ribbon cable supplied with the DC2026.
- 2. Connect the DC2754A's local side interface to its remote side interface using the supplied Ethernet cable to connect between J4 and J6.
- Connect the DC2754A's QuikEval output, J5, to the remote demo circuit using the suppled 14-pin ribbon cable. The DC2754A is compatible with most demo circuits whose feature part utilizes an I<sup>2</sup>C interface.

- 4. Set the V<sub>L2</sub> adjust jumper JP30 to the proper interface voltage for the remote demo circuit. Refer to the remote demo circuit's documentation for information on acceptable voltage levels. V<sub>L2</sub> is the low voltage supply for the attached demo circuit and it also sets the logic level for the remote-side I<sup>2</sup>C signals.
- 5. Following the remote demo circuit's documentation, make any remaining connections required by the remote demo circuit.
- 6. Connect the DC2026 to a host PC with a standard USB A/B cable. While the DC2754A will work with any of the available  $V_{CCIO}$  voltages from the DC2026, 5V is recommended to maximize the available power for the remote demo circuit.
- 7. Run QuikEval on the host PC and the remote demo circuit's control panel will appear. Refer to the remote demo circuit's documentation for information on using the control panel.

# DC2754A JUMPERS AND SWITCHES

As shipped, the jumpers and switches on the DC2754A are set to enable operation as a QuikEval extender with power provided by the DC2026.

See the LTC4331 data sheet Pin Functions for a complete description of the LTC4331 control pin functions.

JP1: Local Side Ready LED Enable. Default ON.

JP2: Local Side Link LED Enable. Default ON.

JP3: Local Side V<sub>CC</sub> LED Enable. Default ON.

JP4: Local Side ON Input. Default ON.

JP5: Remote Side Ready LED Enable. Default OFF.

JP6: Remote Side  $V_{CC2}$  LED Enable. Default OFF.

JP7: Remote Side Link LED Enable. Default OFF.

JP8: Remote Side ON Input. Default ON.

JP9: Remote Side SCL 5.1k Pull-Up Enable. Default ON.

JP10: Remote Side SDA 5.1k Pull-Up Enable. Default ON.

**JP11:** Local Side SDA 5.1k Pull-Up Enable. Default ON.

JP12: Local Side SCL 5.1k Pull-Up Enable. Default ON.

**JP13:** Local Side VL1 Supply Selection. Connect to either  $V_{CC}$  to use  $V_{CC}$  or VL1 to use VL1 turret. Default  $V_{CC}$ .

**JP14:** Remote Side REMOTE Input. Configures LTC4331 to be either a local or remote device. Default 1.

**JP15:** Local Side REMOTE Input. Configures LTC4331 to be either a local or remote device. Default 0.

JP16: Reserved.

JP17: Reserved.

**JP18:** Remote Side A1 Input. Configures internal I<sup>2</sup>C device address. Default Z.

**JP19:** Local Side A1 Input. Configures internal I<sup>2</sup>C device address. Default Z.

**JP20:** Remote Side A2 Input. Configures internal I<sup>2</sup>C device address. Default Z.

**JP21:** Local Side A2 Input. Configures internal I<sup>2</sup>C device address. Default Z.

**JP22:** Remote Side SPEED1 Input. Configures I<sup>2</sup>C bus timing and link baud rate. Default 0.

**JP23:** Local Side SPEED1 Input. Configures  $I^2C$  bus timing and link baud rate. Default 0.

**JP24:** Remote Side SPEED2 Input. Configures  $I^2C$  bus timing and link baud rate. Default 1.

**JP25:** Local Side SPEED2 Input. Configures I<sup>2</sup>C bus timing and link baud rate. Default 1.

**JP26:** Remote Side <u>SLO</u> Input. Limits link transmitter slew rate which also limits maximum link baud rate. Default 1.

**JP27:** Local Side SLO Input. Limits link transmitter slew rate which also limits maximum link baud rate. Default 1.

JP28: Reserved.

JP29: Reserved.

**JP30:** Remote V<sub>L2</sub> Voltage Adjust Selector. Select V<sub>L2</sub> voltage for 5V, 3.3V, 2.5V or EXT for external V<sub>L2</sub> voltage. Use EXT to disable on-board switching supply. Default 5V.

**S1:**  $V_{CC2}$  Local/Remote Selection. When using an Ethernet cable to connect the local and remote sides, this switch selects the source location for  $V_{CC2}$ . Select Remote to connect  $V_{CC}$  to  $V_{CC2}$ . Select Local for no connection between  $V_{CC}$  and  $V_{CC2}$ . S1, S2 and S3 should all be in the same position. Default Remote.

**S2:**  $V_{CC2E}$  Local/Remote Selection. When using an Ethernet cable to connect the local and remote sides, this switch selects the source location for  $V_{CC2E}$ . Select Remote to connect  $V_{CCE}$  to  $V_{CC2E}$ . Select Local for no connection between  $V_{CCE}$  and  $V_{CC2E}$ . S1, S2 and S3 should all be in the same position. Default Remote.

**S3:** GROUND Local/Remote Selection. When using an Ethernet cable to connect the local and remote sides, this switch selects if the local and remote grounds are connected. Select Remote to connect the grounds and Local to disconnect the grounds. S1, S2 and S3 should all be in the same position. Default Remote.

## DC2754A FEATURES

#### SEPARABLE LOCAL AND REMOTE SIDES

The DC2754A comes as a single printed circuit board with the local and remote side circuits located on either side of a score. Breaking this score will allow the circuits to be physically separated. *Note that the scored edges are sharp after separation and they should be de-burred before handling.* 

#### QuikEval INTERFACE

The QuikEval interface consists of two data channels plus power and ground. One data channel uses I<sup>2</sup>C to interface to the EEPROM which stores identification information for the connected demonstration circuit. The second data channel is used by the DC2026 to communicate with the demonstration circuit. Depending on the demonstration circuit, this data channel may be I<sup>2</sup>C or SPI based. A limited number of demo circuits use both SPI and I<sup>2</sup>C interfaces. The DC2754A is only compatible with demonstration circuits which use an I<sup>2</sup>C data channel, see the Demo Circuit Compatibility section for more details.

### DATA I<sup>2</sup>C CHANNEL

The DC2754A's data channel is the primary  $I^2C$  extender channel. The QuikEval interface extender uses this channel once the remote demo circuit's EEPROM has been read via the auxiliary  $I^2C$  channel.

When not using the DC2754A as a QuikEval interface extender, this channel may be easily configured for other applications via its jumpers and 0.1" pin headers.

#### AUXILIARY I<sup>2</sup>C CHANNEL

The DC2754A includes a complete second  $I^2C$  extender channel to access the remote demo circuit's EEPROM. The QuikEval interface uses this dedicated  $I^2C$  channel to identify the connected demo circuit. Once the host PC has read the EEPROM's contents, this channel is not used (unless the host PC re-reads the EEPROM to confirm the demo circuit is still connected). When not using the DC2754A as a QuikEval interface extender, this 2nd extender channel is available for other uses, such as a two-spoke star network or a multi-tier network.

#### **PIN HEADER CONNECTIONS**

All control and I/O pins for the four LTC4331 extenders are available on 0.1" center pin headers.

The data channel's headers are populated and include jumpers to  $V_L$  or GND for configuring the input pins. A small series resistor protects external circuitry from driving into a connected jumper.

The auxiliary channel's headers are unpopulated and the LTC4331 input configuration is set for the QuikEval application via  $0\Omega$  pull-up or pull-down resistors. Note that no series protection resistors are present in the auxiliary channel.

### LOCAL – REMOTE LINK CONNECTION

The DC2754A provides two options for connecting the local and remote sides. A standard straight through Ethernet cable connects power, ground and both the data and auxiliary channels in a single cable. Pluggable terminal blocks allow the use of different cable types as well as access to  $V_{CC}$  and GND for incorporating modified bias or protective networks into RS-485 bus link and/or separating the power and ground connections between the local and remote sides.

Switches S1 - S3 allow the power and ground connections from the Ethernet cable to be broken at the remote side which allows the remote side to be separately powered and/or a ground potential difference. For most applications, all three switches should be in the same position.

The DC2754A includes biasing networks on the local and remote sides for the data and auxiliary channels. This network consists of  $619\Omega$  pull-up and pull-down resistors and  $120\Omega$  terminating resistors and should be acceptable for most nonisolated applications. Using the pluggable terminal blocks, additional parallel resistors may be easily added to reduce the network's effective resistor values as needed.

# DC2754A FEATURES

### DC POWER SUPPLY

The remote side of the extender interface may be powered from the local side if V<sub>CC</sub> and GND are included in the connecting cable(s). In order to protect the remote side from a resonant overvoltage when hot plugging a long cable, the V<sub>CC2</sub> supply includes an electrolytic capacitor to provide damping and a Zener clamp diode to limit any overvoltage. Depending on the cable's length and conductor size and the remote side load current, an external supply may be required even though V<sub>CC</sub> and GND are connected.

The power supply configuration for the data channel of the DC2754A can be configured by jumper settings. This allows for the same or separate  $V_{CC}$  and  $V_L$  supplies.

The auxiliary channel's power supplies are connected via  $0\Omega$  resistors to the corresponding data channel supply (or QuikEval interface). The supplies may be separated between the two interfaces by removing R38 and R29. Overvoltage protection for the remote side's auxiliary channel's  $V_{CC}$  ( $V_{CC2E}$ ) is present only when R29 is connected.

To fully support the QuikEval interface, the DC2754A includes an LT3461 switching power converter on the remote side to power two LT1761 LD0 regulators for the +7V and V<sub>L2</sub> supplies. The V<sub>L2</sub> supply's output is selectable by JP30 to be 5V, 3.3V or 2.5V. To disable the onboard supplies, set jumper JP30 to EXT and provide V<sub>L2</sub> and +7V (if required by the remote demo circuit) at the turret(s).

When using the DC2754A as a QuikEval interface extender, the V<sub>L</sub> supply is provided by the DC2026 which is limited to approximately 100mA. If V<sub>L</sub> and V<sub>CC</sub> are connected by JP13 and switches S1 – S3 are in the local position, then the DC2026 will power both the DC2754A and the remote demo circuit. For maximum available power to the DC2754A, the DC2026's V<sub>CCI0</sub> jumper should be set to 5V. Under these conditions, there is approximately 15mA available to power the remote demo circuit. This is sufficient for many, but not all demo circuits. The available

output current may be increased by using an external 5V, 400mA supply connected to either  $V_{CC}$  or  $V_{CC2}$ . If the cable between the local and remote sides is long, then the external supply should be connected to  $V_{CC2}$ . When using an external supply, the upstream supply needs to be isolated or disabled.

### DEMO CIRCUIT COMPATIBILITY

The DC2754A is compatible with the demo circuits which only use the  $I^2C$  interface to communicate with the feature part. Some demo circuits use one or more digital signal line(s) (pins 5, 6 and/or 14 of the QuikEval connector interface) in addition to the  $I^2C$  interface and in many cases, the DC2754A is compatible with these demo circuits. Some demo circuits use a combination of  $I^2C$ and SPI interfaces and the DC2754A is incompatible with these demo circuits.

Most demo circuits with additional signal lines only use pin 6 as a demo circuit input. A limited number of demo circuits use pin 5 as a demo circuit alert output. The DC2754A is compatible with these demo circuits because it uses the LTC4331's CTRL signal to pass-through pin 6 from the DC2026 to the remote demo circuit and the LTC4331's ALERT signal to pass-through pin 5 from the remote demo circuit to the DC2026.

The DC2754A does not pass-through pin 14 of the QuikEval interface. Therefore, the DC2754A will have limited to no compatibility with demo circuits which use this pin.

The DC2754A is incompatible with the following I<sup>2</sup>C demo circuits: DC726-B, DC812, DC935, DC936, DC1187, DC1815-A/B/C/D, DC1808-A/B, DC1840 and DC1843.

The DC2754A is not compatible with demo circuits which use a SPI interface to communicate with the feature part.

The DC2754A is not compatible with the DC590 because the DC590 does not support clock stretching. The LTC4331 uses clock stretching to accommodate the communications transit and overhead times of the extender link.



#### ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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