Multilayer Ceramic Chip Capacitors

CKCM25X5R0J104M060AA



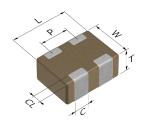






TDK item description CKCM25X5R0J104MT****

Applications	Commercial Grade		
Feature	2-Array 2 in 1 Array		
Series	CKCM25		
Status	↑ Obsolete		
	Discontinue Issue Date : Apr.28, 2018		
	Last Purchase Order Date : Oct.31, 2018		
	Last Shipment Date : Mar.31, 2019		



	Size	
Length(L)	1.37mm ±0.15mm	
Width(W)	1.00mm ±0.15mm	
Thickness(T)	0.60mm ±0.10mm	
Terminal Width(C)	0.36mm Nom.	
Terminal Spacing(P)	0.64mm Nom.	
Recommended Land Pattern (PA)	0.45mm to 0.55mm	
Recommended Land Pattern (PB)	0.45mm to 0.55mm	
Recommended Land Pattern (PC)	0.31mm to 0.41mm	
Recommended Land Pattern (PP)	0.59mm to 0.69mm	

Electrical Characteristics		
Capacitance	100nF ±20%	
Rated Voltage	6.3VDC	
Temperature Characteristic	X5R(±15%)	
Dissipation Factor (Max.)	5%	
Insulation Resistance (Min.)	1000ΜΩ	

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

[!] Images are for reference only and show exemplary products.

[!] This PDF document was created based on the data listed on the TDK Corporation website.

[!] All specifications are subject to change without notice.

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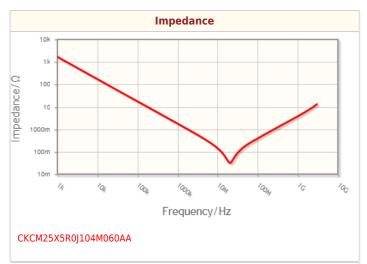


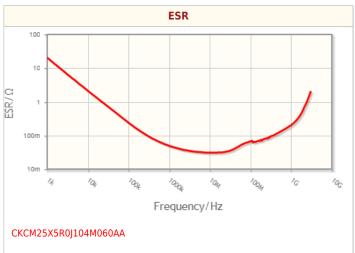


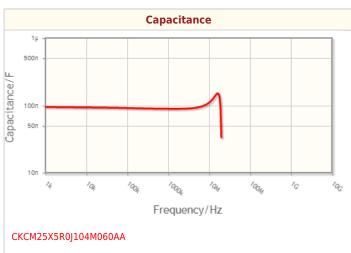


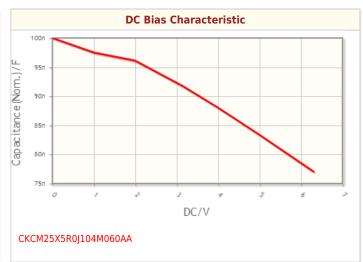


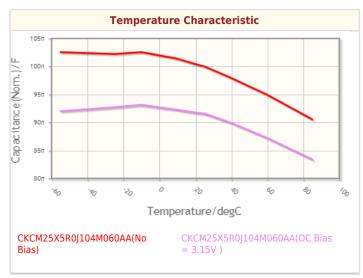
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

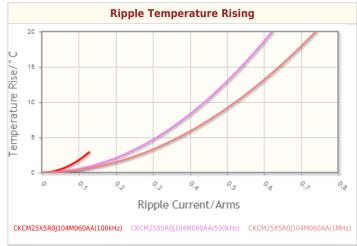












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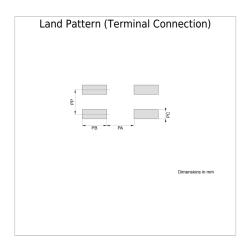








Associated Images



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