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EVAL-SDP-I-FMC

EVAL-SDP-I-FMC



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SDP-I-FMC

SDP-I-FMC Interposer

Overview

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Product Details

The SDP-I-FMC enables easy connection of a wide variety of FPGA evaluation boards to the range of daughter boards from ADI's System Demonstration Platform (SDP) family. The SDP-I-FMC has a Low Pin Count (LPC) FMC connector and a 120 pin connector to connect to SDP daughter boards. The SDP-I-FMC can only be used with 3.3VIO FPGA evaluation boards.

Example projects for prototyping with an FPGA evaluation board, the SDP-I-FMC, and an SDP daughter evaluation board can be found on the ADI [wiki](#).

The System Demonstration Platform is a collection of controller boards, interposer boards, and daughter boards, used for easy, low cost evaluation of ADI components and reference circuits. For a general overview of the entire platform visit the... [Show More..](#)

Documentation

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3 Evaluation Documentation

1 Videos

SDP Facilitates Quick Prototyping and Evaluation

Read our Analog Dialog Article for more information on the SDP

[SDP Wiki](#)

WIKI

[SDP-I-FMC Schematic](#)

WIKI

System Demonstration Platform Simplifies Evaluation

The System Demonstration Platform (SDP) is a low-cost, re-usable and versatile evaluation system. It is comprised of...

Software

1 See All

1 Evaluation Software

[Example Project for SDP daughter boards on ADI wiki](#)

WIKI

Buy

Evaluation Boards

Pricing displayed is based on 1-piece.

Model	Description	Price	RoHS
SDP-FMC-IB1Z Production	SDP to FMC Interposer Board		Yes

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Problem Solvers

4,700+

Patents Worldwide

125,000

Customers

50+

Years

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ADI enables our customers to interpret the world around us by intelligently bridging the physical and digital with unmatched technologies that sense, measure and connect. We collaborate with our customers to accelerate the pace of innovation and

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create breakthrough solutions that are ahead of what's possible.

See the Innovations

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