

## DESCRIPTION

Demonstration circuit 1321 allows a logic analyzer to be directly connected to a DC890 compatible ADC demo circuit for capturing data. DC1321A-B has 100Ω termination resistors installed, for use with LVDS output ADCs and logic analyzer inputs that do not have termination. DC1321A-A has no termination resistors installed, for use with CMOS

output ADCs or with LVDS output ADCs, should the logic analyzer have built-in termination.

Design files for this circuit board are available at <http://www.linear.com/demo/DC1321>

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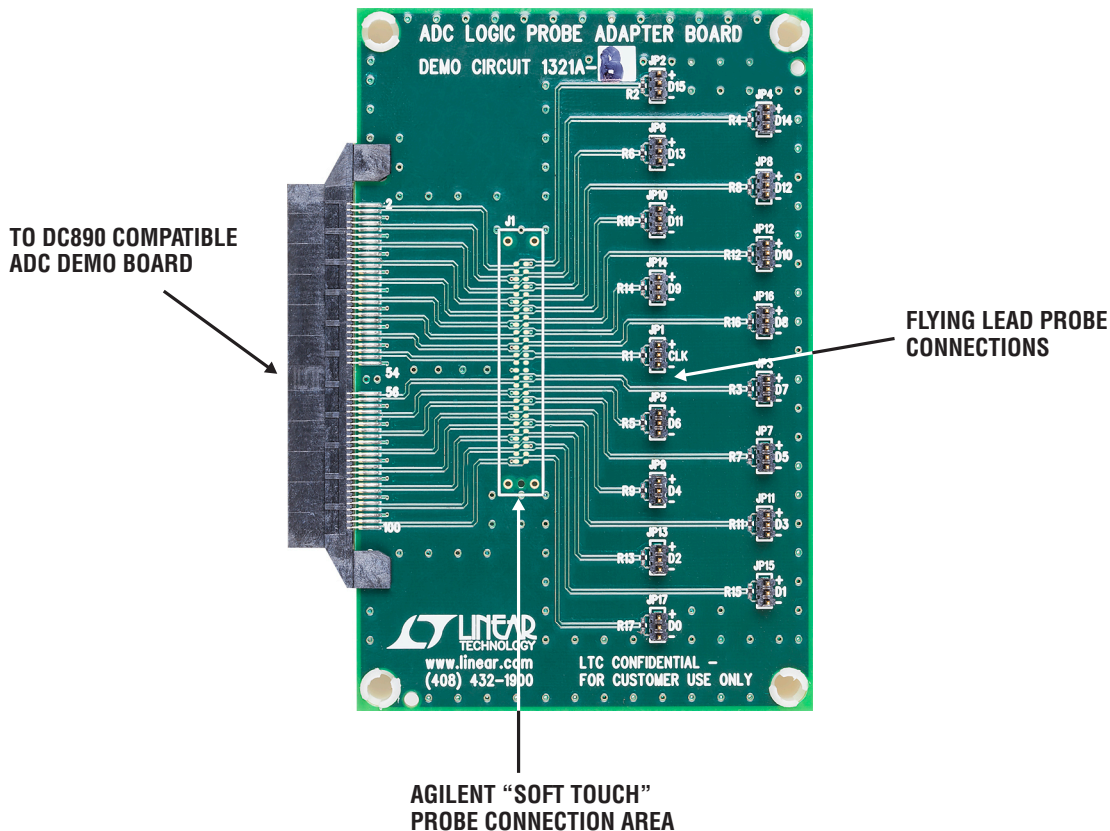


Figure 1. DC1321

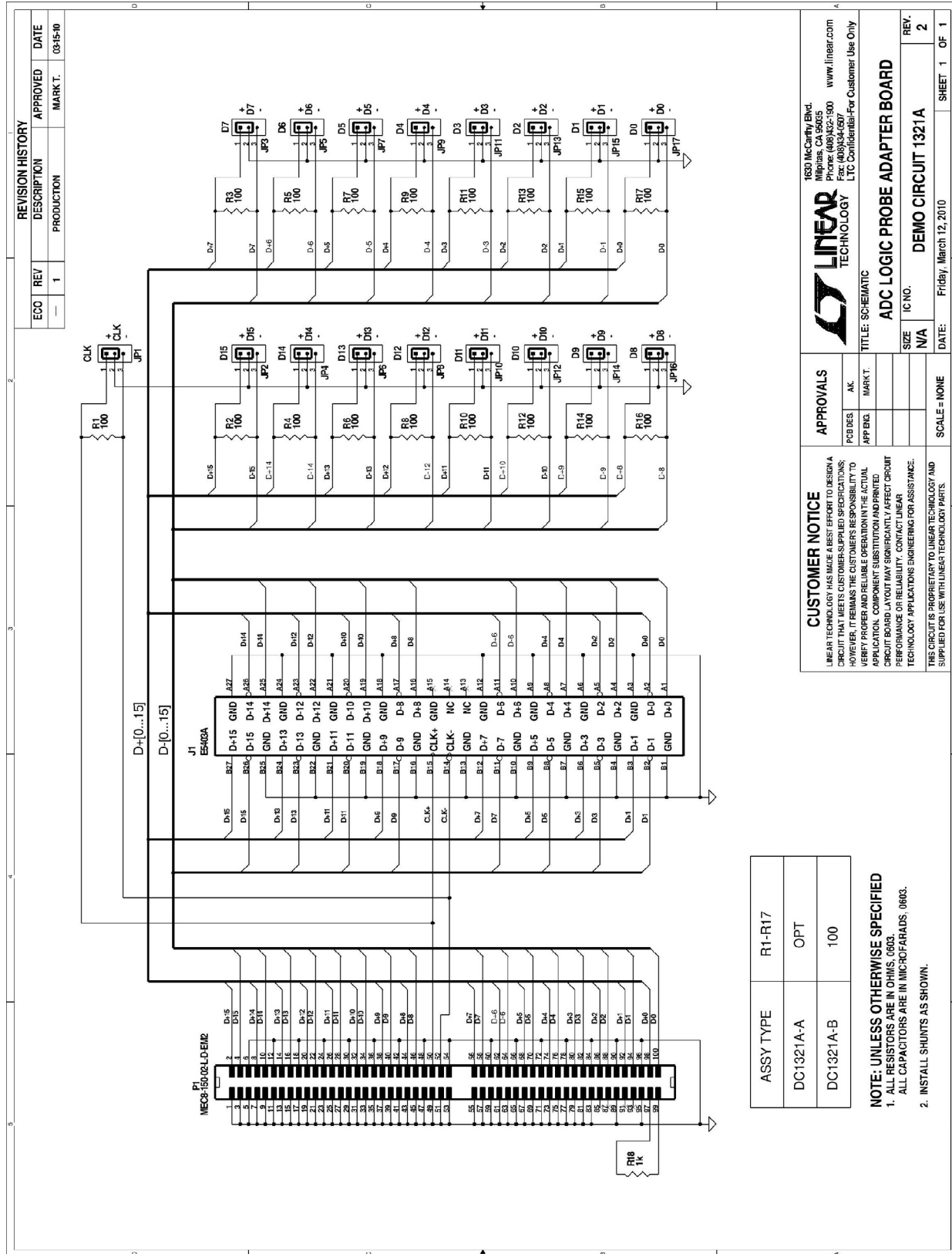
# DEMO MANUAL DC1321

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## PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
1	1	P1	SOCKET, 0.8MM DUAL EDGE MOUNT	SAMTEC, MEC8-150-02-L-D-EM2 (PbF)
2	17	JP1-JP17	HEADER, 3x1 .050" PIN SPACING	SAMTEC, HTMS-103-02-S-S (PbF)
3	1	R18	RES., CHIP 1k 5% 0402	VISHAY, CRCW04021K00JNED
4	4	FOR 4 CORNERS	STAND-OFF, NYLON (SNAP ON), 0.625" TALL	KEYSTONE, 8834 (PbF)
<b>DC1321A-A</b>				
5	0	R1-R17	OPT	
<b>DC1321A-B</b>				
5	17	R1-R17	RES., CHIP 100Ω 5% 0402	VISHAY, CRCW0402100RJNED

**SCHEMATIC DIAGRAM**



REVISION HISTORY		
ECO	REV	DESCRIPTION
—	1	PRODUCTION

APPROVED	DATE
MARKT.	03-15-00

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**LINEAR TECHNOLOGY**

TITLE: SCHEMATIC  
ADC LOGIC PROBE ADAPTER BOARD

IC NO. DEMO CIRCUIT 1321A  
REV. 2

DATE: Friday, March 12, 2010  
SHEET 1 OF 1

SCALE = NONE

**CUSTOMER NOTICE**  
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**APPROVALS**

PCB DES	AK	MARKT.
APP ENG		

ASSY TYPE	R1-R17
DC1321A-A	OPT
DC1321A-B	100

**NOTE: UNLESS OTHERWISE SPECIFIED**

- ALL RESISTORS ARE IN OHMS, 0603.
- ALL CAPACITORS ARE IN MICROFARADS, 0603.
- INSTALL SHUNTS AS SHOWN.

# DEMO MANUAL DC1321

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## DEMONSTRATION BOARD IMPORTANT NOTICE

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This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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