AD9125 Evaluation Board

In addition to the AD9125, the evaluation boards also come with clocking and analog quadrature modulator circuits to ease system level solutions. The AD9125 evaluation board comes in two models; the AD9125-M5372-EBZ has the ADL5372 modulator on board, and the AD9125-M5375-EBZ has the ADL5375 modulator on-board. To operate the evaluation boards, the user must apply power, a clock source, a digital data source, and be able to observe the DAC output on either a spectrum analyzer or an oscilloscope. A low jitter sine or square wave clock will work well as a source. The evaluation board comes with software which allows the user to program the SPI port. Via the SPI port, the DUT and clock circuitry can be programmed into any of its various operating modes.

Documentation and software updates for using High-Speed DAC Evaluation Boards are included in individual, self-extracting update files.

Files included in the AD9125 Update:

- SPI Application
- DPGDownloader Panel
- Quick Start Guide
- AD9125 Data Sheet
- IBIS Model
- Schematics AD9125-M5372-EBZ RevB, AD9125-M5375-EBZ RevC
- Bill of Materials AD9125-M5372-EBZ RevBAD9125-M5375-EBZ RevC
- PCB Gerber files AD9125-M5375-EBZ RevC
- PCB BRD file AD9125-M5372-EBZ RevBAD9125-M5375-EBZ RevC
- PCB Layout PDF AD9125-M5375-EBZ RevC

Download the AD9125 Update

Data Pattern Generator

The Data Pattern Generator is a bench-top instrument for driving vectors into Analog Devices' high-speed Digital-to-Analog converters. The DPG connects to a PC over USB, and allows a user to download a vector from their PC into the DPG's internal memory. Once downloaded, the vector can be played out to an attached Evaluation Board for a specific DAC at full speed. This allows for rapid evaluation of the DAC with both generic and custom-generated test data.

For more information on the DPG line of pattern generators and software:
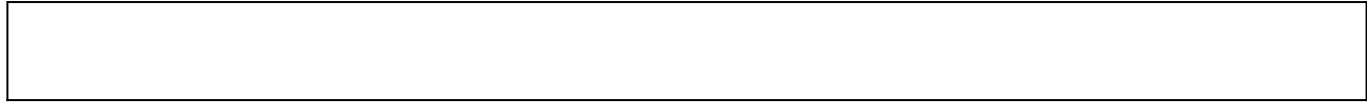
- DAC Software Suite
- DPG1 (obsolete)
- DPG2
- DPG3

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