

Multilayer Ceramic Chip Capacitors

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FAQ

CGA6N3X7R2A225K230AB



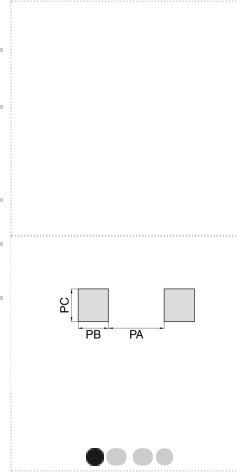
TDK item description ? CGA6N3X7R2A225KT****

Applications Automotive Grade

Feature Mid Mid Voltage (100 to 630V)
AEC-Q200 AEC-Q200

Series CGA6(3225) [EIA 1210]

Status Production



Images are for reference only and show exemplary products.

PDF file of this page

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Documents

- Catalog
- Specification
- RoHS Certificate
- SVHC/REACH Certificate
- Sample Kits
- Characterization Sheet

Technical Support Tools

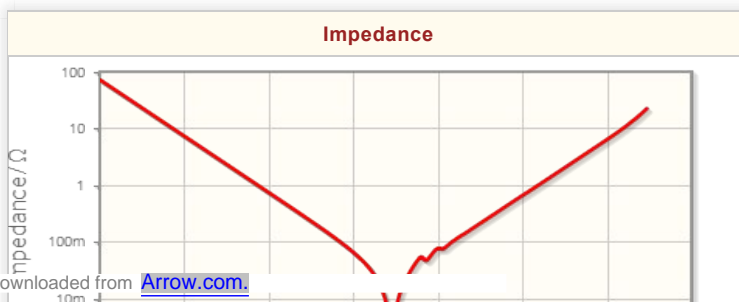
- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias/Superimposition Model (HSPICE)

Size	
Length(L)	3.20mm ±0.40mm
Width(W)	2.50mm ±0.30mm
Thickness(T)	2.30mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	2.00mm to 2.40mm
Recommended Land Pattern (PB)	1.00mm to 1.20mm
Recommended Land Pattern (PC)	1.90mm to 2.50mm

Electrical Characteristics	
Capacitance	2.2µF ±10%
Rated Voltage	100VDC
Temperature Characteristic ?	X7R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	227MΩ

Other	
Soldering Method	Reflow
AEC-Q200	Yes
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	1000pcs

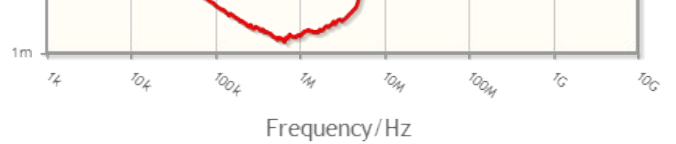
Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)





CGA6N3X7R2A225K230AB

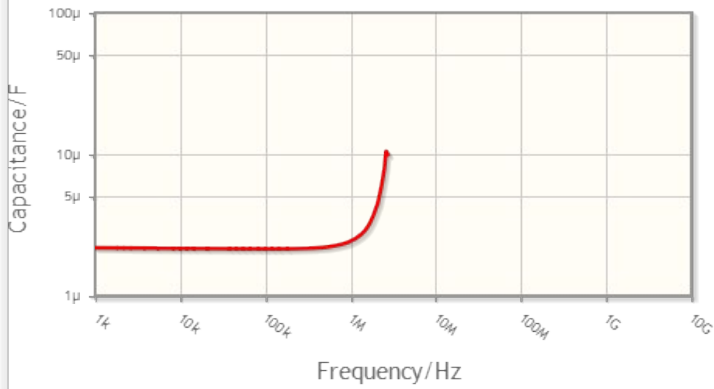
Change settings



CGA6N3X7R2A225K230AB

Change settings

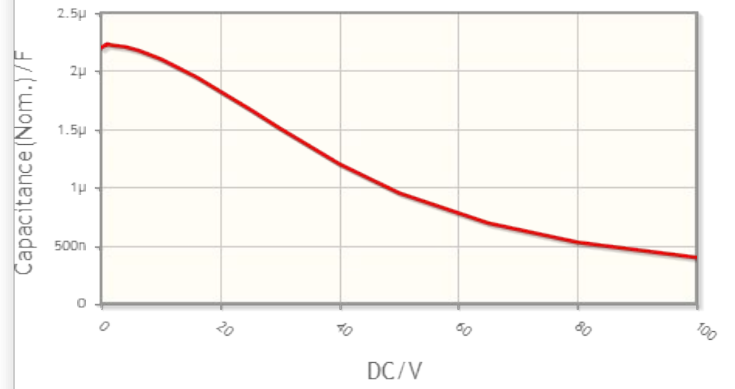
Capacitance



CGA6N3X7R2A225K230AB

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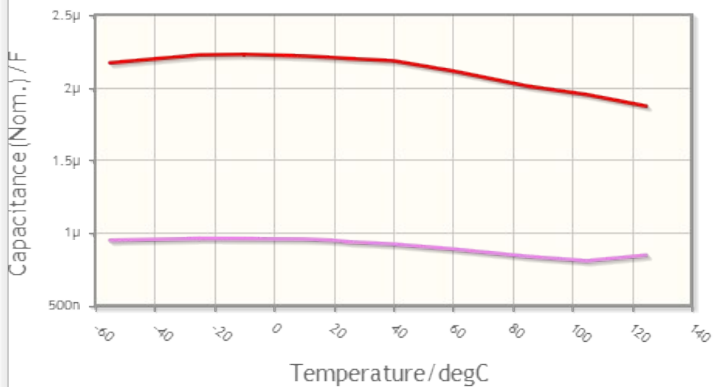
DC Bias Characteristic



CGA6N3X7R2A225K230AB

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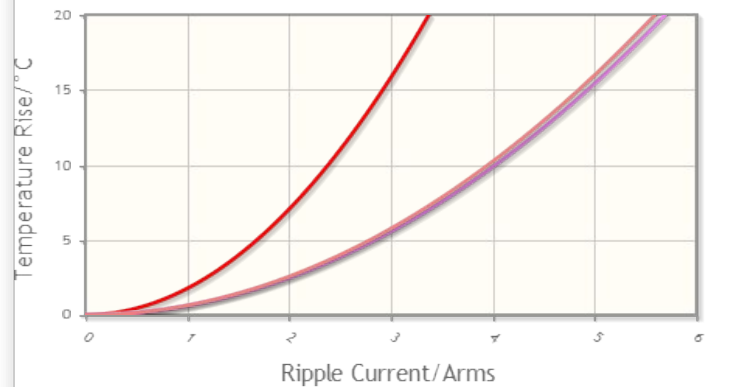
Temperature Characteristic



CGA6N3X7R2A225K230AB(No Bias) CGA6N3X7R2A225K230AB(DC Bias s = 50V)

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Ripple Temperature Rising



CGA6N3X7R2A225K230AB(10kHz) CGA6N3X7R2A225K230AB(50kHz) CGA6N3X7R2A225K230AB(1MHz)

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