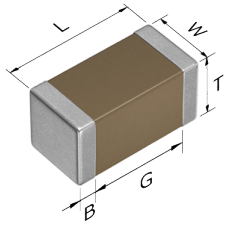
Creation Date : December 31, 2021 (GMT)

Multilayer Ceramic Chip Capacitors

C1608X5R1V475K080AC

RoHS Reach Halogen Free Pb Free

TDK item description	C1608X5R1V475KT****
Applications	Commercial Grade
Feature	General (Up to 75V)
Series	C1608 [EIA 0603]
Status	Production



Size	
Length(L)	1.60mm ±0.20mm
Width(W)	0.80mm ±0.20mm
Thickness(T)	0.80mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	4.7µF ±10%
Rated Voltage	35VDC
Temperature Characteristic	X5R(±15%)
Dissipation Factor (Max.)	10%
Insulation Resistance (Min.)	106MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	NO
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

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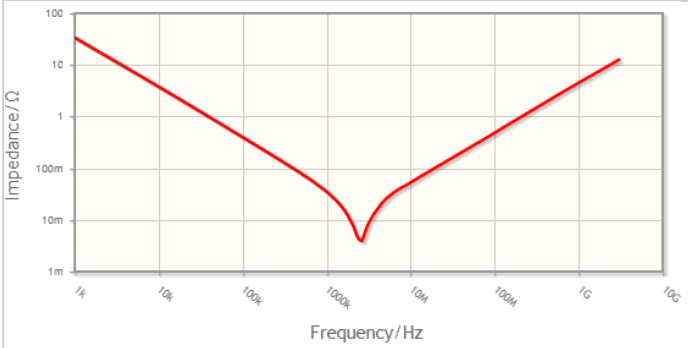
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



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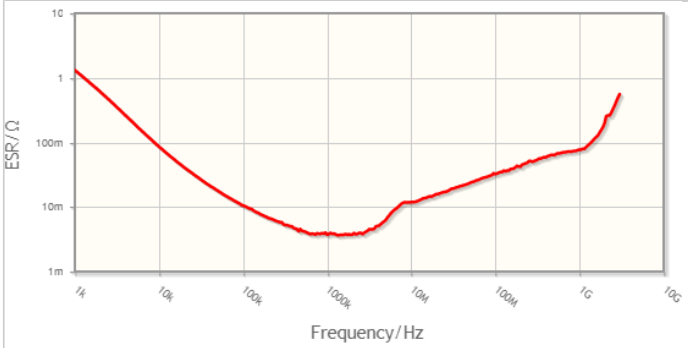
ESR



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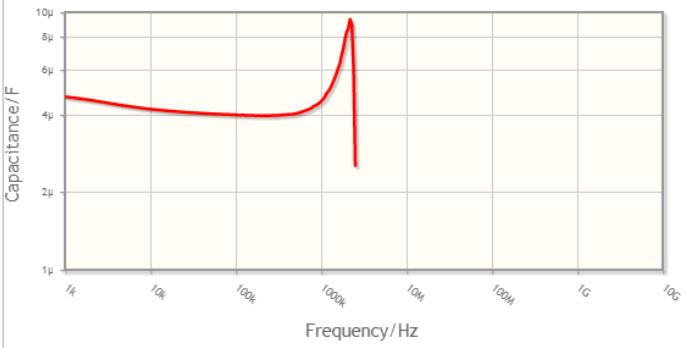
Capacitance



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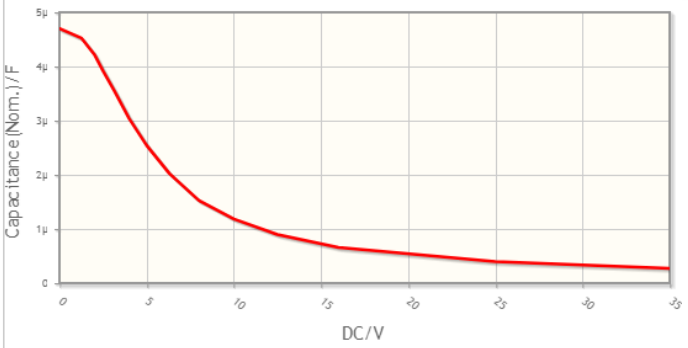
DC Bias Characteristic



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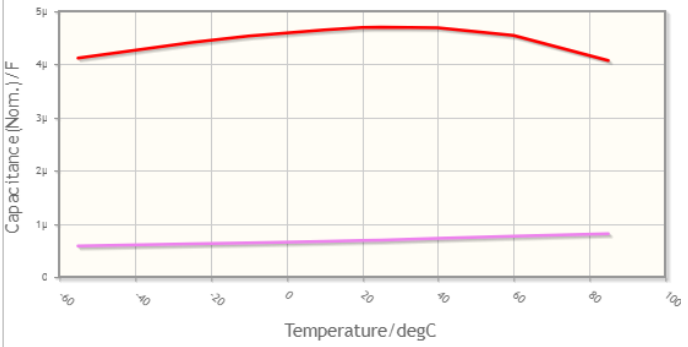
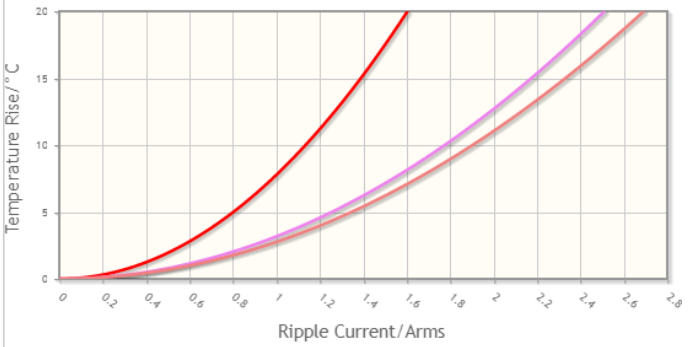
Temperature Characteristic



C1608X5R1V475K080AC(No Bias)
C1608X5R1V475K080AC(DC Bias = 17.5V)

Ripple Temperature Rising



C1608X5R1V475K080AC(100kHz) C1608X5R1V475K080AC(500kHz) C1608X5R1V475K080AC(1MHz)

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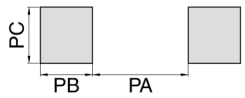
Associated Images

Land Pattern (Terminal Connection)



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