Note: This datasheet may be out of date

Please download the latest datasheet of LDB18869M10G-120 from the official website of Murata Manufacturing Co., Ltd.

https://www.murata.com/en-us/products/productdetail?partno=LDB18869M10G-120

LDB18869M10G-120





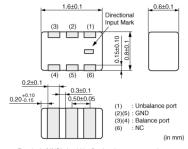






Appearance & Shape





*Terminal of "NC" should be fixed to the no connected pattern. All the technical data and Information contained herein are subject to change without prior notice.



Chip type SMD baluns constructed with copper conductor and ceramic material.

Ideal for high-frequency applications.

Small-size and low-loss baluns can be customized for the balance impedance of 50ohm to 200ohm.

- 1. Available in the 824MHz to 915MHz frequency range.
- 2. Impedance at balanced terminals is 100ohm.
- 3. Small, Low-profiled SMD.
- 4. Low loss.
- 5. Available in tape and reel packing for automatic mounting.



Packaging Information

Packaging	Specifications	Minimum Order Quantity
-	180mm Paper Tape	4000

1 of 3

Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering



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LDB18869M10G-120



Center Frequency	869.50MHz	
Frequency Range	824.00MHz to 915.00MHz	
Insertion Loss I)	1.00dB max. (at 25°C)	
Insertion Loss II)	1.10dB max. (-40 to +85°C)	
Unbalance Impedance (Nom.)	50Ω	
Balance Impedance (Differential) (Nom.)	100Ω	
Unbalance Port VSWR	2.00 max. (Balance Port:at 100ohm)	
Power Capacity	0.5W	
Operating Temperature Range	-40°C to 85°C	
L x W (size)	1.60x0.80mm	
Thickness(max.)	0.7mm	

2 of 3

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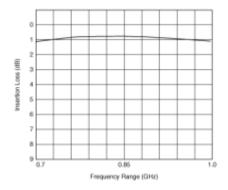
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LDB18869M10G-120



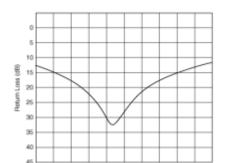


b С

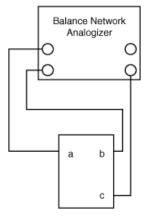
Balance Network Analogizer

> a : unbalance port b, c : balance port

Insertion Loss Characteristics



Measurement Circuit of Insertion Loss



a : unbalance port b, c : balance port

Characteristics of Unbalance Port VSWR

Frequency Range (GHz)

Measurement Circuit of Unbalance Port VSWR

3 of 3

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Last updated :2019/06/15