

# Multilayer Ceramic Chip Capacitors

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Cross Reference

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Technical Support Tool

FAQ

CGA2B2X8R1E682K050BA



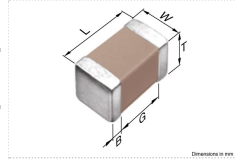
**TDK item description** ? CGA2B2X8R1E682KT\*\*\*\*

**Applications** Automotive Grade

**Feature** 150°C High Temperature Application  
AEC-Q200 AEC-Q200

**Series** CGA2(1005) [EIA 0402]

**Status** Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

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### Documents

- Catalog
- Specification
- RoHS Certificate
- SVHC/REACH Certificate
- Characterization Sheet
- Promotion Video
- Sample Kits

### Technical Support Tools

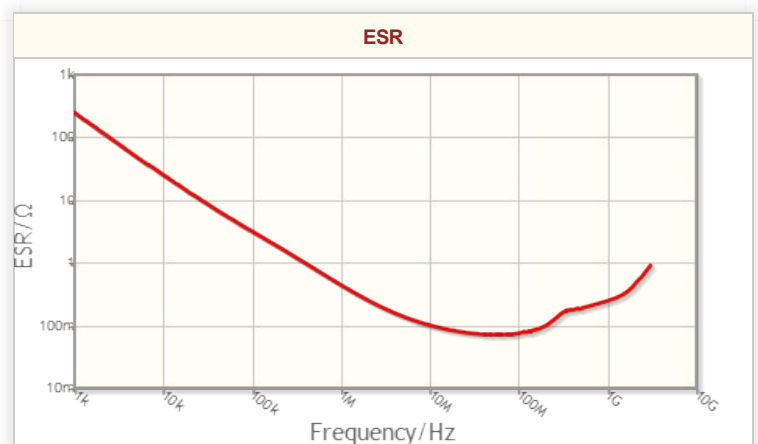
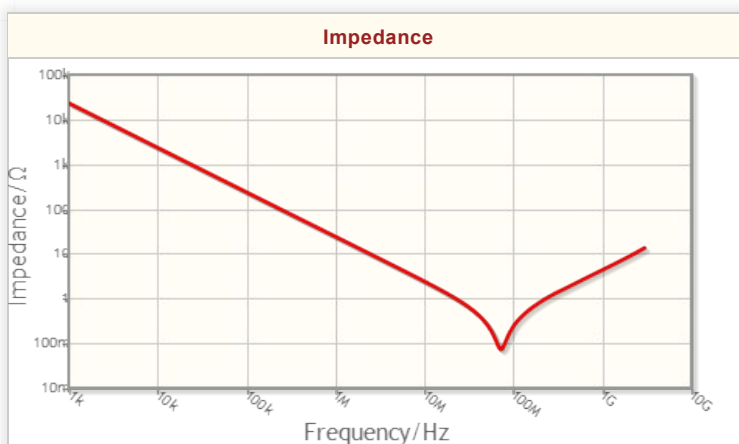
- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias/Superimposition Model (HSPICE)

Size	
Length(L)	1.00mm ±0.05mm
Width(W)	0.50mm ±0.05mm
Thickness(T)	0.50mm ±0.05mm
Terminal Width(B)	0.10mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.30mm to 0.50mm
Recommended Land Pattern (PB)	0.35mm to 0.45mm
Recommended Land Pattern (PC)	0.40mm to 0.60mm

Electrical Characteristics	
Capacitance	6.8nF ±10%
Rated Voltage	25VDC
Temperature Characteristic ?	X8R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	10000MΩ

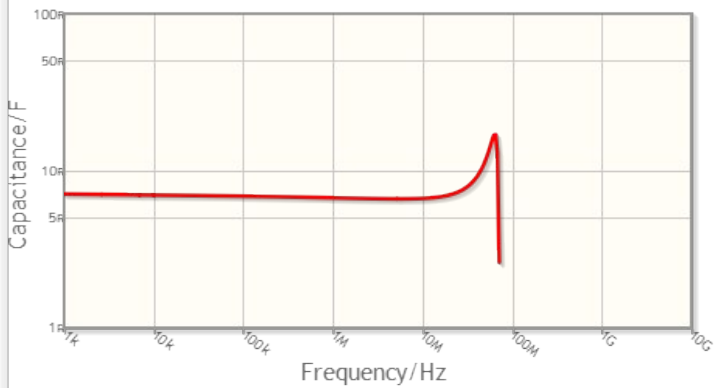
Other	
Soldering Method	Reflow
AEC-Q200	Yes
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	10000pcs

## Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)



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### Capacitance

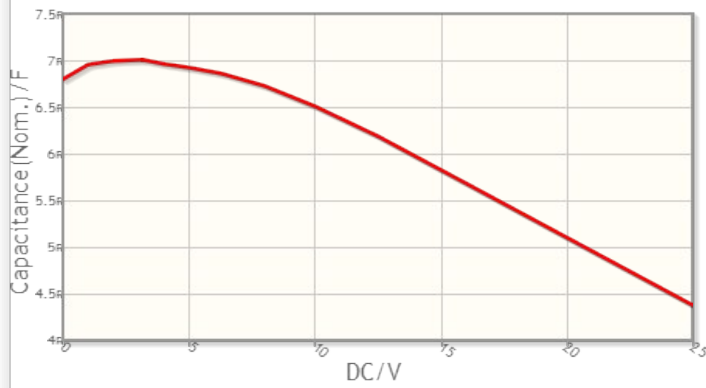


CGA2B2X8R1E682K050BA

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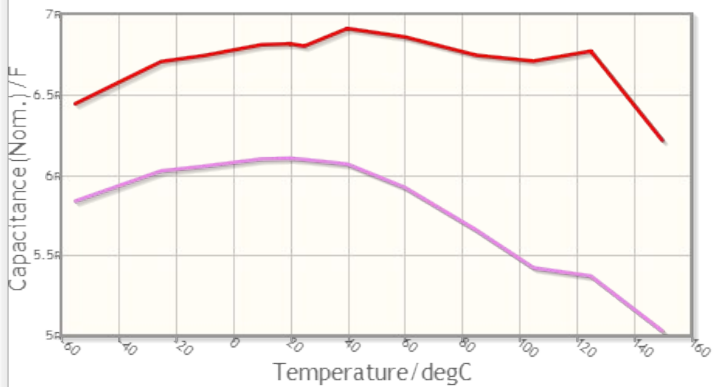
### DC Bias Characteristic



CGA2B2X8R1E682K050BA

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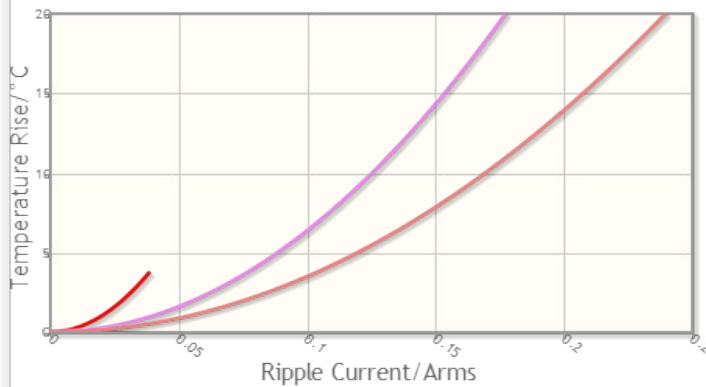
### Temperature Characteristic



CGA2B2X8R1E682K050BA( No Bias )    CGA2B2X8R1E682K050BA( DC Bias = 12.5V )

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### Ripple Temperature Rising



CGA2B2X8R1E682K050BA(10 kHz)    CGA2B2X8R1E682K050BA(50 kHz)    CGA2B2X8R1E682K050BA(1 MHz)

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