

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Sheet 4: TABLE I. Input offset voltage test. Device type 01. For subgroup 2, delete the min limit of "1.5 mV" and substitute "5 mV". Input bias current test. Device type 01. For subgroup 2, delete the min limit of "40 nA" and substitute "60 nA". Input offset current test. Device type 01. For subgroup 2, delete the min limit of "12 nA" and substitute "20 nA". Sheet 5: TABLE I. Common mode rejection ratio test. Device type 01. For subgroup 2, delete the min limit of "86 dB" and substitute "82 dB"; Power supply rejection ratio test. Device type 01. For subgroup 2, delete the min limit of "86 dB" and substitute "82 dB". Changes in accordance with notice of revision 5962-R012-96. - ro	96-01-30	M. A. FRYE
B	Drawing updated to reflect current requirements. Incorporate N.O.R. 5962-R012-96. Redrawn. - ro	05-03-09	R. MONNIN
C	Update boilerplate paragraphs to current MIL-PRF-38535 requirements. - ro	11-04-19	C. SAFFLE

THE ORIGINAL FIRST SHEET OF THIS DRAWING HAS BEEN REPLACED.

REV																				
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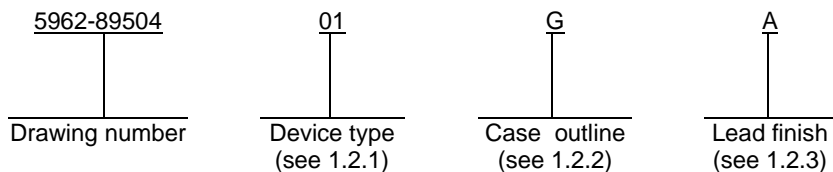
REV STATUS	REV	C	C	C	C	C	C	C	C	C	C	C				
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10					

PMIC N/A	PREPARED BY RICK OFFICER	<p align="center">DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.dsccl.dla.mil</p>													
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p>	CHECKED BY RAY MONNIN														
	APPROVED BY MICHAEL A. FRYE	<p align="center">MICROCIRCUIT, LINEAR, DUAL COMPARATOR, MONOLITHIC SILICON</p>													
	DRAWING APPROVAL DATE 89-05-02														
	AMSC N/A	REVISION LEVEL C	SIZE A	CAGE CODE 67268	5962-89504										
												SHEET 1 OF 10			

1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device type(s). The device type(s) identify the circuit function as follows:

Device type	Generic number	Circuit function	I _{IB} (T _A = 25°C)
01	LT1017	Dual comparator	15 nA
02	LT1018	Dual comparator	75 nA

1.2.2 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
G	MACY1-X8	8	Can
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

1.3 Absolute maximum ratings.

Positive supply voltage (V ₊)	+40 V dc
Negative supply voltage (V ₋)	-40 V dc
Input voltage range	-0.3 V dc to +40 V dc
Differential input voltage (V _{IN})	40 V dc
Output short circuit duration	Indefinite
Storage temperature range	-65°C to +150°C
Power dissipation (P _D)	1.0 W
Lead temperature (soldering, 10 seconds)	+300°C
Junction temperature (T _J)	+150°C
Thermal resistance, junction-to-case (θ _{JC})	See MIL-STD-1835
Thermal resistance, junction-to-ambient (θ _{JA})	150°C/W

1.4 Recommended operating conditions.

Ambient operating temperature range (T _A)	-55°C to +125°C
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2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
 MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
 MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.daps.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Input offset voltage <u>1/</u>	V _{IO}	±0.75 V ≤ V _S ≤ ±20 V	1	All		1.0	mV
			2	01		5.0	
				02		1.5	
3	All		1.4				
Input bias current	I _{IB}	±0.75 V ≤ V _S ≤ ±20 V	1	01		15	nA
				02		75	
			2	01		60	
				02		110	
			3	01		25	
				02		100	
Input offset current	I _{IO}	±0.75 V ≤ V _S ≤ ±20 V	1	01		2.0	nA
				02		8.0	
			2	01		20	
				02		20	
			3	01		3.0	
				02		12	
Common mode rejection mode	CMRR	V _S = ±20 V, -20 V ≤ V _{CM} ≤ 19.1 V	1	All	105		dB
			2	01	82		
				02	86		
			3	All	100		
Power supply rejection ration	PSRR	+0.75 V ≤ V ₊ ≤ +20 V, -20 V ≤ V ₋ ≤ -0.75 V	1	All	96		dB
			2	01	82		
				02	86		
			3	All	95		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit	
					Min	Max		
Gain	G	No load, V _{OUT} = ±19.9 V, V+ = +20 V dc, V- = -20 V dc 2/	1	All	110		dB	
			2		100			
			3		105			
		R _L = 4.0 kΩ, V _{OUT} = ±19 V, V+ = +20 V dc, V- = -20 V dc	1		100			
			3		94			
Output sink current	I _{O(SK)}	V+ = 4.5 V, V- = 0 V, overdrive > 30 mV, V _{OUT} = 2.0 V	1	01	30		mA	
				02	35			
			2	All	10			
					3	25		
Output source current	I _{O(SO)}	V+ = 40 V, V- = 0 V, V _{IN} = 5.0 mV, V _{OUT} < 0.4 V	1	01	30		μA	
				02	75			
			2	01	25			
				02	50			
			3	01	25			
				02	50			
		V+ = 1.2 V, V- = 0 V, V _{IN} = 5.0 mV, V _{OUT} < 0.4 V	1	01	25			
				02	70			
			2	01	25			
				02	40			
			3	01	15			
				02	45			

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit	
					Min	Max		
Positive output saturation voltage	+V _{O(SAT)}	I _{OUT} = 0 μA, V ₊ = 1.2 V, V ₋ = 0 V	1	All		80	mV	
			2			100		
			3			90		
		1			250			
			2,3			300		
Negative output saturation voltage	-V _{O(SAT)}	I _{OUT} = 0 mA, V ₊ = 4.5 V, V ₋ = 0 V, V _{IN} = -10 V	1	01		20	mV	
				02		15		
			2	01		50		
				02		40		
			3	All		20		
				1	All			60
			2					100
			3	01		75		
					02			70
			1.0 mA, V ₊ = 4.5 V, V ₋ = 0 V, V _{IN} = -10 V	1	All			120
		2						200
		3						150
		10 mA, V ₊ = 4.5 V, V ₋ = 0 V, V _{IN} = -10 V	1	01		200		
					02			250
			2	01		600		
				02		400		
			3	All		300		
		30 mA, V ₊ = 4.5 V, V ₋ = 0 V, V _{IN} = -10 V	1	01		600		
					02			700
			3	All		900		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Leakage current	I _L	V _{OUT} = 40 V, V _{IN} > 100 mV, V ₊ = 5.0 V, V ₋ = 0 V	1	01		3.0	μA
				02		8.0	
			2	01		5.0	
				02		15	
			3	01		3.0	
				02		10	
Supply current	I _S	V ₊ = 5.0 V, V ₋ = 0 V	1	01		60	μA
				02		250	
			2	01		80	
				02		300	
			3	01		80	
				02		250	
		V ₊ = +40 V, V ₋ = 0 V	1	01		90	
				02		250	
			2	01		100	
				02		300	
			3	01		100	
				02		270	
Minimum operating voltage	V _{OP}	I _{OUT} = 1.0 mA	1,2,3	01		1.15	V
				02		1.2	

1/ Input offset voltage is guaranteed over a common mode voltage range of $V_- \leq V_{IN} \leq (V_+ - 0.9 V)$.

2/ No load gain is guaranteed but not tested, (device type 01 only).

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Device types	All
Case outlines	G and P
Terminal number	Terminal symbol
1	Output A
2	-Input A
3	+Input A
4	V-
5	+Input B
6	-Input B
7	Output B
8	V+

FIGURE 1. Terminal connections.

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3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime -VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime 's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

(2) $T_A = +125^\circ\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3
Group A test requirements (method 5005)	1,2,3
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime -VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 11-04-19

Approved sources of supply for SMD 5962-89504 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.dscc.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8950401GA	60264	MTLT1017QH
	<u>3/</u>	LT1017MH/883
5962-8950401GC	60264	MTL1017MH/883
5962-8950401PA	60264	MTLT1017QD8
	60264	MTL1017MJ8/883
	64155	LT1017MJ8/883
5962-8950401PC	60264	MTL1017MD8/883
	60264	MTL1017MJ8/883
5962-8950402GA	60264	MTLT1018QH
	<u>3/</u>	LT1018MH/883
5962-8950402GC	60264	MTL1018MH/883
5962-8950402PA	60264	MTLT1018QD8
	60264	MTL1018MJ8/883
	64155	LT1018MJ8/883
5962-8950402PC	60264	MTL1018MD8/883
	60264	MTL1018MJ8/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

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DATE: 11-04-19

<u>Vendor CAGE number</u>	<u>Vendor name and address</u>
60264	Minco Technology Labs, Inc. 1805 Rutherford Lane Austin, TX 78754-5101
64155	Linear Technology Corporation 1630 McCarthy Blvd. Milpitas, CA 95035-7417

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The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.