

LTC4418

Dual Channel Prioritized PowerPath Controller

DESCRIPTION

Demonstration circuit 2707B uses the LTC[®]4418 to arbitrate between two input supply rails, selecting the highest priority, valid supply to power the load. The V1 rail is defined to have higher priority and the V2 rail has lower priority. Both rails have individual overvoltage and undervoltage comparators for setting valid window thresholds with external resistive dividers. If the highest priority rail voltage V1 falls out of the defined window (overvoltage or

undervoltage), the V2 rail, if it is valid, is enabled and powers the load. Two or more LTC4418s can be cascaded to provide switchover between more than two rails.

Design files for this circuit board are available at <http://www.analog.com/DC2707B>

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PERFORMANCE SUMMARY

Specifications are at $T_A = 25^\circ\text{C}$

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V1, V2, V _{OUT}	V1, V2, V _{OUT} Operating Supply Range		2.5		40	V
V _{INTVCC}	V _{INTVCC} Voltage		2.5	3.3	4	V
ΔV_G	Open Clamp Voltage ($V_S - V_G$)	V _{OUT} = 11V, G1 = G2 = Open	5.4	6.2	6.7	V
$\Delta V_{G(\text{SOURCE})}$	Sourcing ($V_S - V_G$) Clamp Voltage	V _{OUT} = 11V, I = -10 μ A	5.8	6.6	7	V
$\Delta V_{G(\text{SINK})}$	Sinking ($V_S - V_G$) Clamp Voltage	V _{OUT} = 11V, I = 10 μ A	4.5	5.2	6	V
$\Delta V_{G(\text{OFF})}$	Gate Off Threshold	VS1 = VS2 = 2.8V, V _{OUT} = 11V, Gate Rising	0.2	0.3	0.4	V
I _{G(DN)}	Gate Pull-Down Current	VG = 3V, VS Floating	28	60	120	mA
V _{REV}	Reverse Voltage Threshold	Measure (V1 or V2) - V _{OUT} Falling	75	125	185	mV
t _{G(SWITCHOVER)}	Break-Before-Make Time	V _{OUT} = 11V, C _{GATE} = 10nF	1	2.7	4	μ s
t _{SS}	Soft-Start Timeout		20	35	70	ms
V _{EN(TH)}	EN Threshold Voltage	EN Rising	0.6	1	1.4	V
V _{SHDN(TH)}	SHDN Threshold Voltage	SHDN Rising	0.6	1	1.4	V
V _{SHDN_EN(HYS)}	SHDN, EN Threshold Hysteresis			130		mV
I _{CTRL}	SHDN, EN Pull-Up Current	SHDN = EN = 0V	-1.5	-3.2	-5.5	μ A
V _{TH}	OV/UV Comparator Threshold	V _{OUT} = 11V, OV Rising, UV Falling	0.985	1	1.015	V
V _{HYS(INT)}	OV/UV Comparator Hysteresis	V _{OUT} = 11V,	15	30	45	mV
I _{LEAK}	OV/UV Leakage Current	OV = 1.015V, UV = 0.985V			± 10	nA
I _{TMR}	TMR Pull-Up Current TMR Pull-Down Current	Timer On, V _{TMRM} \leq 600mV Timer On, V _{TMRM} \geq 1.6V	-1 1	-2 2	-3.5 3.5	μ A μ A
t _{VALID}	OV, UV Validation Time	TMR = V _{INTVCC} CTMR = 1nF	2 9	3.5 16	7 32	μ s ms
V1	Operating Voltage of Channel V1		9.35	12	14.65	V
V2	Operating Voltage of Channel V2		18.45	24	29.4	V
I _{LOAD}	Load Current		0	1		A
AVI	Auxiliary Voltage Input		6		24	V
External 3.3V	Input Voltage Range		3.0	3.3	3.5	V

OVERVIEW

The LTC4418 controls two sets of external back-to-back P-channel MOSFETs to connect the proper supply rail to the load. Precision comparators monitor both rails continuously against their OV/UV window. The highest priority input supply whose voltage is within its OV/UV window for the adjustable validation time of 15.8ms or 3.5 μ s (selectable on the board) is considered valid and connected to the load. A low signal on the $\overline{\text{VALID1}}$ and $\overline{\text{VALID2}}$ pins indicates validation of the V1 and V2 voltages. Connecting the CAS output to the EN pin on another LTC4418 or LTC4417 increases the number of multiplexed input supplies. Figure 2 demonstrates cascading prioritized controllers.

DC2707B is designed to operate from 12V and 24V, applied to V1 and V2 respectively. The valid range of both supplies is approximately 20% as set by OV and UV comparators and their associated resistive dividers. The board V1 and V2 inputs are protected from positive and

negative glitches with bidirectional TVS diodes that can continuously tolerate input voltages up to $\pm 30\text{V}$ while the LTC4418 V1 and V2 pins can tolerate +60V and -42V . Maximum continuous load current is 2.5A (limited by the MOSFET), and a maximum pulse drain current of this MOSFET is 30A.

LEDs with accompanying circuitry are included to provide visual information about the operating status of the rails V1 and V2. This LED circuit is powered from the 3.3V auxiliary rail with two options of getting power: from external power supply connected directly to auxiliary rail or from the low dropout linear regulator LT3060 (U5) installed on the board with the input voltage range 6V to 24V.

One of the rails V1 or V2 can be used to power LT3060 when its voltage is within the LT3060 input voltage range.

The auxiliary 3.3V rail also powers 100k Ω pull-ups for both $\overline{\text{VALID}}$ pins.

OPERATING PRINCIPLES

To eliminate back-and-forth switching during switchover, the LTC4418 provides a fixed 30mV hysteresis in the OV and UV comparator and an externally adjustable current mode hysteresis using the OV/UV resistive dividers. The JP1 (HYS) jumper allows selection of a fixed 30mV or adjustable hysteresis.

The controller's "break-before-make" switching method prevents cross conduction between input channels and reverse current from the output capacitor into the selected input supply.

Each channel's control circuit has a REV comparator that delays the connection until the output voltage droops 125mV below the input voltage. This prevents reverse current.

The LTC4418 has two common control pins: EN and $\overline{\text{SHDN}}$.

Pulling the EN pin below 1V turns off all external back-to-back P-channel MOSFETs. When this pin is driven above 1V, the highest priority valid channel is connected to the load without resetting the adjustable OV/UV timers.

Pulling the $\overline{\text{SHDN}}$ pin below 1.0V turns off all external back-to-back P-channel MOSFETs, placing the controller in a low current state and resetting the adjustable timers used to validate the input rail voltages. It requires a timer interval to validate each rail voltage after the $\overline{\text{SHDN}}$ pin signal goes high.

To minimize inrush current at start-up, the gate driver soft-starts the gate drive of the first input to connect to V_{OUT} . The gate pin is regulated to create an approximately 4V/ms slew rate on V_{OUT} . Logic level P-channel MOSFETs with a threshold below 1V will result in faster soft-start slew rate on V_{OUT} . Slew rate control is terminated when any channel disconnects or a time period of 35ms has elapsed. Once soft-start has terminated, the gate driver operates normally. A $\overline{\text{SHDN}}$ low to high transition reactivates soft-start, provided V_{OUT} drops below 2.3V before $\overline{\text{SHDN}}$ is high. V_{OUT} drooping below 1.7V also reactivates soft-start.

When connecting a high voltage supply to a lower voltage output, significant inrush current can occur while charging an output capacitor with low ESR. Inrush current during a switchover can cause two issues, P-channel MOSFETs are subjected to damaging power dissipation

OPERATING PRINCIPLES

and an undesirable UV fault from significant input voltage droop also known as motorboating.

With the LTC4418, inrush current can be reduced by slew rate limiting the output voltage. The gate driver can be configured to slew rate limit the output voltage with

a resistor, capacitor and Schottky diode, as shown in Figure 4 of the data sheet.

The LTC4418 does not actively protect against output short circuit events and there may be some cases where the output short may cause the MOSFET to fail.

TURRETS

V1 (12V): 12V supply input, do not exceed +30V or –30V due to protection TVS present on the board.

V2 (24V): 24V supply input, do not exceed +30V or –30V due to protection TVS present on the board.

GND: Adjacent ground connection for input supplies and for load.

V_{OUT}: Output for the load. Designed for 2.5A maximum continuous current (with component selected).

6V to 24V: Auxiliary voltage input (AVI) converted by an LDO U5 to 3.3V for LEDs, logic, and pull-ups.

3.3V: 3.3V output provided by U5 or by external supply.

Each of the following turrets is directly connected to the LTC4418 pin of the same name:

VALID1: Pulled up with 100kΩ to auxiliary 3.3V supply.

VALID2: Pulled up with 100kΩ to auxiliary 3.3V supply.

EN: Pulled up by internal 3.2μA to INTV_{CC} or by 10kΩ to external 3.3V.

SHDN: Pulled up by internal 3.2μA to INTV_{CC} or by 100kΩ to external 3.3V.

CAS: Used to cascade a second LTC4418 or LTC4417.

Connect the CAS turret of the high priority LTC4418 to the EN pin of the lower priority LTC4418.

JUMPERS

JP1, HYS: Add 30mV fixed hysteresis to the OV and UV comparators or 3% referred to actual supply input. In the RHYS position input referred hysteresis is set to 0.25V as controlled by R15. Default position is 30mV.

Note: Be careful when changing hysteresis. It affects the risk of motorboating.

JP2, EN: Controls EN pin. Default position (EXT + 3.3V) connects EN to the auxiliary 3.3V supply. Changing the jumper position connects EN to GND.

JP3, SHDN: Controls $\overline{\text{SHDN}}$ pin. Default position (EXT + 3.3V) connects $\overline{\text{SHDN}}$ to the auxiliary 3.3V supply. Changing the jumper position connects $\overline{\text{SHDN}}$ to GND. Do not change the JP3 position on the fly.

JP4, VALIDATION TIME: Allows to set 3.5μs or 15.8ms validation time. Default position is 3.5μs.

LEDS

D6: Indicates power present at V_{OUT}.

D7: Indicates power being taken from V1.

D8: Indicates power being taken from V2.

D9: Indicates validation of V1.

D10: Indicates validation of V2.

D11: Indicates auxiliary 3.3V supply.

DEMO MANUAL DC2707B

QUICK START PROCEDURE

Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

1. Disconnect both power supply inputs from the board.
2. Connect the auxiliary power source (6V to 24V) to the DC2707B. The green LED (LDO-D11) lights up, indicating the presence of auxiliary 3.3V supply for powering logic.
3. With power off, connect two power supplies with output voltage 12V and 24V to the corresponding DC2707B turrets or banana jacks V1 (+12V), V2 (+24V) and GND.
4. Connect 10Ω load resistor (100W) to the DC2707B output turret or banana jack (V_{OUT}).
5. Verify correct jumper setting for validation time (3.5μs default), HYS (R_{HYS} default), EN (EXT + 3.3V) and SHDN (EXT + 3.3V).
6. Turn on power supplies and observe the transient of powering a load from V1 = 12V.
7. The prioritizing function is demonstrated by simply turning off the V1 supply. The output will be powered from the remaining supply V2. V1 can be adjusted up and down beyond ±20% to invalidate the V1 input and observe the transient from V2 to V1 and vice versa.

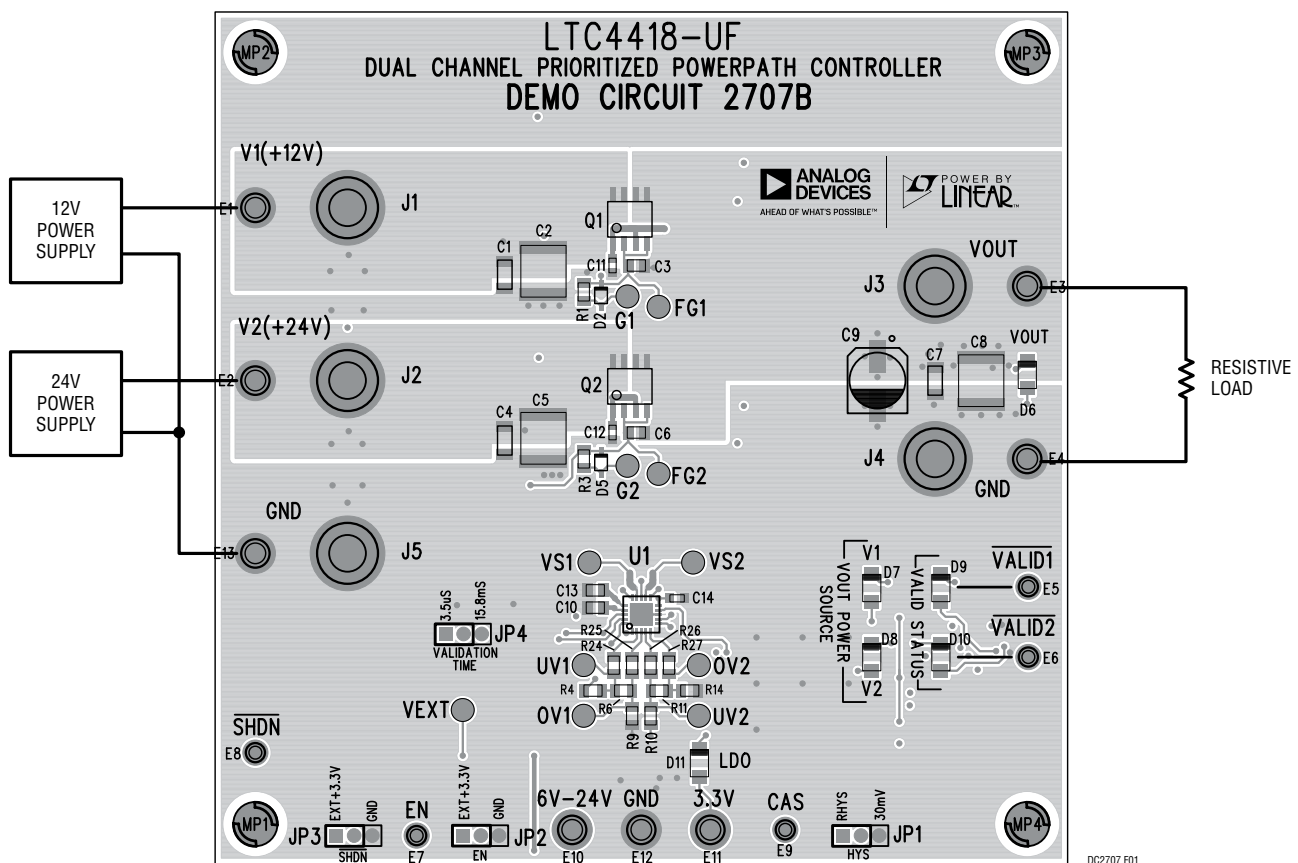


Figure 1. Proper Measurement Equipment Setup

DC2707 F01

QUICK START PROCEDURE

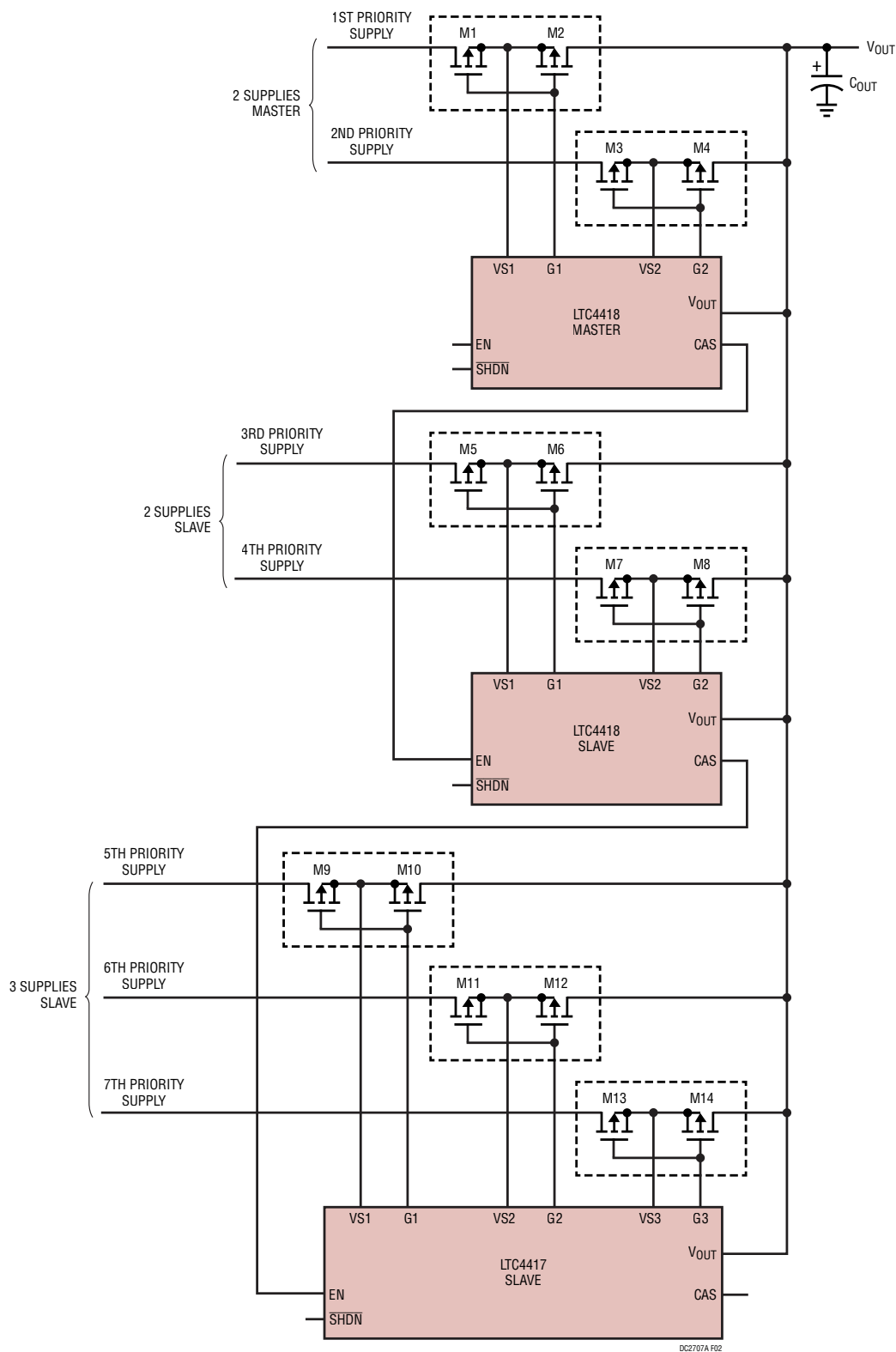


Figure 2. Cascading LTC4418s and LTC4417



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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