

Product Search Data Sheet

Note: This datasheet may be out of date.

Please download the latest datasheet of BLM31PG601SN1# from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en/products/productdetail?partno=BLM31PG601SN1%23

"#" indicates a package specification code.









< List of part numbers with package codes >

BLM31PG601SN1B

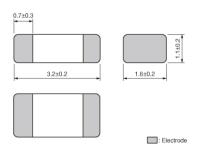
BLM31PG601SN1K

BLM31PG601SN1L



Appearance & Shape





(in mm)

Packaging Information

Packaging	Specifications	Standard Packing Quantity
В	Bulk(Bag)	1000
К	330mm Embossed Tape	10000
L	180mm Embossed Tape	3000



Applications

Other Usage

For general

1 of 4

Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

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Features

- 1.The chip ferrite beads BLM series is designed to function nearly as a resistor at noise frequencies, which greatly reduces the possibility of resonance and leaves signal wave forms undistorted.

 BLM series is effective in circuits without stable ground lines because BLM series does not need a connection to ground.
- 2.The nickel barrier structure of the external electrodes provides excellent solder heat resistance.
 3.BLM_P series can be used in high current circuits due to its low DC resistance. It can match power lines to a maximum of 6ADC.

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Specifications

Shape	SMD
Size Code (in inch)	1206
Length	3.2mm
Length Tolerance	±0.2mm
Width	1.6mm
Width Tolerance	±0.2mm
Thickness	1.1mm
Thickness Tolerance	±0.2mm
Operating Temperature Range	-55°C to 125°C
Mass(typ.)	0.025g
Number of Circuit	1
Rated Current (at 85°C)	1.5A
Rated Current (at 125°C)	1A
DC Resistance(max.)	0.08Ω
Impedance (at 100MHz)	600Ω
Impedance (at 100MHz) Tolerance	±25%
Size Code (in mm)	3216

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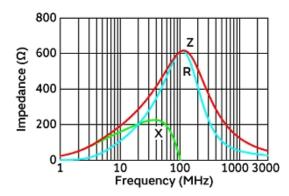
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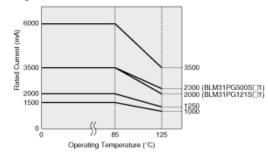
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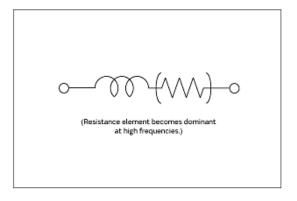
In operating temperature exceeding +85°C, derating of current is necessary for BLM31PG series. Please apply the derating curve shown in chart according to the operating temperature.

Derating of Rated Current



Impedance-Frequency Characteristics

Derating of Rated Current



Equivalent Circuit

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