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ADN2915 EVALUATION BOARD

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Product Details

Test pattern generation and error detection are critical functions needed for design, development, and debug of systems with high speed serial interfaces.

The ADI-BERT reference circuit solution is a flexible, continuous rate pattern generator and error detector of up to 11.3 Gbps that enables test pattern generation for a wide range of industry protocols and data rates.

The ADN2915 clock and [More](#)

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Documentation

- UG-551: XStream™ ADI-BERT: An 11.3 Gbps Bit Error Rate Tester Solution Based on ADN2915, a Continuous-Tuning Wideband Clock and Data Recovery IC (pdf, 1884 kB)

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PRICE, PACKAGING, AVAILABILITY

ADN2915 Evaluation Board

Model	Description
EVALZ-ADI-BERT Status: Production	PRBS Generator/Error Dectector for ADN2915
EVALZ-ADN2915 Status: Production	Evaluation Board for ADN2915 with ADI-BERT

Pricing displayed is based on 1-piece. The USA list pricing shown is for budgetary use only, shown in United States dollars (FOB USA per unit), and is subject to change. International prices may vary due to local duties, taxes, fees and exchange rates.

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