

Film Capacitors

EMI Suppression Capacitors (MKP)

 Series/Type:
 B32032 ... B32036

 Date:
 November 2019

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EMI suppression capacitors (MKP)

Y2 / 350 V AC

B32032 ... B32036

Typical applications

- Y2 class for interference suppression
- "Line to ground" applications
- Severe Ambient conditions
- Automotive

Climatic

- Max. operating temperature: 110 °C
- Climatic category (IEC 60068-1:2013): 40/110/56

Construction

- Dielectric: polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

Features

- THB Grade IIIb¹⁾
- Small dimensions
- Good self-healing properties
- High voltage capability
- RoHS-compatible
- Halogen-free capacitors available on request
- AEC-Q200D compliant

Terminals

- Parallel wire leads, lead-free tinned
- Special lead lengths available on request

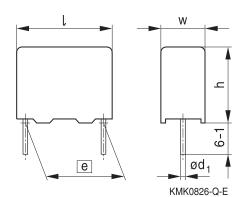
Marking

Manufacturer's logo, lot number, date code, rated capacitance (coded), capacitance tolerance (code letter), rated AC voltage (IEC), series number, sub-class (Y2), dielectric code (MKP), climatic category, passive flammability category, approvals.

Delivery mode

Bulk (untaped) Taped (Ammo pack or reel) For taping details, refer to chapter "Taping and packing"

Dimensional drawing



Dimensions in mm

Lead spacing <i>e</i> ±0.4	Lead diameter d ₁ ±0.05	Туре
15 27.5	0.8	B32032 B32034
37.5	1.0	B32036

1) Certification ongoing and pending acc. to IEC60384-14AMD1:2016





Marking example (position of marks may vary):



Approvals

Approval marks	Standards	Certificate
15	EN 60384-14:2014/A1:2016 IEC 60384-14:2013/AMD1:2016	ENEC-01910 (pproved by UL Demko)
c Al us	UL 60384-14:2014 / CSA E60384-14:2013	E97863

Overview of available types

Lead spacing	15 mm	22.5 mm	27.5 mm	37.5 mm
Туре	B32032	B32033	B32034	B32036
C _R (μF)				
0.0047				
0.0056				
0.0068				
0.0082				
0.010				
0.015				
0.018				
0.022				
0.027				
0.033				
0.039				
0.047				
0.056				
0.068				
0.082				
0.10				
0.12				
0.15				
0.18				





Overview of available types

0.22		
0.27		
0.33 0.39 0.47		
0.39		
0.47		
0.56		
0.56 0.68		
0.82		
1.0		
1.2		

Ordering codes and packing units

Lead	C _R	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times I$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
15	0.0047	$5.0\times10.5\times18.0$	B32032A4472+***	4680	5200	4000
	0.0056	5.0 imes 10.5 imes 18.0	B32032A4562+***	4680	5200	4000
	0.0068	5.0 imes 10.5 imes 18.0	B32032A4682+***	4680	5200	4000
	0.0082	5.0 imes 10.5 imes 18.0	B32032A4822+***	4680	5200	4000
	0.010	5.0 imes 10.5 imes 18.0	B32032A4103+***	4680	5200	4000
	0.015	$6.0\times11.0\times18.0$	B32032A4153+***	3840	4400	4000
	0.018	$7.0\times12.5\times18.0$	B32032A4183+***	3320	3600	4000
	0.022	$7.0\times12.5\times18.0$	B32032A4223+***	3320	3600	4000
	0.027	$8.0\times14.0\times18.0$	B32032A4273+***	2920	3000	2000
	0.033	$8.0\times14.0\times18.0$	B32032A4333+***	2920	3000	2000
	0.039	8.5 imes 14.5 imes 18.0	B32032A4393M***	2720	2800	2000
	0.039	9.0 imes17.5 imes18.0	B32032B4393K***	2560	2800	2000
	0.047	9.0 imes17.5 imes18.0	B32032A4473+***	2560	2800	2000
	0.056	9.0 imes17.5 imes18.0	B32032A4563M***	2560	2800	2000
	0.056	11.0 imes 18.5 imes 18.0	B32032B4563K***	—	2200	1200
	0.068	$11.0\times18.5\times18.0$	B32032A4683+***	_	2200	1200

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further intermediate capacitance values on request.

Composition of ordering code

- + = Capacitance tolerance code:
 - M = ±20%
 - $K = \pm 10\%$

- *** = Packaging code:
 - 289 = Straight terminals, Ammo pack
 - 189 = Straight terminals, Reel
 - 003 = Straight terminals, untaped (lead length 3.2 ±0.3 mm)
 - 000 = Straight terminals, untaped (lead length 6 -1 mm)



Y2

Ordering codes and packing units

Lead	C _R	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times I$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
22.5	0.033	$6.0\times15.0\times26.5$	B32033A4333+***	2720	2800	2880
	0.047	$7.0\times16.0\times26.5$	B32033A4473+***	2320	2400	2520
	0.056	$7.0\times16.0\times26.5$	B32033A4563+***	2320	2400	2520
	0.068	$8.5\times16.5\times26.5$	B32033A4683+***	1920	2000	2040
	0.082	$8.5\times16.5\times26.5$	B32033A4823M***	1920	2000	2040
	0.082	$10.5\times16.5\times26.5$	B32033B4823K***	1560	1600	2160
	0.10	$10.5\times16.5\times26.5$	B32033A4104M***	1560	1600	2160
	0.10	$10.5\times18.5\times26.5$	B32033B4104K***	1560	1600	2160
	0.12	$10.5\times18.5\times26.5$	B32033A4124M***	1560	1600	2160
	0.12	$10.5\times20.5\times26.5$	B32033B4124+***	_	—	2160
	0.15	$11.0\times20.5\times26.5$	B32033A4154M***	—	—	2040
	0.15	$12.0\times22.0\times26.5$	B32033B4154K***	—	—	1800
	0.18	$12.0\times22.0\times26.5$	B32033A4184M***	—	—	1800
	0.18	$14.5\times29.5\times26.5$	B32033B4184K***	—	—	1040
	0.22	$14.5\times29.5\times26.5$	B32033A4224+***	_	—	1040
	0.27	$14.5\times29.5\times26.5$	B32033A4274+***	-	_	1040
	0.33	$15.5\times33.5\times26.5$	B32033B4334+***	-		1120
	0.39	$15.5\times33.5\times26.5$	B32033A4394+***	—	_	1120

MOQ = Minimum Order Quantity, consisting of 4 packing units. Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

M = ±20%

K = ±10%

- *** = Packaging code:
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Y2

B32032 ... B32036

Y2 / 350 V AC

Ordering codes and packing units

Lead	C _R	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times I$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
27.5	0.10	$11.0 \times 19.0 \times 31.5$	B32034A4104+***	—	1400	1280
	0.15	$11.0 \times 19.0 \times 31.5$	B32034A4154M***	—	1400	1280
	0.15	$11.0\times21.0\times31.5$	B32034B4154K***	—	1400	1280
	0.18	$11.0 \times 21.0 \times 31.5$	B32034A4184+***	—	1400	1280
	0.22	$13.5\times23.0\times31.5$	B32034B4224+***	—	1000	1040
	0.27	$15.0\times24.5\times31.5$	B32034A4274+***	—	—	960
	0.33	$16.0\times32.0\times31.5$	B32034B4334+***	—	—	880
	0.33	$18.0\times27.5\times31.5$	B32034C4334+***	—	—	800
	0.39	$16.0\times32.0\times31.5$	B32034A4394M***	—	—	880
	0.39	$18.0\times27.5\times31.5$	B32034B4394+***	—	—	800
	0.47	$16.0\times32.0\times31.5$	B32034A4474M***	—	—	880
	0.47	$18.0\times33.0\times31.5$	B32034C4474K***	—	—	800
	0.56	$21.0\times31.0\times31.5$	B32034C4564+***	—	—	720
	0.68	$22.0\times36.5\times31.5$	B32034A4684+***	—	—	640
	0.82	$22.0\times48.0\times31.5$	B32034B4824K***	—	—	320
	1.0	$22.0\times48.0\times31.5$	B32034A4105+***	—	—	320
37.5	0.27	$12.0\times22.0\times42.0$	B32036A4274+***	—	—	1620
	0.33	$14.0\times25.0\times42.0$	B32036B4334K***	_	—	1380
	0.39	$14.0\times25.0\times42.0$	B32036A4394+***	_	—	1380
	0.47	$16.0\times28.5\times42.0$	B32036A4474+***	_	—	800
	0.56	$16.0\times28.5\times42.0$	B32036A4564+***	_	—	800
	0.68	$17.0\times32.0\times42.0$	B32036A4684+***	—	—	760
	0.82	$18.0\times32.5\times42.0$	B32036A4824M***	—	—	720
	0.82	$20.0\times39.5\times42.0$	B32036B4824K***	—		640
	1.0	$20.0\times39.5\times42.0$	B32036A4105+***	_	_	640
	1.2	$28.0\times42.5\times42.0$	B32036A4125+***	_	_	440

MOQ = Minimum Order Quantity, consisting of 4 packing units. Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

- $M = \pm 20\%$
- $K = \pm 10\%$

*** = Packaging code:

289 = Straight terminals, Ammo pack

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	Y2 / 350 V AC

Ordering code example

B Compo- nents class	3203 Series	3 Lead spacing (mm)	A Dimension code	4 Rated voltage	154 Rated capacitance	M Capacitance tolerance
Passive compo- nents	Y2 MKP	2 = 15.0 3 = 22.5 4 = 27.5 6 = 37.5	See table "Ordering codes and packing units"	4 = 350 V AC	15 · 10⁴ pF	K = ±10% M = ±20% + = K or M

Technical data

Reference standard: UL / IEC 60384-14:2013/AMD1:2016. All data given at T = 20 $^{\circ}$ C, unless otherwise specified.

Rated AC voltage V _{AC} (IEC 60384-14:2013)	350 V AC (50/60 Hz)
Rated DC voltage V _{DC}	1000 V DC
Max. operating temperature T_{op} ($T_{op} = T_A + T_{self-heating}$)	+110 °C
DC test voltage for 2 seconds	4200 V (C \le 0.33 μ F) 4000 V (C $>$ 0.33 μ F), 2 s
AC test voltage (type test)	2000 V _{RMS} , 60 s

The repetition of this DC voltage test may damage the capacitor. Special care must be taken in case of use several capacitors in a parallel configuration (refer to section "Parallel configuration).

Dissipation factor tan δ at 1 kHz (upper limit)	1 · 10 ⁻³		
Insulation resistance R _{ins} or	$C_{R} \le 0.33 \mu\text{F}$	C _R > 0.33 μF	
time constant $\tau = C_R \cdot R_{ins}$ at 100 V DC,	100 000 MΩ	30 000 s	
rel. humidity \leq 65% and for 60 s			
(minimum as-delivered values)			
Passive flammability category	В		
Capacitance tolerances	±10% (K), ±20% (M)		
(measured at 1 kHz)			
Grade III high robustness	T = +85 °C ± 2 °C		
under high humidity	Relative humidity: $85\% \pm 2\%$	%	
	V _R = 350 V AC, 50/60 Hz		
Test B:	Test duration: 1000 hours		
Criteria for passing after	Capacitance change (Δ C/C) ≤10%	
biased damp heat test	ed damp heat test Dissipation factor change ($\Delta \tan \delta$) < 0.005		
	Insulation resistance R _{ins}	> 50% of initial limit	
	Mechanical criteria: not app	olicable	





Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in $V/\mu s$.

" k_0 " represents the maximum permissible pulse characteristic of the waveform applied to the capacitor, expressed in V²/µs.

Note:

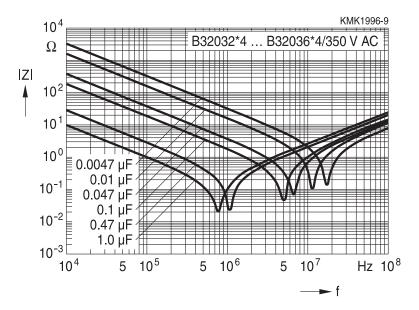
The values of dV/dt and k_0 provided below must not be exceeded in order to avoid damaging the capacitor. These parameters are given for isolated pulses in such a way that the heat generated by one pulse will be completely dissipated before applying the next pulse. For a train of pulses, please refer to the curves of permissible AC voltage-current versus frequency

dV/dt and k₀ values

Lead spacing	15 mm	22.5 mm	27.5 mm	37.5 mm
dV/dt in V/µs	600	500	400	300
k₀in V²/µs	508 000	423 000	338 000	254 000

Impedance Z versus frequency f

(typical values)

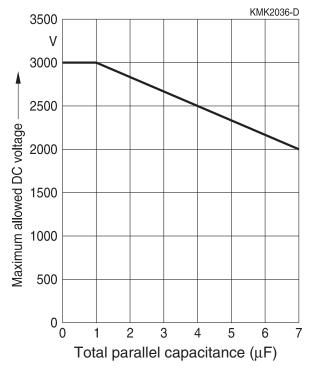




Parallel configuration

Only capacitors of the same capacitance may be connected in parallel and the maximum DC test voltage must be reduced as a function of the total parallel capacitance value. The diagram below shows a typical trend of maximum allowed voltage versus total parallel capacitance. (Cut off current 10 mA, rise time \leq 1000 V/s, dwell time \leq 2s, maximum allowed parallel connected capacitors 8 pcs).

B3203* DC test voltage as function of total parallel capacitance (μ F):

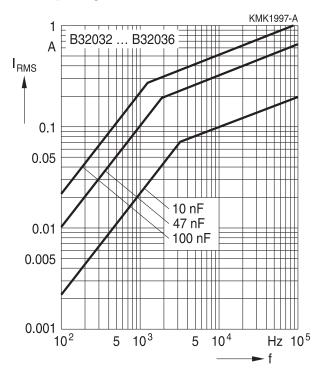




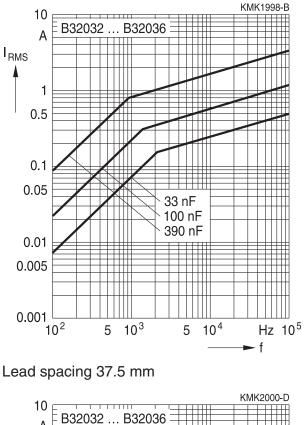


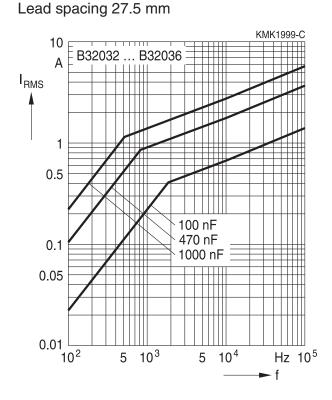
Permissible current I_{\text{RMS}} versus frequency (for sinusoidal waveform, T_A \leq 90 °C and **∆ESR <100% from receipt condition)**

Lead spacing 15 mm

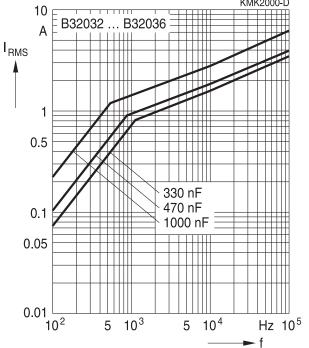


Lead spacing 22.5 mm





Lead spacing 37.5 mm





Y2

Testing and Standards

Test	Reference	Conditions of test		Performance requirements
Voltage proof	IEC 60384-14:2013/ AMD1:2016	Voltage proof between terminals: 1500 V AC, 1 min Terminals and enclosure: $2 V_{B} + 1500 V AC$		Within specified limits
Special voltage proof	_	Voltage proof between 2000 V AC, 1min	terminals:	Within specified limits
Robustness of termina- tions	IEC 60068-2-21:2006	$for 0.5 < d_1 \le 0.8 \text{ mm}$ 10	J _a 1) ensile rce D N D N	Capacitance and tan δ within specified limits
Resistance to soldering heat	IEC 60068-2-20:2008, test Tb, method 1A	Solder bath temperature at		$\Delta C/C_0 \le 5\%$ tan δ within specified limits
Vibration	IEC 60384-14:2013/ AMD1:2016	Test FC: vibration sinusoidal Displacement: 0.75 mm Accleration: 98 m/s ² Frequency: 10 Hz 500 Hz Test duration: 3 orthogonal axes, 2 hours each axe		No visible damage
Bump	IEC 60384-14:2013/ AMD1:2016	Test Eb: Total 4000 bumps with 400 m/s ² mounted on PCB 6 ms duration		No visible damage $ \Delta C/C_0 \le 5\%$ tan δ within specified limits
Damp heat, steady state	IEC 60384-14:2013/ AMD1:2016	40 °C / 93% RH / 56 days		$\begin{split} & \text{No visible damage} \\ & \Delta C/C_0 \leq 5\% \\ & \Delta \tan \delta \leq 0.008 \text{ for } C \leq 1\mu\text{F} \\ & \Delta \tan \delta \leq 0.005 \text{ for } C > 1\mu\text{F} \\ & \text{Voltage proof} \\ & R_{\text{ins}} \geq 50\% \text{ of initial limit} \end{split}$
Grade III High robustness under high humidity Test B:	IEC 60384-14:2013/ AMD1:2016	85 °C / 85% RH / 1000 hours / 350 V AC, 50 Hz		$\begin{split} \Delta C/C_0 &\leq 10\% \\ \Delta \tan \delta &\leq 0.005 \\ R_{ins} &\geq 50\% \text{ of initial limit} \end{split}$
Biased humidity test	AEC-Q200D	40 °C / 93% RH / 1000 hours / 350 V AC, 50 Hz		$\begin{split} \Delta C/C_0 &\leq 10\% \\ \Delta \tan \delta &\leq 0.005 \\ R_{ins} &\geq 50\% \text{ of initial limit} \end{split}$



Y2

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Y2 / 350 V AC

Test	Reference	Conditions of test	Performance requirements
Temperature cycling	AEC-Q200D	T_A = lower category temperature T_B = upper category temperature 1000 cycles, duration t = 30 min	No visible damage $ \Delta C/C_0 \le 5\%$ tan δ within specified limits
Climatic sequence	IEC 60384-14:2013/ AMD1:2016	Dry heat Tb / 16 h Damp heat cyclic, 1 st cycle +55 °C / 24 h / 95% 100% RH Cold Ta / 2 h Damp heat cyclic, 5 cycles +55 °C / 24 h / 95% 100% RH	$\begin{array}{l} \mbox{No visible damage} \\ \Delta C/C_0 \leq 5\% \\ \Delta \mbox{ tan } \delta \leq 0.008 \mbox{ for } C \leq 1 \mu F \\ \Delta \mbox{ tan } \delta \leq 0.005 \mbox{ for } C > 1 \mu F \\ \mbox{Voltage proof} \\ R_{ins} \geq 50\% \mbox{ of initial limit} \end{array}$
Impulse test endurance	IEC 60384-14:2013/ AMD1:2016	3 impulses 110 °C / 1.7 V _R / 1000 hours, 1000 V _{RMS} for 0.1 s every hour	No visible damage $ \Delta C/C_0 \le 10\%$ $ \Delta \tan \delta \le 0.008$ for $C \le 1 \mu F$ $ \Delta \tan \delta \le 0.005$ for $C > 1 \mu F$ Voltage proof $R_{ins} \ge 50\%$ of initial limit
Passive flammability	IEC 60384-14:2013/ AMD1:2016	Flame applied for a period of time depending on capacitor volume	В
Active flammability	IEC 60384-14:2013/ AMD1:2016	20 discharges at 5 kV + AC	The cheesecloth shall not burn with a flame

Mounting guidelines

1 Soldering

1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20:2008, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2:2007, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

Solder bath temperature	235 ±5 °C	
Soldering time	2.0 ±0.5 s	
Immersion depth	2.0 + 0/-0.5 mm from capacitor body or seating plane	
Evaluation criteria:		
Visual inspection	Wetting of wire surface by new solder ≥90%, free-flowing solder	





1.2 Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20:2008, test Tb, method 1. Conditions:

Series		Solder bath temperature	Soldering time
MKT	boxed (except $2.5 \times 6.5 \times 7.2$ mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP			
MKP	(lead spacing >7.5 mm)		
MKT	boxed (case $2.5 \times 6.5 \times 7.2$ mm)		5 ±1 s
MKP	(lead spacing ≤7.5 mm)		<4 s
MKT	uncoated (lead spacing ≤10 mm) insulated (B32559)		recommended soldering profile for MKT uncoated (lead spacing \leq 10 mm) and insulated (B32559)
300	КМК1242-V	· ·	+
°C			
T 250			
200			
150			
100			
50			
0	0 50 100 150 200 s 25	50	

Immersion depth	2.0 + 0/-0.5 mm from capacitor body or seating plane	
Shield	Heat-absorbing board, (1.5 ± 0.5) mm thick, between	
E de la companya de la	capacitor body and liquid solder	
Evaluation criteria:		
Visual inspection	No visible damage	
$\Delta C/C_0$	2% for MKT/MKP/MFP	
	5% for EMI suppression capacitors	
tan δ	As specified in sectional specification	

Please read *Cautions and warnings* and *Important notes* at the end of this document. Downloaded from Arrow.com. Page 13 of 25





1.3 General notes on soldering

Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature T_{max} . Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics:
 - diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

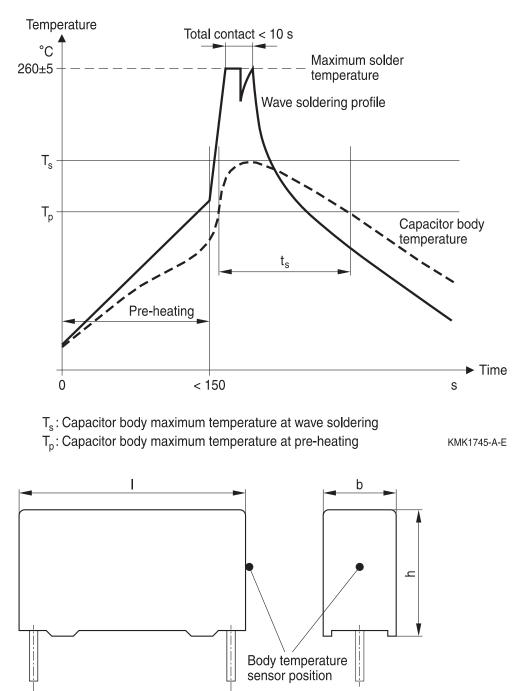




The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

Recommendations

As a reference, the recommended wave soldering profile for our film capacitors is as follows:



Ρ

KMK1744-9-E





Body temperature should follow the description below:

- MKP capacitor During pre-heating: T_p ≤110 °C During soldering: T_s ≤120 °C, t_s ≤45 s
- MKT capacitor During pre-heating: T_p ≤125 °C During soldering: T_s ≤160 °C, t_s ≤45 s

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T_s) must be \leq 120 °C.

One recommended condition for manual soldering is that the tip of the soldering iron should be <360 °C and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings \leq 10 mm (B32560/B32561) the following measures are recommended:

- pre-heating to not more than 110 °C in the preheater phase
- rapid cooling after soldering

Please refer to our Film Capacitors Data Book in case more details are needed.



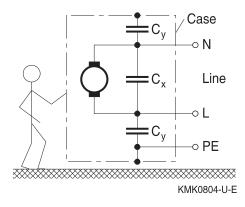


Application note

A Y capacitor to be connected between AC lines and the Frame Ground (FG) has an effect of emitting common mode current to the FG.Leak current corresponding in frequency and voltage to the AC power supply flows in the Y capacitor.

As the capacity of a Y capacitor grows, leak current also increases. This can cause electric shock. Therefore, UL and other safety standards restrict the capacity so that the amount of leak current does not exceed a certain level. Usually, two Y capacitors are used, as shown in Figure below. As AC lines are connected with capacitors, they are effective in reducing differential mode noises. In particular, they are effective for high frequency ranges of 8 to 10 MHz.

Example of EMI suppression, typical application:







B32032 ... B32036

Y2 / 350 V AC

Cautions and warnings

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of TDK Electronics.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Торіс	Safety information	Reference chapter "General technical information"
Storage conditions	Make sure that capacitors are stored within the specified range of time, temperature and humidity conditions.	4.5 "Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive flammability), avoid overload of the capacitors (active flammability) and consider the flammability of materials.	5.3 "Flammability"
Resistance to vibration	Do not exceed the tested ability to withstand vibration. The capacitors are tested to IEC 60068-2-6:2007. TDK Electronics offers film capacitors specially designed for operation under more severe vibration regimes such as those found in automotive applications. Consult our catalog "Film Capacitors for Automotive Electronics".	5.2 "Resistance to vibration"





Topic	Safety information	Reference chapter "Mounting guidelines"
Soldering	Do not exceed the specified time or temperature limits during soldering.	1 "Soldering"
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"
Embedding of capacitors in finished	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account.	3 "Embedding of capacitors in finished assemblies"
assemblies	Caution: Consult us first, if you also wish to embed other uncoated component types!	

Design of our capacitors

Our EMI capacitors use polypropylene (PP) film metalized with a thin layer of Zinc (Zn). The following key points have made this design suitable to IEC/UL testing, holding a minimum size.

- Overvoltage AC capability with very high temperature Endurance test of IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) must be performed at 1.25 × V_R at maximum temperature, during 1000 hours, with a capacitance drift less than 10%.
- Higher breakdown voltage withstanding if compared to other film metallizations, like Aluminum. IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) establishes high voltage tests performed at $4.3 \times V_R - 1$ minute, impulse testing at 2500 V for C = 1 µF and active flammability tests.
- Damp heat steady state: 40 °C/ 93% RH / 56 days. (without voltage or current load)

Effect of humidity on capacitance stability

Long contact of a film capacitor with humidity can produce irreversible effects. Direct contact with liquid water or excess exposure to high ambient humidity or dew will eventually remove the film metallization and thus destroy the capacitor. Plastic boxed capacitors must be properly tested in the final application at the worst expected conditions of temperature and humidity in order to check if any parameter drift may provoke a circuit malfunction.

In case of penetration of humidity through the film, the layer of Zinc can be degraded, specially under AC operation (change of polarity), accelerated by the temperature, provoking an increment of the serial resistance of the electrode and eventually a reduction of the capacitance value. For DC operation, the parameter drift is much less.

Plastic boxes and resins can not protect 100% against humidity. Metal enclosures, resin potting or coatings or similar measures by customers in their applications will offer additional protection against humidity penetration.





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Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products.

Detailed information can be found on the Internet under

www.tdk-electronics.tdk.com/orderingcodes.

Correlation of data sheet values and modelling tool outputs

Data sheet values and results of design tools may deviate as they have not been derived in the same context.

While data sheets show individual parameter statements without considering a possible dependency to other parameters. Tools model a complete given scenario as input and processed inside the tool.

Furthermore as we constantly strive to improve our models, the results of tools can change over time and be a non-binding indication only.



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Y2

Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
α^{c}	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
A	Capacitor surface area	Kondensatoroberfläche
β _c	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
С	Capacitance	Kapazität
C _R	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative	Relative Kapazitätsänderung (relative
	deviation of actual value)	Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation	Kapazitätstoleranz (relative Abweichung
	from rated capacitance)	vom Nennwert)
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change	Absolute Temperaturänderung
	(self-heating)	(Selbsterwärmung)
∆tan δ	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate	Differentielle Spannungsänderung
	of voltage rise)	(Spannungsflankensteilheit)
$\Delta V / \Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f ₁	Frequency limit for reducing permissible	Grenzfrequenz für thermisch bedingte
	AC voltage due to thermal limits	Reduzierung der zulässigen
		Wechselspannung
f ₂	Frequency limit for reducing permissible	Grenzfrequenz für strombedingte
	AC voltage due to current limit	Reduzierung der zulässigen
		Wechselspannung
f _r	Resonant frequency	Resonanzfrequenz
F _D	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
F _T	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I _c	Category current (max. continuous	Kategoriestrom (max. Dauerstrom)
	current)	



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Symbol	English	German
I _{RMS}	(Sinusoidal) alternating current,	(Sinusförmiger) Wechselstrom
	root-mean-square value	
İ _z	Capacitance drift	Inkonstanz der Kapazität
k _o	Pulse characteristic	Impulskennwert
L _s	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λο	Constant failure rate during useful	Konstante Ausfallrate in der
	service life	Nutzungsphase
λ_{test}	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P _{diss}	Dissipated power	Abgegebene Verlustleistung
P _{gen}	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des
		Entladekreises
R _i	Internal resistance	Innenwiderstand
R _{ins}	Insulation resistance	Isolationswiderstand
R _P	Parallel resistance	Parallelwiderstand
R _s	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
Т	Temperature	Temperatur
τ	Time constant	Zeitkonstante
tan δ	Dissipation factor	Verlustfaktor
$\tan \delta_{D}$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
tan δ _P	Parallel component of dissipation factor	Parallelanteil des Verlfustfaktors
tan δ_s	Series component of dissipation factor	Serienanteil des Verlustfaktors
T _A	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T _{max}	Upper category temperature	Obere Kategorietemperatur
T _{min}	Lower category temperature	Untere Kategorietemperatur
t _{oL}	Operating life at operating temperature and voltage	Betriebszeit bei Betriebstemperatur und -spannung
T _{op}	Operating temperature, $T_A + \Delta T$	Beriebstemperatur, $T_A + \Delta T$
T _R	Rated temperature	Nenntemperatur
T _{ref}	Reference temperature	Referenztemperatur
t _{SL}	Reference service life	Referenz-Lebensdauer



Y2

Symbol	English	German
V _{AC}	AC voltage	Wechselspannung
V _c	Category voltage	Kategoriespannung
$V_{C,RMS}$	Category AC voltage	(Sinusförmige)
		Kategorie-Wechselspannung
V_{CD}	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
V_{ch}	Charging voltage	Ladespannung
V_{DC}	DC voltage	Gleichspannung
V_{FB}	Fly-back capacitor voltage	Spannung (Flyback)
Vi	Input voltage	Eingangsspannung
Vo	Output voltage	Ausgangssspannung
V_{op}	Operating voltage	Betriebsspannung
V _p	Peak pulse voltage	Impuls-Spitzenspannung
V_{pp}	Peak-to-peak voltage Impedance	Spannungshub
V _R	Rated voltage	Nennspannung
Ŷ _R	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
V_{RMS}	(Sinusoidal) alternating voltage,	(Sinusförmige) Wechselspannung
	root-mean-square value	
V_{SC}	S-correction voltage	Spannung bei Anwendung "S-correction"
V_{sn}	Snubber capacitor voltage	Spannung bei Anwendung
		"Beschaltung"
Z	Impedance	Scheinwiderstand
е	Lead spacing	Rastermaß



The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
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