

Strengthening the link between
the real and the digital world

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CYPT1061G30-12GGMBES

Status: [Contact Sales](#)

Product Specs	Pricing & Inventory	Packaging/Ordering	Quality and RoHS	Technical Documents	
CYPT1061G30-12GGMBES				DESIGN SUPPORT	
Automotive Qualified	N			Development Kits	
Density (Kb)	16384			Forums	
Frequency (MHz)	N/A			Knowledge Base	
Max. Operating Temp. (°C)	125			Packaging	
Max. Operating VCCQ (V)	3.60			Product Brochures	
Max. Operating Voltage (V)	3.60			Quality & Reliability	
Min. Operating Temp. (°C)	-55			Samples	
Min. Operating VCCQ (V)	2.20			Technical Support	
Min. Operating Voltage (V)	2.20			Technical Training	
Organization (X x Y)	1M x 16			Third Party Network (CYPros)	
Part Family	Async SRAM with ECC				
Speed (ns)	12				
Tape & Reel	N				
Temp. Classification	Military				
PRICING & INVENTORY AVAILABILITY				TRAINING	
1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000+ unit Price*
Contact Sales					
Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors	
Out of Stock	0	Please click here to check lead times	Contact Sales	Contact Sales	
* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku					
PACKAGING/ORDERING					
No. of Pins	54				
Package Cross Section Drawing	Download				
Package Carrier	TRAY				
Package Carrier Drawing / Orientation	Package Orientation				
Standard Pack Quantity	1				
Minimum Order Quantity (MOQ)	1				
Order Increment	1				
Estimated Lead Time (days)	91				
HTS Code	8542.32.0041				
ECCN Suball	3A001.A.1.C				
QUALITY AND ROHS					
Moisture Sensitivity Level (MSL)	1				
Peak Reflow Temp. (°C)	220 (Cypress Reflow Profile)				
RoHS Compliant	N Print RoHS Certificate of Compliance				
PB Free	Y				
Lead/Ball Finish	Sn/Pb				
Marking	Cypress Marking Format				
Device Qualification Reports					
FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.					
QTP# 124902 16-mbit asynchronous sram family ull65nm (ll65up-25odr) technology, umc fab 12a.pdf Last Update: Sep 21, 2015					
TECHNICAL DOCUMENTS					
Application Notes (2)					
AN88889 - Mitigating Single-Event Upsets Using Cypress's 65-nm Asynchronous SRAM May 11, 2016					
AN89371 - Power Saving With Cypress's 65-nm Asynchronous PowerSnooze™ SRAM Aug 26, 2015					

Feedback

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CORPORATE HEADQUARTERS

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CUSTOMER SERVICE SUPPORT

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8:00AM - 5:00PM (local time)
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Cypress Developer Community

INTERNATIONAL SUPPORT

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