

Applications Support & Community Buy

Home > Products > Memory > Asynchronous SRAM > Async Micropower (MoBL®) SRAMs > CY62165E-3XWI

CY62165E-3XWI Status: In Production

 \sim Alert me about changes to this product $% \frac{1}{2}\left(\frac{1}{2}\right) =\frac{1}{2}\left(\frac{1}{2}\right) +\frac{1}{2}\left(\frac{1}{2}\right) +\frac{1}{2}\left($

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Datasheet Packaging/Ordering Inventory Quality and RoHS Technical Documents Related Products Support and Community

CY62165E-3XWI Ν Automotive Qualified 1.65 Min. Operating Voltage (V) Max. Operating Voltage (V) 5.50 Min. Operating Temp. (°C) -40 Max. Operating Temp. (°C) 85 Frequency (MHz) N/A Temp. Classification Industrial Max. Operating VCCQ (V) 5.50 Min. Operating VCCQ (V) 1.65 Speed (ns) N/A Density (Kb) 16384

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Pricing & Inventory Availability

Organization (X x Y)

1-9 unit Price* 100-249 unit Price* 250-999 unit Price* 25-99 1000 + 10-24 unit Price* unit Price* unit Price* Contact Sales

Availability	Quantity	Ships In	Order Now
Out of Stock	0	Please click here to check lead times	Check Distributors**

* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

x8 / x16

** Check Distributors: This part is sold through distributors only.

Packaging/Ordering		
No. of Pins	1	
Package Cross Section Drawing	Download	
Package Carrier Drawing / Orientation	Package Orientation	
Standard Pack Quantity	Contact Sales	
Minimum Order Quantity (MOQ)	1	
Order Increment	1	
Estimated Lead Time (days)	21	
HTS Code	8542.90.0000	
ECCN	3A991	
ECCN Suball	(B.2.A.)	

Quality and RoHS Moisture Sensitivity Level (MSL)	5
Moisture Selfsitivity Level (MSL)	5
Peak Reflow Temp. (°C)	N/A (Cypress Reflow Profile)
RoHS Compliant	N Print RoHS Certificate of Compliance
PB Free	N
Lead/Ball Finish	N/A
Marking	Cypress Marking Format

FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report. Please click here.

Technical Documents

Application Notes (2)

4517 - Design Recommendation for Battery-Backed SRAMs Using Cypress MoBL® SRAMs Last Update: 24/06/2014 AN6081 - Interfacing 90-nm Cypress Asynchronous SRAMs in Legacy Systems Last Update: 19/12/2012

Product Change Notice (PCN) (2) PCN125176 Last Update: 03/12/2012 PCN115016 Last Update: 20/01/2011 Product Information Notice (PIN) (3)

PIN145296 Last Update: 16/05/2014 PIN145277 Last Update: 19/03/2014 PCN125157 Last Update: 19/06/2012

White Papers (1) SRAM Board Design Guidelines Last Update: 14/04/2014

Support and Community

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