

September 2009

MOC119M Photodarlington Optocoupler (No Base Connection)

Features

- High current transfer ratio of 300%
- No base connection for improved noise immunity
- Underwriters Laboratory (UL) recognized File #E90700
- IEC 60747-5-2 approval available as a test option add option 'V' (e.g., MOC119VM)

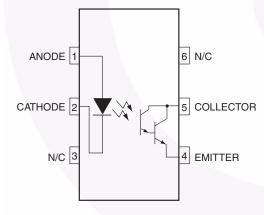
Applications

- Appliances, measuring instruments
- I/O interface for computers
- Programmable controllers
- Portable electronics
- Interfacing and coupling systems of different potentials and impedance
- Solid state relays

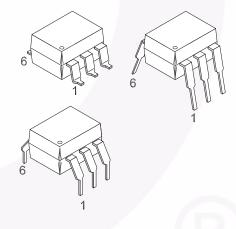
Description

The MOC119M device has a gallium arsenide infrared emitting diode coupled to a silicon darlington phototransistor.

Schematic



Package Outlines



Absolute Maximum Ratings (T_A = 25°C unless otherwise specified.)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Units
TOTAL DEVICE			
T _{STG}	Storage Temperature	-40 to +150	°C
T _{OPR}	Operating Temperature	-40 to +100	°C
T _{SOL}	Lead Solder Temperature (wave solder)	260 for 10 sec	°C
P _D	Total Device Power Dissipation @ T _A = 25°C	250	mW
	Derate above 25°C	2.94	mW/°C
EMITTER			
I _F	DC/Average Forward Input Current	60	mA
V _R	Reverse Input Voltage	3	V
P _D	LED Power Dissipation @ T _A = 25°C	120	mW
	Derate above 25°C	1.41	mW/°C
DETECTOR			
V _{CEO}	Collector-Emitter Voltage	30	V
V _{ECO}	Emitter-Collector Voltage	7	V
P_{D}	Detector Power Dissipation @ T _A = 25°C	150	mW
	Derate above 25°C	1.76	mW/°C
I _C	Continuous Collector Current	150	mA

Electrical Characteristics (T_A = 25°C unless otherwise specified.)

Individual Component Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.*	Max.	Unit
EMITTER				1		
V _F	Input Forward Voltage	I _F = 10mA		1.15	1.5	V
C _{IN}	Input Capacitance	V _R = 0, f = 1MHz		18		pF
I _R	Reverse Leakage Current	V _R = 3.0V		0.05	100	μA
DETECTOR				-		
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 100μA	30			V
BV _{ECO}	Emitter-Collector Breakdown Voltage	I _E = 10μA	7			V
I _{CEO}	Collector-Emitter Dark Current	V _{CE} = 10V			100	nA

Transfer Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.*	Max.	Units
DETECTOR					•	
CTR	Current Transfer Ratio	I _F = 10mA, V _{CE} = 2V	300	450		%
V _{CE (SAT)}	Collector-Emitter Saturation Voltage	I _C = 10mA, I _F = 10mA			1	V
SWITCHING	SWITCHING TIMES					
t _{on}	Turn-on Time	$V_{CE} = 10V, R_{L} = 100\Omega,$		3.5		μs
t _{off}	Turn-off Time	I _F = 5mA		95		μs

Isolation Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.*	Max.	Units
V _{ISO}	Input-Output Isolation Voltage	f = 60Hz, t = 1 sec.	7500			Vac(pk)
R _{ISO}	Isolation Resistance	V _{I-O} = 500VDC		10 ¹¹		Ω
C _{ISO}	Isolation Capacitance	V = 0V, f = 1MHz		0.2		pF

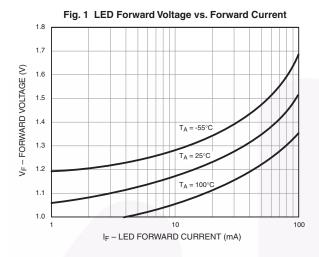
^{*}Typical values at T_A = 25°C

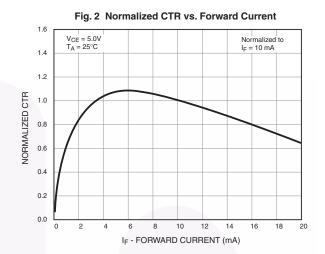
Safety and Insulation Ratings

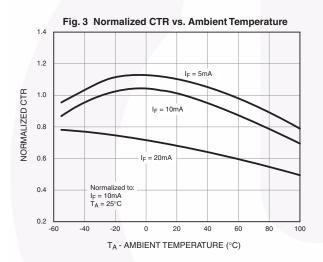
As per IEC 60747-5-2, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

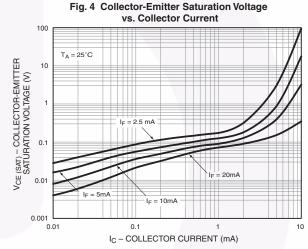
Symbol	Parameter	Min.	Тур.	Max.	Unit
	Installation Classifications per DIN VDE 0110/1.89 Table 1				
	For Rated Main Voltage < 150Vrms		I-IV		
	For Rated Main voltage < 300Vrms		I-IV		
	Climatic Classification		55/100/21		
	Pollution Degree (DIN VDE 0110/1.89)		2		
CTI	Comparative Tracking Index	175			
V _{PR}	Input to Output Test Voltage, Method b, V _{IORM} x 1.875 = V _{PR} , 100% Production Test with tm = 1 sec, Partial Discharge < 5pC	1594			V _{peak}
	Input to Output Test Voltage, Method a, V _{IORM} x 1.5 = V _{PR} , Type and Sample Test with tm = 60 sec, Partial Discharge < 5pC	1275			V _{peak}
V _{IORM}	Max. Working Insulation Voltage	850			V _{peak}
V_{IOTM}	Highest Allowable Over Voltage	6000			V _{peak}
	External Creepage	7			mm
	External Clearance	7			mm
	Insulation Thickness	0.5			mm
RIO	Insulation Resistance at Ts, V _{IO} = 500V	10 ⁹			Ω

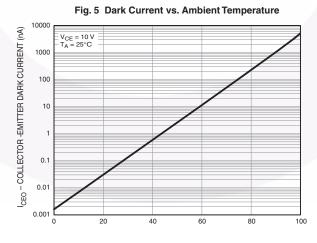
Typical Performance Curves







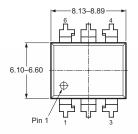


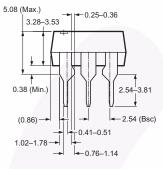


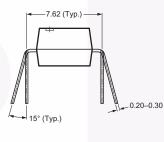
T_A – AMBIENT TEMPERATURE (°C)

Package Dimensions

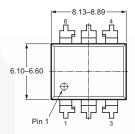
Through Hole

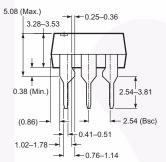


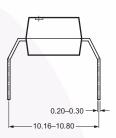




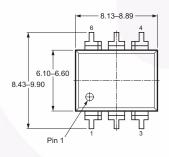
0.4" Lead Spacing

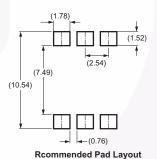


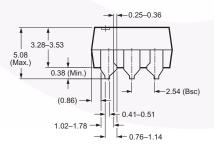


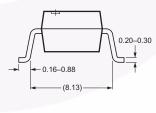


Surface Mount







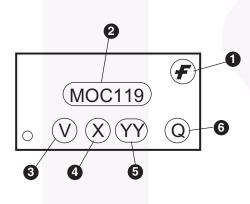


Note: All dimensions in mm.

Ordering Information

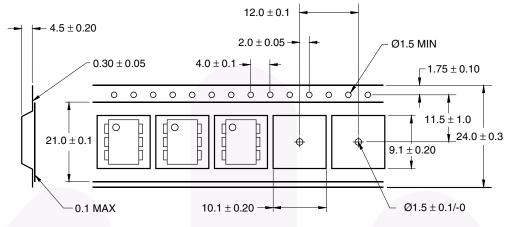
Suffix	Example	Option	
No Suffix	MOC119M	Standard Through Hole Device (50 units per tube)	
S	MOC119SM	Surface Mount Lead Bend	
SR2	MOC119SR2M	Surface Mount; Tape and Reel (1,000 units per reel)	
Т	MOC119TM	0.4" Lead Spacing	
V	MOC119VM	IEC60747-5-2 approved	
TV	MOC119TVM	IEC60747-5-2 approved, 0.4" Lead Spacing	
SV	MOC119SVM	IEC60747-5-2 approved, Surface Mount	
SR2V	MOC119SR2VM	IEC60747-5-2 approved, Surface Mount, Tape & Reel (1,000 units per reel)	

Marking Information



Definitions				
1	Fairchild logo			
2	Device number			
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)			
4	One digit year code, e.g., '7'			
5	Two digit work week ranging from '01' to '53'			
6	Assembly package code			

Tape Dimensions

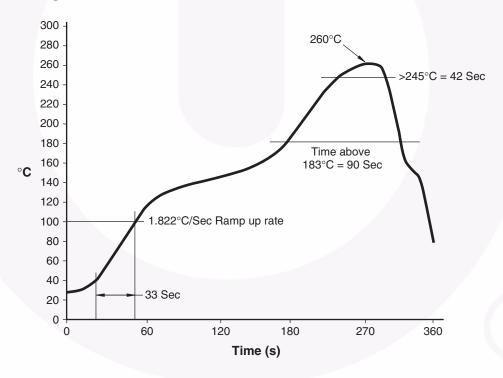


User Direction of Feed ----

Note:

All dimensions are in millimeters.

Reflow Soldering Profile







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Definition of Terms

Sommetion of Termo				
Datasheet Identification	Product Status	Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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