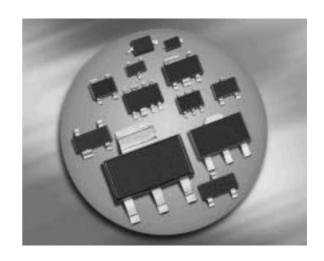


Medium Power AF Schottky Diode

Forward current: 750 mA
Reverse voltage: 40 V

- For low-loss, fast-recovery, meter protection, bias isolation and clamping applications
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101





BAT165



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Package	Configuration	Marking
BAT165	SOD323	single	C/White

Maximum Ratings at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Diode reverse voltage ²⁾	V_{R}	40	V
Forward current ²⁾	1 _F	750	mA
Average rectified forward current (50/60Hz, sinus)	I _{FAV}	500	mA
Non-repetitive peak surge forward current	/ _{FSM}	2.5	Α
(<i>t</i> ≤ 10ms)			
Total power dissipation	P _{tot}	600	mW
<i>T</i> _S ≤ 93°C			
Junction temperature	T_{j}	150	°C
Storage temperature	$T_{\rm stg}$	-65 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ³⁾	R _{thJS}	≤ 95	K/W

¹Pb-containing package may be available upon special request

²For $T_A > 25$ °C the derating of V_R and I_F has to be considered. Please refer to the atteched curves.

 $^{^3}$ For calculation of R_{thJA} please refer to Application Note Thermal Resistance



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

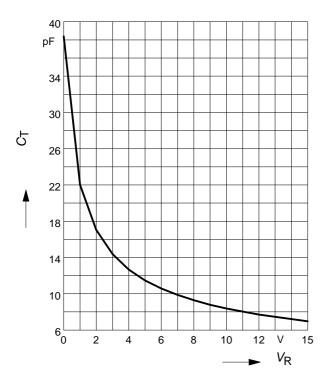
Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
DC Characteristics					1
Reverse current ¹⁾	I _R				μΑ
$V_{R} = 30 \text{ V}$		-	-	12	
$V_{R} = 40 \text{ V}$		-	-	50	
$V_{R} = 40 \text{ V}, T_{A} = 65 ^{\circ}\text{C}$		-	-	900	
Forward voltage	V _F				V
$I_{\rm F} = 10 \text{mA}$		0.23	0.315	0.4	
$I_{\rm F} = 100 \text{mA}$		0.32	0.39	0.47	
$I_{\rm F} = 250 \text{mA}$		0.35	0.44	0.54	
$I_{\rm F} = 750 \text{mA}$		0.44	0.58	0.74	
AC Characteristics					
Diode capacitance	C _T	-	8.4	12	pF
$V_{R} = 10 \text{ V}, f = 1 \text{ MHz}$					

¹Pulsed test: $t_p = 300 \ \mu s; \ D = 0.01$



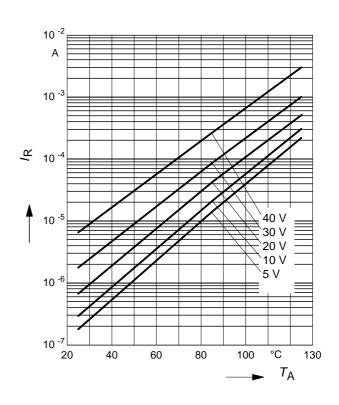
Diode capacitance $C_T = f(V_R)$

f = 1MHz



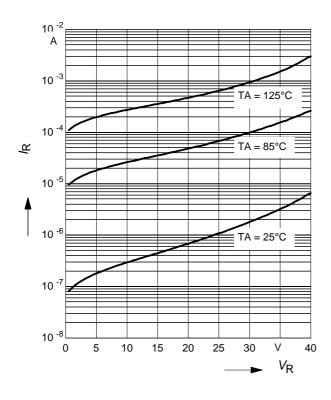
Reverse current $I_R = f(T_A)$

 V_{R} = Parameter



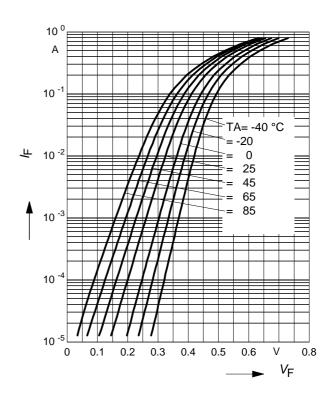
Reverse current $I_R = f(V_R)$

 T_A = Parameter



Forward current $I_F = f(V_F)$

 T_A = Parameter

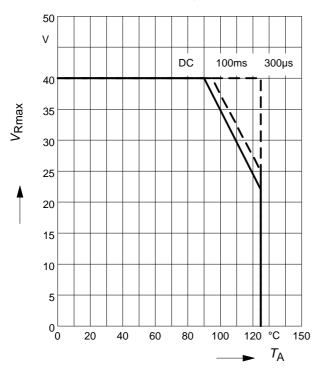




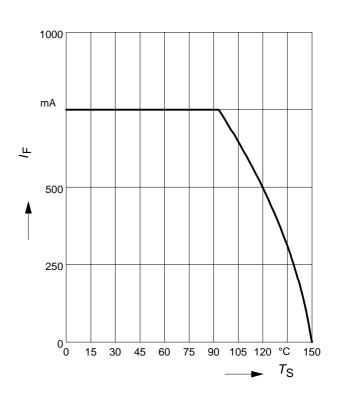
Permissible Reverse voltage $V_R = f(T_A)$

 t_p = Parameter, Duty cycle < 0.01

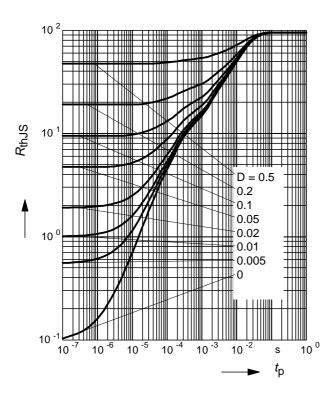
Device mounted on PCB with R_{th} = 160 k/W



Forward current $I_F = f(T_S)$

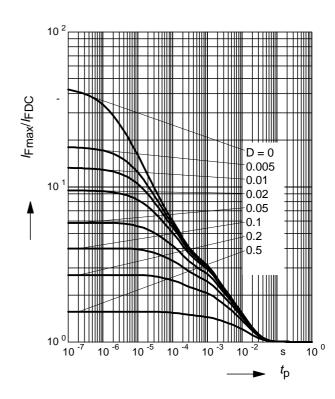


Permissible Puls Load $R_{thJS} = f(t_p)$



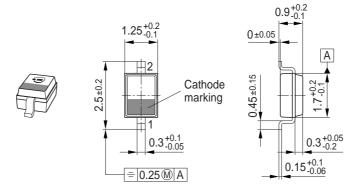
Permissible Pulse Load

$$I_{\text{Fmax}}/I_{\text{FDC}} = f(t_{\text{p}})$$

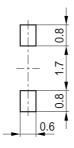




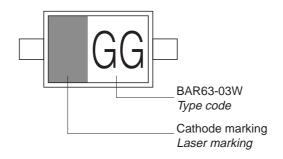
Package Outline



Foot Print

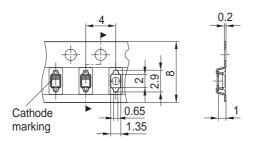


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel



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