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Vishay Siliconix

N-Channel 200 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A)	Q _g (TYP.)	
200	0.017 at V _{GS} = 10 V	90	64 nC	
200	0.018 at V _{GS} = 7.5 V	88	04 IIC	



Ordering Information:

SUP90140E-GE3 (lead (Pb)-free and halogen-free)

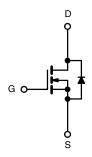
FEATURES

- ThunderFET® power MOSFET
- Maximum 175 °C junction temperature
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- · Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- · Motor drive switch
- DC/AC inverter
- · Solar micro inverter
- Class D audio amplifier



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V _{DS}	200	V		
Gate-Source Voltage		V _{GS}			± 20
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 25 °C	I _D	90	A	
Continuous Drain Current (1) = 130 C)	T _C = 70 °C	טי	75		
Pulsed Drain Current (t = 100 μs)	I _{DM}	240			
Avalanche Current	L = 0.1 mH	I _{AS}	60		
Single Avalanche Energy ^a	L = 0.111111	E _{AS}	180	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	375 b	W	
Maximum Fower Dissipation	T _C = 125 °C	FD	125 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.4	C/VV	

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V_{DS} $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_D=250\;\mu A$	2	-	4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA	
		V _{DS} = 200 V, V _{GS} = 0 V	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_J = 125 °C	-	-	150	μA	
		$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	5	mA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	90	-	-	Α	
Drain Course On State Decistor 2	В	V _{GS} = 10 V, I _D = 30 A	-	0.0138	0.0170	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 7.5 \text{ V}, I_D = 30 \text{ A}$	-	0.0141	0.0180		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	-	75	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	4132	-	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 100 \text{ V}, f = 1 \text{ MHz}$	-	246	-		
Reverse Transfer Capacitance	C _{rss}		-	21	-		
Total Gate Charge ^c	Q_g		-	64	96	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	-	16.7	-		
Gate-Drain Charge ^c	Q_{gd}		-	16.9	-		
Gate Resistance	R_g	f = 1 MHz	1.5	3	5	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	13	26		
Rise Time ^c	t _r	$V_{DD} = 100 \text{ V}, R_{I} = 1.66 \Omega$	-	112	200		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 60 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	35	70	ns	
Fall Time ^c	t _f		-	80	150		
Drain-Source Body Diode Ratings at	nd Characteri	stics ^b (T _C = 25 °C)					
Pulsed Current (t = 100 μs)	I _{SM}		-	-	240	Α	
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V	-	0.8	1.2	V	
Reverse Recovery Time	t _{rr}		-	160	320	ns	
Peak Reverse Recovery Charge	I _{RM(REC)}	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	11	20	Α	
Reverse Recovery Charge	Q _{rr}		-	0.9	1.8	μC	

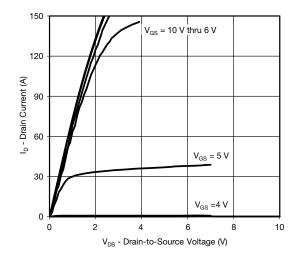
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

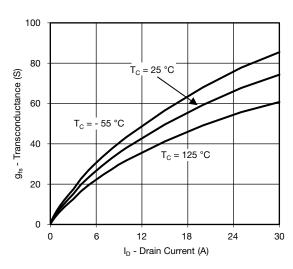
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



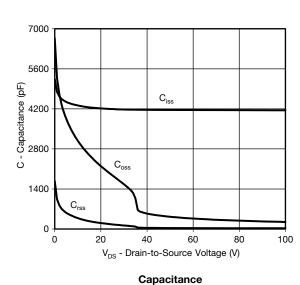
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

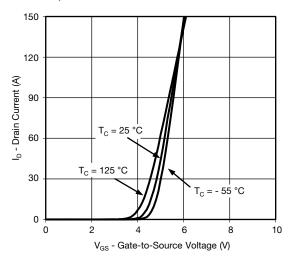


Output Characteristics

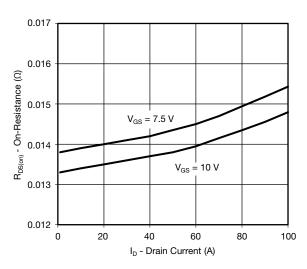


Transconductance

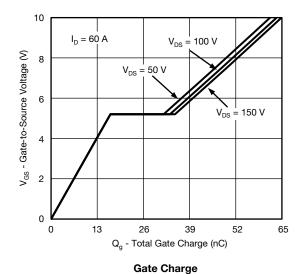




Transfer Characteristics



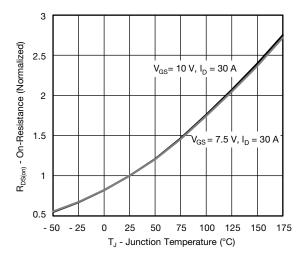
On-Resistance vs. Drain Current



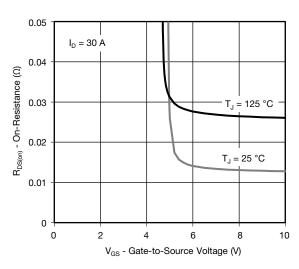
S15-2641-Rev. A, 16-Nov-15 3 Document Number: 79036



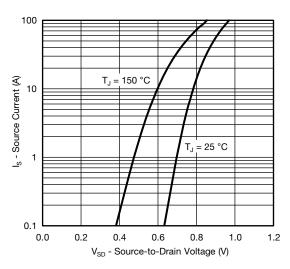
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



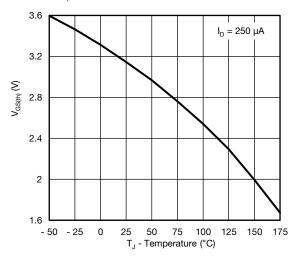
On-Resistance vs. Junction Temperature



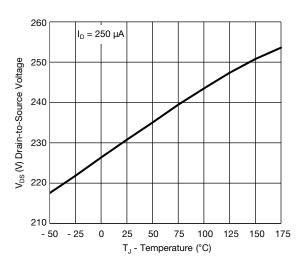
On-Resistance vs. Gate-to-Source Voltage



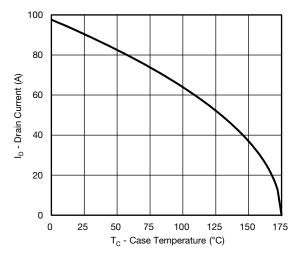
Source Drain Diode Forward Voltage



Threshold Voltage



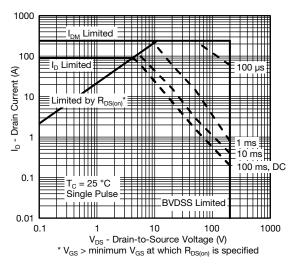
Drain Source Breakdown vs. Junction Temperature

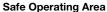


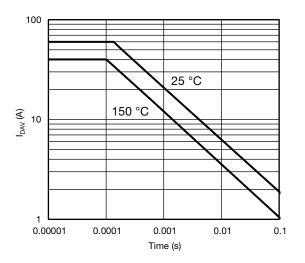
Current De-rating



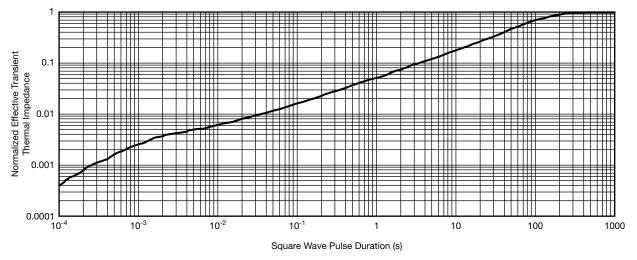
THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)







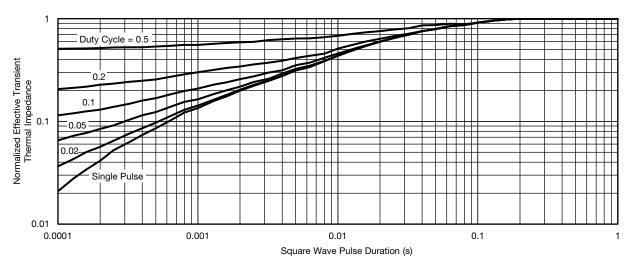
Single Pulse Avalanche Current Capability vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

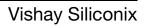


Normalized Thermal Transient Impedance, Junction-to-Case

Note

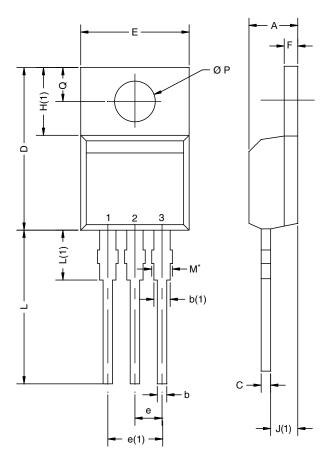
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg279036.





TO-220AB



	D2

	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

Note

Revison: 16-Jun-14 1 Document Number: 71195

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM

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