



SPECIFICATION FOR APPROVAL

CUSTOMER	
NOMINAL FREQUENCY	25.000000 MHz
HOLDER TYPE	TYPE FK 3.2x2.5 SEAM SEALED CRYSTAL CLOCK OSCILLATOR
SPEC. NO. (P/N)	FK2500070
CUSTOMER P/N	
ISSUE DATE	May 18, 2018
VERSION	В

APPROVED	PREPARED	QA
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- *Pb-free
- *RoHS Compliant
- *HF-Halogen Free
- *REACH Compliant

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VERSION HISTORY

Version No.	Version Date	Description	Notes
Α	Mar.11,2014	Initial Release	
В	May.18,2018	Updated Logo	

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ELECTRICAL SPECIFICATIONS

SRe Part Number: FK2500070

Item	Symbol	Specifications	Units	Notes
Nominal Frequency	Fo	25.000000	MHz	
Frequency Stability	FT	± 25	ppm	**See note
Operating Temperature Range	TR	-40 to +85	C	
Supply Voltage	V_{DD}	+1.5 ± 5.0%	V	
Logic Type	LT	LVCMOS		
Supply Current, Output Enabled	I _{DD} /OE	5	mA	Max.
Supply Current, Output Disabled	I _{DD} /OD	10	μΑ	Max.
Duty Cycle (Symmetry)	DC/SY	45 / 55	%	Measured 50% of Waveform
Rise / Fall Time	T_R/T_F	4	ns	Max. measured 20/80% of Waveform
Output Voltage "0" Level	V_{OL}	10% V _{DD}	V	Max at I _{OL} = 4.0mA Min.
Output Voltage "1" Level	V_{OH}	90% V _{DD}	V	Min at I _{OH} = -4.0mA Max.
Output Load	CL	15	pF	Max.
Jitter, Phase	RMS	1	ps	Max. 12KHz ~ 5MHz Frequency Band
Jitter, Accumulated	RMS(1-σ)	4	ps	Max. 20,000 Consecutive Periods
Jitter, Peak to Peak	Pk-Pk	40	ps	Max. 100,000 Random Periods
Start Up Time		10	ms	Max.
Storage Temperature Range		-55 to +125	C	

[★] This product doesn't include harmful substance that stipulated by SONY SS-00259 Level 1 and S-AT2-001 Level 1 standard. RoHS Compliant (Pb - Free).

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (Pin1), Output Enable	$0.7V_{DD}$			V	Or Open
Input Voltage (Pin1), Output Disable (low power standby)			$0.3V_{DD}$	V	Output is Hi-Z
Internal Pullup Resistance	30			ΚΩ	
Output Disable Delay			50	μs	

DECDES.

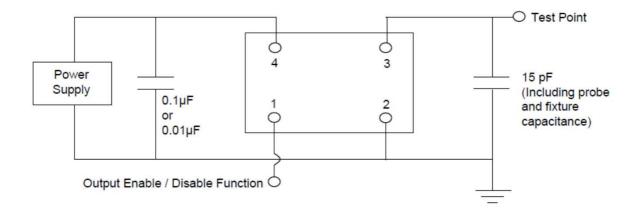
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^{**}Stability includes all combinations of Operating Temperature, Load changes, rated Input (Supply) Voltage changes, Initial Calibration Tolerance (25°C), Aging (1 year at 25°C Average Effective Ambient Temperature), Shock and Vibration.

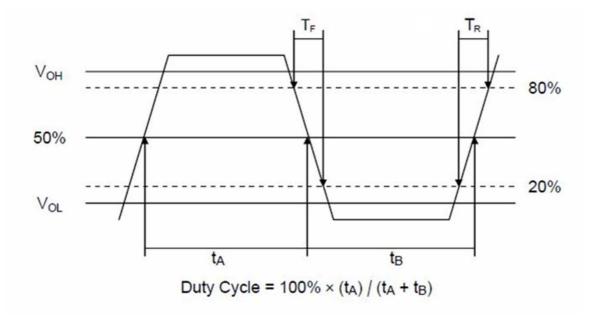
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TEST CIRCUIT



OUTPUT WAVEFORM



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RELIABILITY SPECIFICATIONS

ENVIRONMENTAL:

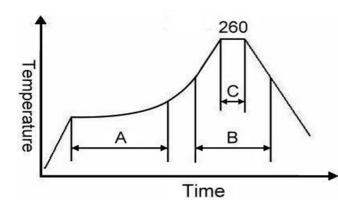
- a) THERMAL SHOCK: MIL-STD-883, Method 1011, Condition A
- b) MOISTURE RESISTANCE: MIL-STD-883, Method 1004
- c) VIBRATION: MIL-STD-883, Method 2007, Condition A
- d) RESISTANCE TO SOLDERING HEAT: J-STD-020D Table 5-2 Pb-free devices (except 2 cycles max)
- e) HAZARDOUS SUBSTANCE: Pb free and RoHS Compliant.

MECHANICAL:

- a) SHOCK: MIL-STD-883, Method 2002, Condition B
- b) SOLDERABILITY: JESD22-B102-D Method 2 (Preconditioning E)
- c) TERMINAL STRENGTH: MIL-STD-883, Method 2004, Test Condition D
- d) GROSS LEAK: MIL-STD-883, Method 1014, Condition C
- e) FINE LEAK: MIL-STD-883, Method 1014, Condition A2, R1=2x10⁻⁸ atm cc/s
- f) SOLVENT RESISTANCE: MIL-STD-202, Method 215

SUGGESTED IR REFLOW PROFILE

*As per IPC-JEDEC J-STD-020D



Note	Note:					
	Stage	Temperature	Time			
Α	Preheat	150~200°C	60~120 Sec			
В	Primary Heat	217°C	60~150 Sec			
С	Peak	260°C	10 Sec			

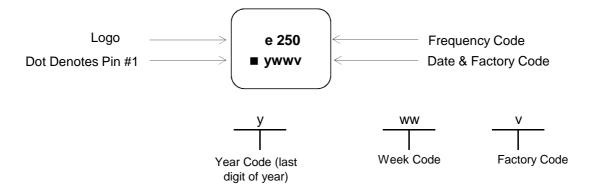
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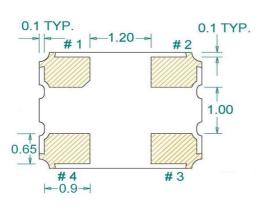
MARKING



MECHANICAL DRAWINGS (Scale: None. Dimensions are in mm.)

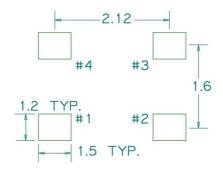
3.2 ± 0.1 2.5 ± 0.1





(Bottom View)

Recommended Land Pattern*



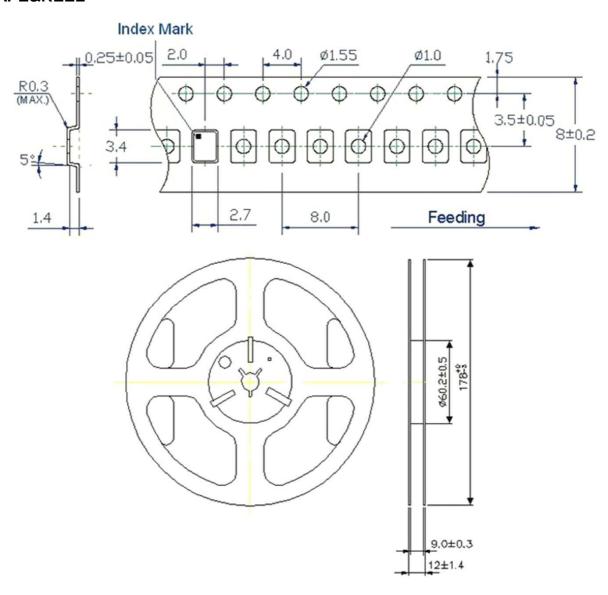
*External high-frequency power decoupling is recommended.(see test circuit for minimum recommendation). To ensure optimal performance, do not route traces beneath the package.

Pin	Function
1	OE
2	Ground
3	Clock Output
4	V_{DD}

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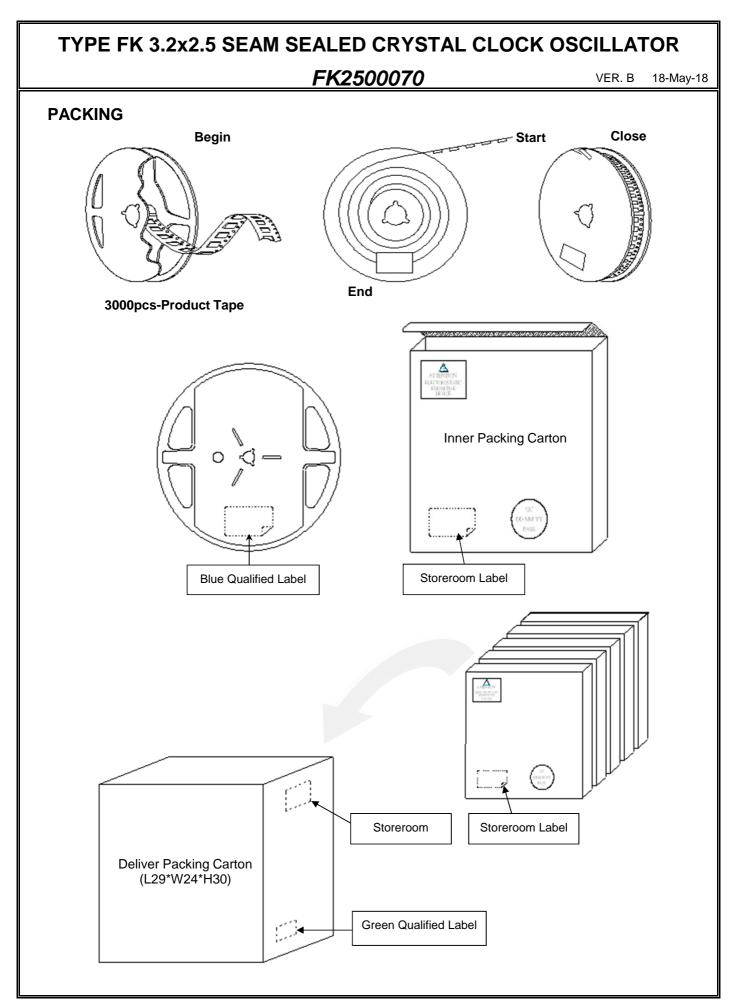
TAPE&REEL



- 1. 230mm minimum leafer which consist of carrier and/or tape followed by a minimum of 160mm of empty carrier tape sealed with cover tape.
- 2. 160mm minimum trailer of empty carrier tape sealed with cover tape.



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