

PCIMX7D3DVK10SB Information

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General Information

Parameter	Value
Part Number	PCIMX7D3DVK10SB
Description	i.MX 7Dual:2x Cortex A7, 2x USB 2.0 OTG with PHY, PCIe, 2xSDIO/MMC, 2x Ethernet, Security
Product Line	GEVJMX71
PTI	MVMG
Material Type	Tested Packaged Device
Life Cycle Description (code)	PRODUCT NEWLY INTRO D/RAMP-UP
Status	Active

Package Information

Parameter	Value
Package Type and Termination Count	MAPBGA 488
Package Description and Mechanical Drawing	MAPBGA 488 12*12*1.1P0.4
Package Length (nominal)(mm)	12.000
Package Width (nominal)(mm)	12.000
Package Thickness (nominal)(mm)	1.040
Tape & Reel	No

Environmental and Compliance Information

Parameter	Value
Pb-Free	Contact Us
RoHS Compliant	Contact Us
Material Composition Declaration (MCD)	Contact Us
RoHS Certificate of Analysis (CoA)	Contact Us
2nd Level Interconnect	Contact Us
Moisture Sensitivity Level (MSL)	3
Floor Life	168 HOURS
Peak Package Body Temperature (PPT)(°C)	260
Maximum Time at Peak Temperature (s)	40
Number of Reflow Cycles	3
REACH SVHC	NXP REACH Statement

Ordering Information

Parameter	Value
Minimum Package Quantity (MPQ)	189
MPQ Container	TRAY
Exempt from Minimum Delivery Value	Yes
Preferred Order Quantity (POQ)	945
POQ Container	BRICK
Leadtime (weeks)	17
Export Control Classification Number (US)	5A992
Harmonized Tariff (US) Disclaimer	8542.31.0000

ENC Status	-
Other Trade Compliance Documents	-
Budgetary Price excluding tax(US\$) Change Currency	-
Order	

Operating Characteristics

Parameter	Value
Sample Exception Availability	N