RoHS

COMPLIANT HALOGEN

FREE

Thyristor High Voltage, Phase Control SCR, 40 A



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PRIMARY CHARACTERISTICS							
I _{T(AV)}	35 A						
V _{DRM} /V _{RRM}	800 V, 1200 V						
V _{TM}	1.45 V						
I _{GT}	150 mA						
TJ	-40 °C to +125 °C						
Package	TO-247AC 3L						
Circuit configuration	Single SCR						

FEATURES

- · Designed and gualified according to JEDEC[®]-JESD 47
- Low IGT parts available
- 125 °C max. operating junction temperature
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch motor control, UPS, welding and battery charge

DESCRIPTION

The VS-40TPS... high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

MAJOR RATINGS AND CHARACTERISTICS									
PARAMETER	TEST CONDITIONS	VALUES	UNITS						
I _{T(AV)}	Sinusoidal waveform	35	٨						
I _{RMS}		55	— A						
V _{RRM} /V _{DRM}		800 to 1200	V						
I _{TSM}		600	A						
V _T	40 A, T _J = 25 °C	1.45	V						
dV/dt		1000	V/µs						
dl/dt		100	A/µs						
TJ		-40 to +125	°C						

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
VS-40TPS08A-M3	800	900				
VS-40TPS08-M3	800	900	10			
VS-40TPS12A-M3	1200 1300					
VS-40TPS12-M3	1200	1300				



VS-40TPS...-M3 Series



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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TI		VALUES	UNITS	
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° cor	T_{C} = 79 °C, 180° conduction half sine wave			
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}				55	А
Maximum peak, one-cycle		10 ms sine pulse, ra	ted V _{RRM} applied		500	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no	voltage reapplied	1 - 11 - 1	600	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, ra	ted V _{RRM} applied	Initial $T_{ij} = T_{ij} max.$	1250	A ² s
Maximum tion fusing		10 ms sine pulse, no	voltage reapplied		1760	A-S
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms,	no voltage reapplied		17 600	A²√s
Low level value of threshold voltage	V _{T(TO)1}			1.02	V	
High level value of threshold voltage	V _{T(TO)2}	T _J = 125 °C		1.23	v	
Low level value of on-state slope resistance	r _{t1}				9.74	mΩ
High level value of on-state slope resistance	r _{t2}		7.50	1115.2		
Maximum peak on-state voltage	V _{TM}	110 A, T _J = 25 °C			1.85	V
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C			100	A∕µs
Maximum holding current	I _H	Anode supply = 6 V,	resistive load, initial T _J	= 1 A, I _T = 25 °C	200	
Maximum latching current	١L	Anode supply = 6 V, resistive load, $T_J = 25 \degree C$		300		
		T _J = 25 °C	V _R = Rated V _{RRM} /V _{DRM}		0.5	mA
Maximum reverse and direct leakage current	I _{RRM} /I _{DRM}	T _J = 125 °C			10	
Maximum rate of rise of off-state voltage 40TPS12A	dV/dt	$T_J = T_J$ maximum, linear to 80 % V _{DRM} , R _g - k = 100 Ω		500	V/µs	
Maximum rate of rise of off-state voltage 40TPS12	αν/αι	ıj = ıjmaxınum, ır	$- \kappa = 100.52$	1000	v/µS	

TRIGGERING						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
Maximum peak gate power	P _{GM}			10	W	
Maximum average gate power	P _{G(AV)}			2.5	vv	
Maximum peak gate current	I _{GM}			2.5	А	
Maximum peak negative gate voltage	- V _{GM}			10	V	
		T _J = - 40 °C		4.0		
Maximum required DC gate voltage to trigger	V_{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	2.5	V	
		T _J = 125 °C		1.7		
		T _J = - 40 °C		270	mA	
Maximum required DC acts surrent to triager	I _{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	150		
Maximum required DC gate current to trigger		T _J = 125 °C		80		
		$T_J = 25 \ ^{\circ}C$, for 40TPSAPb	40			
Maximum DC gate voltage not to trigger for 40TPS12	V_{GD}	T 105 °C V retod	velue.	0.25	V	
Maximum DC gate current not to trigger for 40TPS12	I_{GD}	T _J = 125 °C, V _{DRM} = rated value		6	mA	
Maximum DC gate voltage not to trigger for 40TPS12A	V_{GD}	T do5 20 M and all a		0.15	V	
Maximum DC gate current not to trigger for 40TPS12A	I _{GD}	$I_{\rm J} = 125$ C, $v_{\rm DRM} = rated V$	$J_{\rm J}$ = 125 °C, V _{DRM} = rated value			

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THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS			
Maximum junction and sto temperature range	rage	T _J , T _{Stg}		-40 to +125	°C			
Maximum thermal resistan junction to case	ce,	R _{thJC}	DC operation	0.6				
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	40	°C/W			
Maximum thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2				
Approximate weight				6	g			
Approximate weight				0.21	oz.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf ⋅ in)			
				40TPS08A				
Marking davias				40TPS12A				
Marking device			Case style TO-247AC 3L	40TPS08				
				40TPS12				

Maximum Average On-State Power Loss (W)

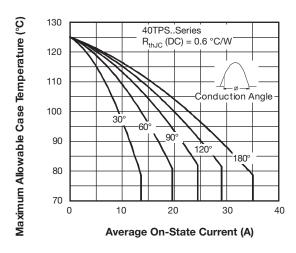


Fig. 1 - Current Rating Characteristics

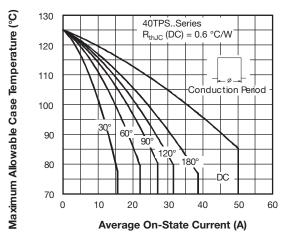


Fig. 2 - Current Rating Characteristics

60 180 120 50 90 60° 40 30 RMS Limit 30 20 Conduction Angle 10 40TPS..Series T_{.1} = 125 °C 0 0 5 10 15 20 25 30 35 40 Average On-State Current (A)

Fig. 3 - On-State Power Loss Characteristics

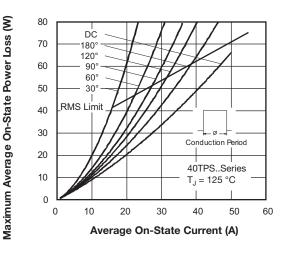


Fig. 4 - On-State Power Loss Characteristics

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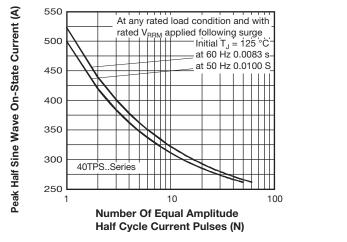


Fig. 5 - Maximum Non-Repetitive Surge Current

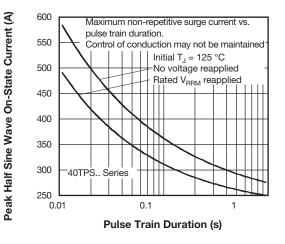


Fig. 6 - Maximum Non-Repetitive Surge Current

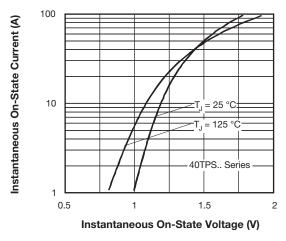
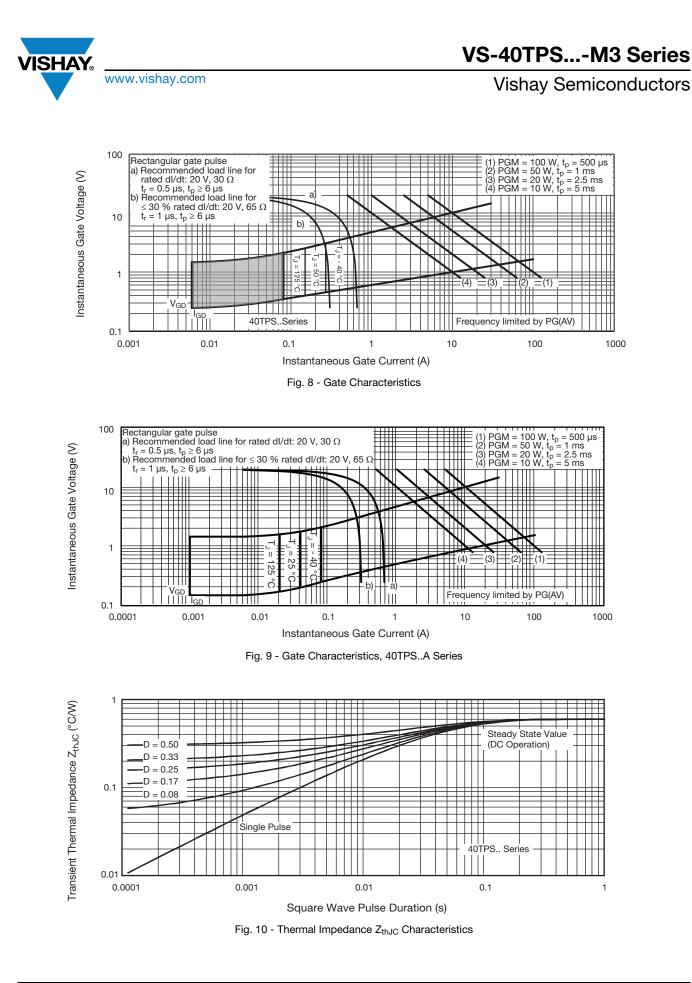


Fig. 7 - On-State Voltage Drop Characteristics

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ORDERING INFORMATION TABLE

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ISHA

Device and	1/0	40	-	-	•	10		
Device code	VS-	40	Т	P	S	12	A	-M3
	1	2	3	4	5	6	7	8
	1 - 2 -			iiconduo ng (40 =	ctors pro	oduct		
	3 -	Circ		iguratio				
	4 -		kage: TO-247	AC 3L				
	5 -		e of silio standar		ery recti	fier		08 =
	6 - 7 -		age rati = low l _o	-	tion 40 ı	nA max	timum	12 = 1
	8 -			tandard ntal digit	lgt sele :	ction		
		-M3	8 = halog	gen-free	, RoHS-	complia	ant, anc	l termin

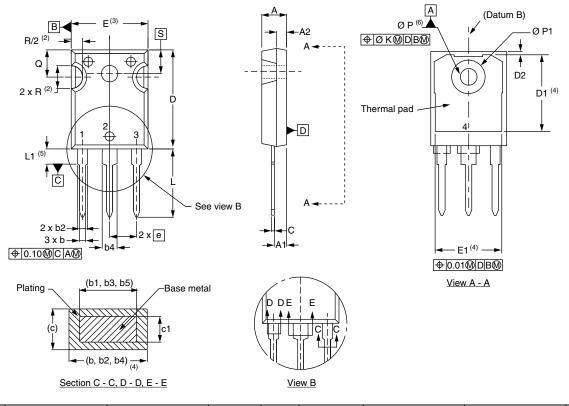
ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-40TPS08A-M3	25	500	Antistatic plastic tubes					
VS-40TPS08-M3	25	500	Antistatic plastic tubes					
VS-40TPS12A-M3	25	500	Antistatic plastic tubes					
VS-40TPS12-M3	25	500	Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96138				
Part marking information	www.vishay.com/doc?95007				



TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NUTES	STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension Q

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