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April 1988 Revised September 2000

# 74F153 Dual 4-Input Multiplexer

# **General Description**

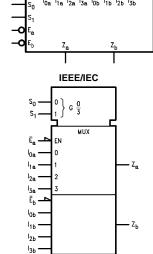
The F153 is a high-speed dual 4-input multiplexer with common select inputs and individual enable inputs for each section. It can select two lines of data from four sources. The two buffered outputs present data in the true (non-inverted) form. In addition to multiplexer operation, the F153 can generate any two functions of three variables.

# **Ordering Code:**

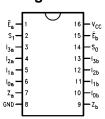
Order Number	Package Number	Package Description
74F153SC	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
74F153SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74F153PC	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

# **Logic Symbols**



# **Connection Diagram**



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DS009482

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# **Unit Loading/Fan Out**

Pin Names	Description	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>	
FIII Names	Description	HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>	
I <sub>0a</sub> –I <sub>3a</sub>	Side A Data Inputs	1.0/1.0	20 μA/–0.6 mA	
I <sub>0b</sub> -I <sub>3b</sub>	Side B Data Inputs	1.0/1.0	20 μA/–0.6 mA	
S <sub>0</sub> , S <sub>1</sub>	Common Select Inputs	1.0/1.0	20 μA/–0.6 mA	
Ea	Side A Enable Input (Active LOW)	1.0/1.0	20 μA/–0.6 mA	
E <sub>b</sub>	Side B Enable Input (Active LOW)	1.0/1.0	20 μA/–0.6 mA	
Z <sub>a</sub>	Side A Output	50/33.3	−1 mA/20 mA	
Z <sub>b</sub>	Side B Output	50/33.3	–1 mA/20 mA	

#### **Truth Table**

Select	Inputs		Output					
S <sub>0</sub>	S <sub>1</sub>	Ē	I <sub>0</sub>	I <sub>1</sub>	l <sub>2</sub>	I <sub>3</sub>	z	
Х	Χ	Н	Χ	Х	Х	Χ	L	
L	L	L	L	Χ	Χ	Χ	L	
L	L	L	Н	Χ	Х	Χ	Н	
Н	L	L	Χ	L	Χ	Χ	L	
Н	L	L	Х	Н	Х	Χ	Н	
L	Н	L	Х	Х	L	Χ	L	
L	Н	L	Х	Χ	Н	Χ	Н	
Н	Н	L	Х	Χ	Х	L	L	
Н	Н	L	Х	Х	Х	Н	Н	

H = HIGH Voltage Level

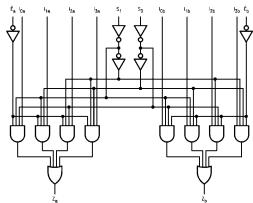
#### **Functional Description**

The F153 is a dual 4-input multiplexer. It can select two bits of data from up to four sources under the control of the common Select inputs  $(S_0,\,S_1).$  The two 4-input multiplexer circuits have individual active LOW Enables  $(\overline{E}_a,\,\overline{E}_b)$  which can be used to strobe the outputs independently. When the Enables  $(\overline{E}_a,\,\overline{E}_b)$  are HIGH, the corresponding outputs  $(Z_a,\,Z_b)$  are forced LOW. The F153 is the logic implementation of a 2-pole, 4-position switch, where the position of the switch is determined by the logic levels supplied to the two Select inputs. The logic equations for the outputs are as follows:

$$\begin{split} Z_{a} &= \overline{E}_{a} \bullet (I_{0a} \bullet \overline{S}_{1} \bullet \overline{S}_{0} + I_{1a} \bullet \overline{S}_{1} \bullet S_{0} + \\ I_{2a} \bullet S_{1} \bullet \overline{S}_{0} + I_{3a} \bullet S_{1} \bullet S_{0} \\ Z_{b} &= \overline{E}_{b} \bullet (I_{0b} \bullet \overline{S}_{1} \bullet \overline{S}_{0} + I_{1b} \bullet \overline{S}_{1} \bullet S_{0} + \\ I_{2b} \bullet S_{1} \bullet \overline{S}_{0} + I_{3b} \bullet S_{1} \bullet S_{0}) \end{split}$$

The F153 can be used to move data from a group of registers to a common output bus. The particular register from which the data came would be determined by the state of the Select inputs. A less obvious application is as a function generator. The F153 can generate two functions of three variables. This is useful for implementing highly irregular random logic.

# **Logic Diagram**



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

L = LOW

X = Immaterial

# **Absolute Maximum Ratings**(Note 1)

Recommended Operating Conditions

 $\begin{array}{ll} \mbox{Storage Temperature} & -65\mbox{°C to } +150\mbox{°C} \\ \mbox{Ambient Temperature under Bias} & -55\mbox{°C to } +125\mbox{°C} \\ \end{array}$ 

 $\begin{array}{lll} \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \mbox{V}_{\mbox{CC}} \mbox{ Pin Potential to Ground Pin} & -0.5\mbox{V to } +7.0\mbox{V} \\ \mbox{Input Voltage (Note 2)} & -0.5\mbox{V to } +7.0\mbox{V} \\ \end{array}$ 

Input Voltage (Note 2) -0.5V to +7.0V
Input Current (Note 2) -30 mA to +5.0 mA
Voltage Applied to Output

 $\begin{array}{ll} \mbox{Standard Output} & -0.5\mbox{V to V}_{\mbox{CC}} \\ \mbox{3-STATE Output} & -0.5\mbox{V to +5.5\mbox{V}} \end{array}$ 

in LOW State (Max) twice the rated  $I_{OL}$  (mA)

Free Air Ambient Temperature  $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Supply Voltage +4.5V to +5.5V

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

in HIGH State (with  $V_{CC} = 0V$ )

Current Applied to Output

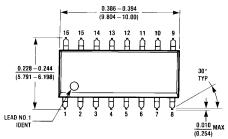
Symbol	Parameter	•	Min	Тур	Max	Units	V <sub>CC</sub>	Conditions
V <sub>IH</sub>	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal
V <sub>IL</sub>	Input LOW Voltage				0.8	V		Recognized as a LOW Signal
V <sub>CD</sub>	Input Clamp Diode Voltage				-1.2	V	Min	$I_{IN} = -18 \text{ mA}$
V <sub>OH</sub>	Output HIGH Voltage	10% V <sub>CC</sub>	2.5			V Min		$I_{OH} = -1 \text{ mA}$
		5% V <sub>CC</sub>	2.7			v	IVIIII	$I_{OH} = -1 \text{ mA}$
V <sub>OL</sub>	Output LOW Voltage	10% V <sub>CC</sub>			0.5	V	Min	I <sub>OL</sub> = 20 mA
I <sub>IH</sub>	Input HIGH Current				5.0	μΑ	Max	$V_{IN} = 2.7V$
I <sub>BVI</sub>	Input HIGH Current Breakdown Test				7.0	μΑ	Max	V <sub>IN</sub> = 7.0V
I <sub>CEX</sub>	Output High Leakage Current				50	μΑ	Max	$V_{OUT} = V_{CC}$
V <sub>ID</sub>	Input Leakage Test		4.75			V	0.0	$I_{ID} = 1.9 \mu A$
			4.73			v	0.0	All Other Pins Grounded
I <sub>OD</sub>	Output Leakage Circuit Current				3.75	μΑ	0.0	V <sub>IOD</sub> = 150 mV
								All Other Pins Grounded
I <sub>IL</sub>	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$
los	Output Short-Circuit Curre	nt	-60		-150	mA	Max	V <sub>OUT</sub> = 0V
I <sub>CCL</sub>	Power Supply Current			12	20	mA	Max	$V_O = LOW$

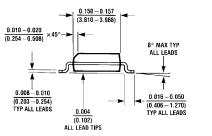
#### **AC Electrical Characteristics**

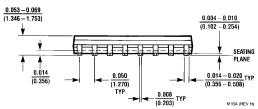
Symbol	Parameter	$T_{A} = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$			$T_{A} = 0^{\circ}C \text{ to } +70^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$		Units	
		Min	Тур	Max	Min	Max	Ī	
t <sub>PLH</sub>	Propagation Delay	4.5	8.1	10.5	4.5	12.0	ns	
t <sub>PHL</sub>	S <sub>n</sub> to Z <sub>n</sub>	3.5	7.0	9.0	3.5	10.5	115	
t <sub>PLH</sub>	Propagation Delay	4.5	7.1	9.0	4.5	10.5		
t <sub>PHL</sub>	$\overline{E}_n$ to $Z_n$	3.0	5.7	7.0	2.5	8.0	ns	
t <sub>PLH</sub>	Propagation Delay	3.0	5.3	7.0	3.0	8.0	ns	
t <sub>PHL</sub>	I <sub>n</sub> to Z <sub>n</sub>	2.5	5.1	6.5	2.5	7.5	ns	

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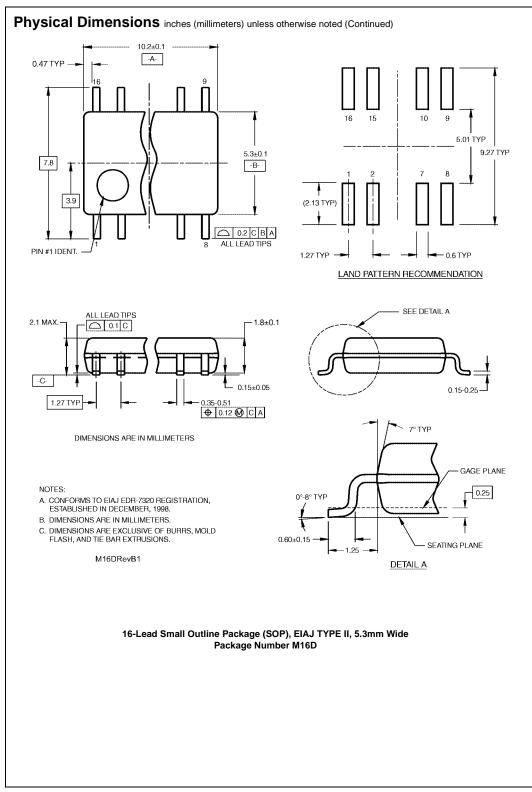
# Physical Dimensions inches (millimeters) unless otherwise noted



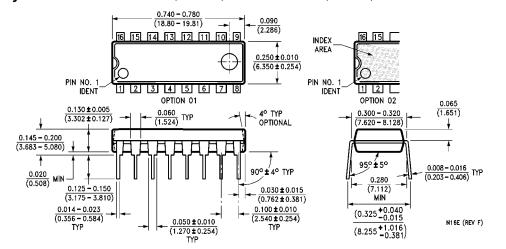




16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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