



# N-Channel 75-V (D-S) MOSFET

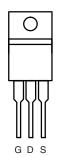
| PRODUCT SUMMARY     |                                  |                    |                       |  |
|---------------------|----------------------------------|--------------------|-----------------------|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$             | I <sub>D</sub> (A) | Q <sub>g</sub> (Typ.) |  |
| 75                  | 0.0068 at V <sub>GS</sub> = 10 V | 90 <sup>d</sup>    | 75                    |  |

#### **FEATURES**

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



#### TO-220AB

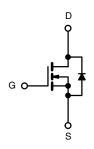


Top View

Ordering Information: SUP90N08-6m8P-E3 (Lead (Pb)-free)

#### **APPLICATIONS**

- Power Supply
  - Secondary Synchronous Rectification
- Industrial



N-Channel MOSFET

| <b>ABSOLUTE MAXIMUM RATINGS</b>                     | $T_C = 25  ^{\circ}C$ , unless oth  | erwise noted                      |                  |     |  |
|---|-------------------------------------|-----------------------------------|------------------|-----|--|
| Parameter   | Symbol                              | Limit                             | Unit             |     |  |
| Drain-Source Voltage                                | V <sub>DS</sub>                     | 75                                | V                |     |  |
| Gate-Source Voltage                                 | V <sub>GS</sub>                     | ± 22                              | V                |     |  |
| Continuous Drain Current (T <sub>.I</sub> = 175 °C) | T <sub>C</sub> = 25 °C              | I-                                | 90 <sup>d</sup>  |     |  |
| Continuous Diam Current (1 <sub>J</sub> = 175 C)    | T <sub>C</sub> = 70 °C              | I <sub>D</sub>                    | 90 <sup>d</sup>  | ^   |  |
| Pulsed Drain Current                                |                                     | I <sub>DM</sub>                   | 240              | A   |  |
| Avalanche Current                                   | I <sub>AS</sub>                     | 50                                |                  |     |  |
| Single Avalanche Energy <sup>a</sup>                | L = 0.1 mH                          | E <sub>AS</sub>                   | 125              | mJ  |  |
| Maximum Power Dissipation <sup>a</sup>              | T <sub>C</sub> = 25 °C              |                                   | 272 <sup>b</sup> | 14/ |  |
|   | T <sub>A</sub> = 25 °C <sup>c</sup> | P <sub>D</sub>                    | 3.75             | W   |  |
| Operating Junction and Storage Temperature Range    |                                     | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 175      | °C  |  |

| THERMAL RESISTANCE RATINGS                   |                   |       |        |  |  |
|--|-------------------|-------|--------|--|--|
| Parameter                                    | Symbol            | Limit | Unit   |  |  |
| Junction-to-Ambient (PCB Mount) <sup>c</sup> | R <sub>thJA</sub> | 40    | - °C/W |  |  |
| Junction-to-Case (Drain)                     | R <sub>thJC</sub> | 0.55  |        |  |  |

#### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.c. When Mounted on 1" square PCB (FR-4 material).
- d. Package limited.

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# **SUP90N08-6m8P**

# Vishay Siliconix



| SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted |                      |   |      |        |          |      |  |
|---|----------------------|---|------|--------|----------|------|--|
| Parameter   | Symbol               | Test Conditions   | Min. | Тур.   | Max.     | Unit |  |
| Static  |                      |   | I    |        | T I      |      |  |
| Drain-Source Breakdown Voltage                                | $V_{DS}$             | $V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$  | 75   |        |          | V    |  |
| Gate Threshold Voltage  | V <sub>GS(th)</sub>  | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$  | 2.5  |        | 4.5      |      |  |
| Gate-Body Leakage   | $I_{GSS}$            | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$   |      |        | ± 250    | nA   |  |
| Zero Gate Voltage Drain Current                               |                      | $V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}$   |      |        | 1        |      |  |
|   | I <sub>DSS</sub>     | $V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$                             |      |        | 50       | μΑ   |  |
|   |                      | $V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$                       |      |        | 250      |      |  |
| On-State Drain Current <sup>a</sup>                           | I <sub>D(on)</sub>   | $V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$  | 70   |        |          | Α    |  |
| Drain-Source On-State Resistance <sup>a</sup>                 | В                    | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A   |      | 0.0056 | 0.0068   | Ω    |  |
|   | R <sub>DS(on)</sub>  | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C                            |      | 0.009  | 0.011    |      |  |
| Forward Transconductance <sup>a</sup>                         | 9 <sub>fs</sub>      | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A   |      | 50     |          | S    |  |
| Dynamic <sup>b</sup>  | •                    |   |      | •      |          |      |  |
| Input Capacitance   | C <sub>iss</sub>     |   |      | 4620   |          | pF   |  |
| Output Capacitance  | C <sub>oss</sub>     | $V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$                                  |      | 517    |          |      |  |
| Reverse Transfer Capacitance                                  | C <sub>rss</sub>     |   |      | 247    |          |      |  |
| Total Gate Charge <sup>c</sup>                                | $Q_g$                |   |      | 75     | 115      | nC   |  |
| Gate-Source Charge <sup>c</sup>                               | $Q_{gs}$             | $V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$                              |      | 25.5   |          |      |  |
| Gate-Drain Charge <sup>c</sup>                                | $Q_{gd}$             |   |      | 20     |          |      |  |
| Gate Resistance   | $R_{g}$              | f = 1 MHz   |      | 1.2    | 2.4      | Ω    |  |
| Turn-On Delay Time <sup>c</sup>                               | t <sub>d(on)</sub>   |   |      | 16     | 30       |      |  |
| Rise Time <sup>c</sup>  | t <sub>r</sub>       | $V_{DD}$ = 30 V, $R_L$ = 0.6 $\Omega$<br>$I_D$ $\cong$ 50 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$ |      | 11     | 20       | ns   |  |
| Turn-Off Delay Time <sup>c</sup>                              | t <sub>d(off)</sub>  |   |      | 24     | 40       |      |  |
| Fall Time <sup>c</sup>  | t <sub>f</sub>       |   | 10   | 20     |          |      |  |
| Source-Drain Diode Ratings and Ch                             | aracteristics 7      | r <sub>C</sub> = 25 °C <sup>b</sup>   |      |        | <u> </u> |      |  |
| Continuous Current  | Is                   |   |      |        | 85       |      |  |
| Pulsed Current  | I <sub>SM</sub>      |   |      |        | 240      | Α    |  |
| Forward Voltage <sup>a</sup>                                  | V <sub>SD</sub>      | I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V  |      | 0.83   | 1.5      | V    |  |
| Reverse Recovery Time   | t <sub>rr</sub>      | I <sub>F</sub> = 75 A, dl/dt = 100 A/μs   |      | 60     | 100      | ns   |  |
| Peak Reverse Recovery Current                                 | I <sub>RM(REC)</sub> |   |      | 3.3    | 4.5      | Α    |  |
| Reverse Recovery Charge                                       | Q <sub>rr</sub>      |   |      | 100    | 150      | nC   |  |

#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

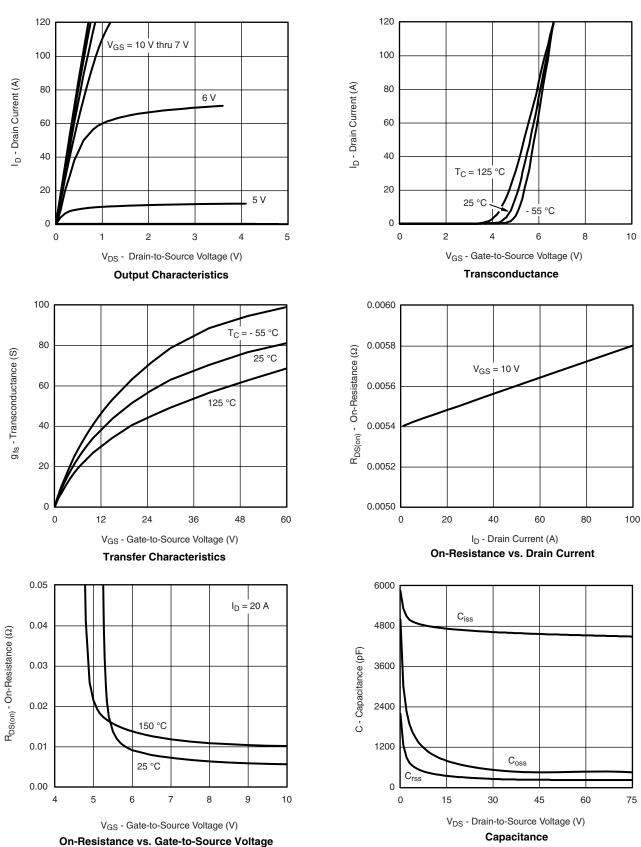
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

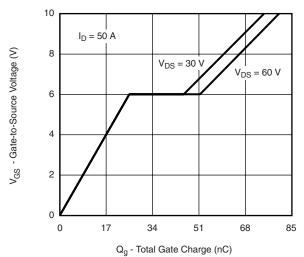


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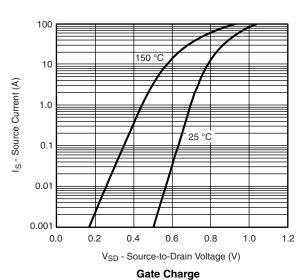
# Vishay Siliconix

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

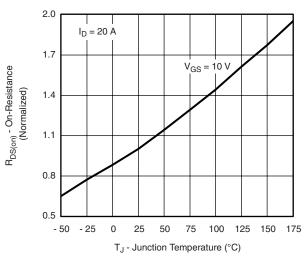


On-Resistance vs. Junction Temperature

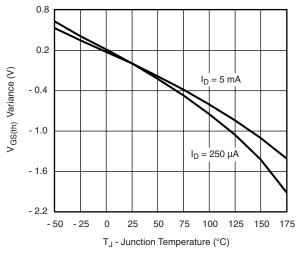


90 | I<sub>D</sub> = 1 mA | 90 | 86 | 82 | 78 | 74 | -50 -25 0 25 50 75 100 125 150 175

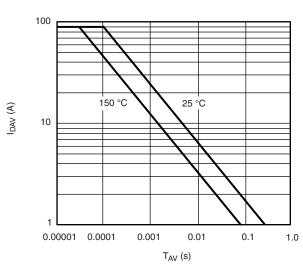
 $\label{eq:TJ-Junction} T_{J} \text{ - Junction Temperature (°C)}$  Source-Drain Diode Forward Voltage



Threshold Voltage



On-Resistance vs. Junction Temperature

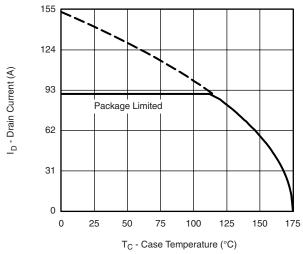


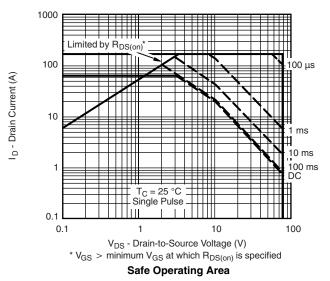
Single Pulse Avalanche Current Capability vs. Temperature



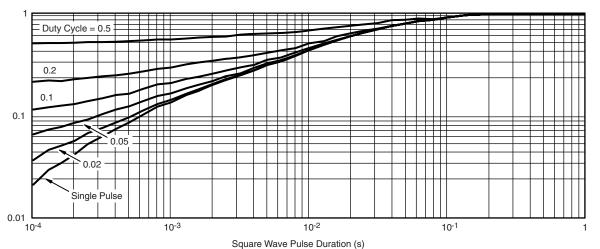
Normalized Effective Transient Thermal Impedance Vishay Siliconix

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Single Pulse Avalanche Current Capability vs. Time



Normalized Thermal Transient Impedance, Junction-to-Case

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