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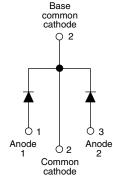
Vishay Semiconductors

HALOGEN FREE

Schottky Rectifier, 2 x 40 A



TO-247AC

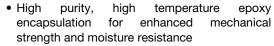


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PRODUCT SUMMARY					
Package	TO-247AC				
I _{F(AV)}	2 x 40 A				
V_{R}	20 V				
V _F at I _F	0.36 V				
I _{RM} max.	1100 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	27 mJ				

FEATURES

- 150 °C T_J operation
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability





- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	80	Α				
V _{RRM}		20	V				
I _{FSM}	$t_p = 5 \mu s sine$	2200	Α				
V _F	40 Apk, T _J = 150 °C (per leg)	0.32	V				
T _J	Range	- 55 to 150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-80CPQ020PbF	VS-80CPQ020-N3	UNITS			
Maximum DC reverse voltage	V _R	20	20	V			
Maximum working peak reverse voltage	V _{RWM}	20	20	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg	1	50 % duty cycle at T _C = 138 °C, rectangular waveform		40			
forward current	per device	I _{F(AV)}			80	A		
Maximum peak one cycle	Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	2200			
non-repetitive surge current per leg		I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	500			
Non-repetitive avalanche e	nergy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 6 A, L = 1.5 mH		27	mJ		
Repetitive avalanche current per leg I _{Al}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		6	Α		

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VS-80CPQ020PbF, VS-80CPQ020-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TES	VALUES	UNITS		
		40 A	T _{.1} = 25 °C	0.46		
		80 A	1j = 25 C	0.55		
Maximum forward	V _{FM} ⁽¹⁾	40 A	T 105 %C	0.36	V	
voltage drop per leg	V _{FM} (·)	80 A	T _J = 125 °C	0.46	V	
		40 A	T 150 °C	0.32		
		80 A	T _J = 150 °C	0.43		
	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = 5 V	110		
Maximum reverse		T _J = 150 °C	V _R = 10 V	600	A	
leakage current per leg		T _J = 25 °C	V Detect V	5.5	mA	
		T _J = 125 °C	V _R = Rated V _R	1100		
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.185	V	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		6500	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R 10 000		V/µs	

Note

 $^{(1)}~$ Pulse width $<300~\mu s,~duty~cycle < 2~\%$

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg	В	DC operation	0.6				
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	0.3	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.25				
Approximate weight			6	g			
Approximate weight			0.21	oz.			
Mounting torque minimum			6 (5)	kgf · cm			
Mounting torque maximum			12 (10)	(lbf \cdot in)			
Marking device		Case style TO-247AC (JEDEC)	80CP	Q020			

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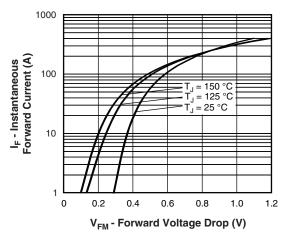


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

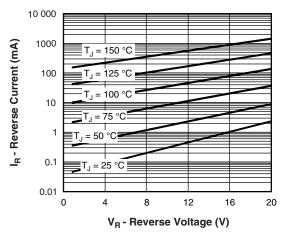


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

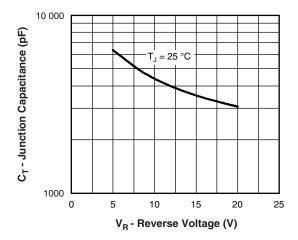


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

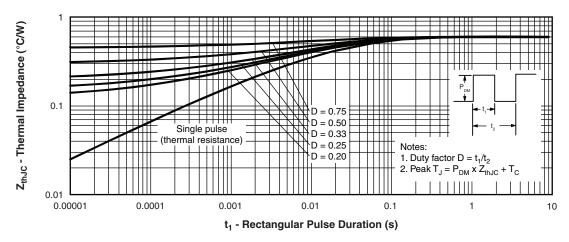


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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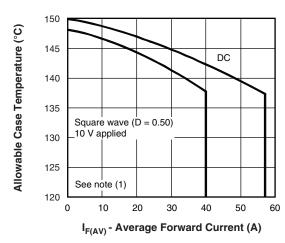


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

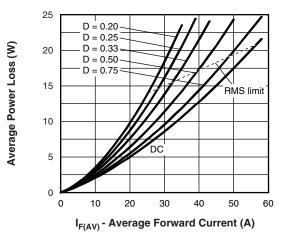


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

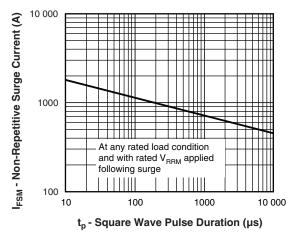


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

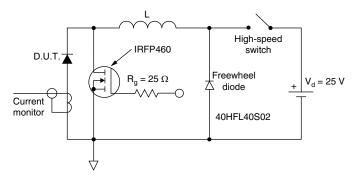


Fig. 8 - Unclamped Inductive Test Circuit

Note

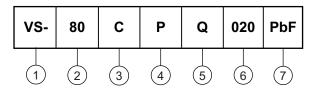
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 10 \text{ V}$

VS-80CPQ020PbF, VS-80CPQ020-N3

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (80 = 80 A)

- Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

6 - Voltage code (020 = 20 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-80CPQ020PbF	25	500	Antistatic plastic tube				
VS-80CPQ020-N3	25	500	Antistatic plastic tube				

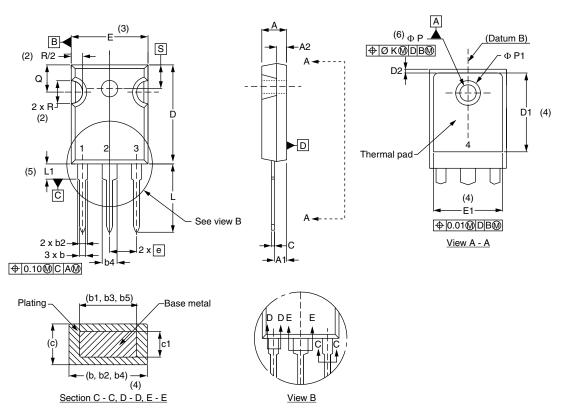
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
De transfer information	TO-247AC PbF	www.vishay.com/doc?95226			
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007			
SPICE model		www.vishay.com/doc?95289			



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TO-247AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
р	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
ØK	2.54		0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c

Legal Disclaimer Notice



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