

COMPLIANT

High Performance Schottky Rectifier, 300 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	300 A				
V_{R}	100 V				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

FEATURES

- 175 °C T_J operation
- · Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-303CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS VALUES					
I _{F(AV)}	Rectangular waveform	300	Α			
V_{RRM}		100	V			
I _{FSM}	$t_p = 5 \mu s sine$	22 000	Α			
V _F	150 A _{pk} , T _J = 125 °C (per leg)	0.72	V			
T _J	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-303CNQ100PbF	UNITS		
Maximum DC reverse voltage	V_{R}	100 V			
Maximum working peak reverse voltage	V_{RWM}				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward current	per leg	L	50 % data and at T = 400 % and and an and an and an and		FO 0/ duty evals at T 120 °C reates suley we referre		150	
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 138 °C, rectangular waveform					
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	22 000	A		
		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	2500				
Non-repetitive avalanche energy per leg E _A		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 13 \text{A}, L = 0.2 \text{mH}$		15	mJ		
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А			

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS VALUES			UNITS	
		150 A	T _{.1} = 25 °C	0.91	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	300 A	1j=25 C	1.09		
See fig. 1	V _{FM} (1)	150 A	T _{.1} = 125 °C	0.72	, v	
		300 A	1J = 125 C	0.85		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	4.5	mA	
See fig. 2		T _J = 125 °C	v _R = nateu v _R	80	IIIA	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C 4		4150	pF	
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane 6.0		nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _I			V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temper	erature range	T _J , T _{Stg}	-55	-	175	°C
Thermal resistance junction to case	per leg	D	-	-	0.28	
Thermal resistance, junction to case	per module	R_{thJC}	-	-	0.14	°C/W
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
Weight			-	68	-	g
			-	2.4	-	oz.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	(,
Vertical pull			-	-	80	- lbf ⋅ in
2" lever pull			-	-	35	ni i in

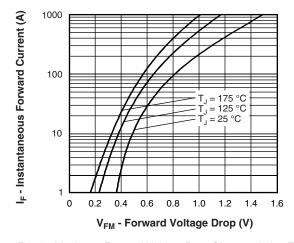


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

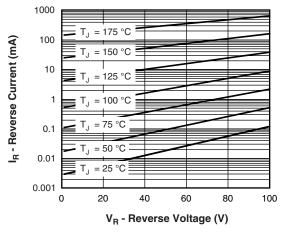


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

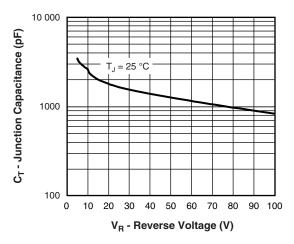


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

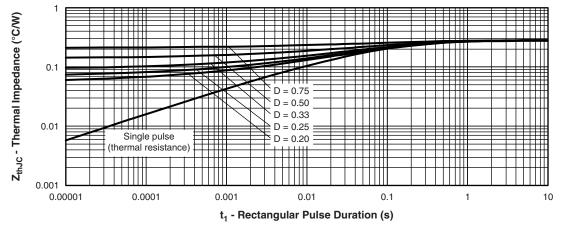


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

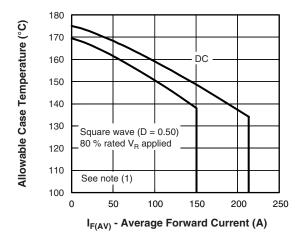


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

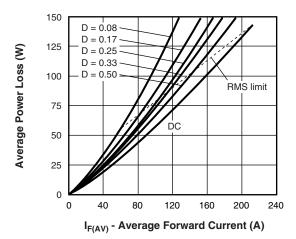


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

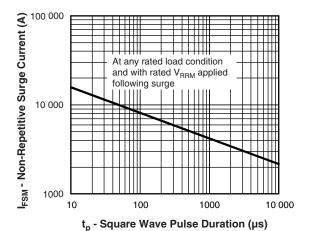


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

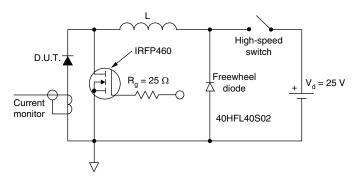


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code	VS-	30	3	С	N	Q	100	PbF
	1	2	3	4	5	6	7	8
	1 -	- Vishay Semiconductors product						
	2 -	 Average current rating (x 10) 						
	3 -	- Product silicon identification						
	4 -	- C = circuit configuration						
	5 -	N = not isolated						
	6 -	Q = Schottky rectifier diode						
	7 -	Volt	Voltage rating (100 = 100 V)					
	8 -	Lea	Lead (Pb)-free					

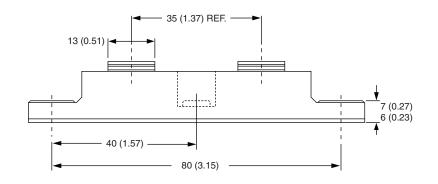
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

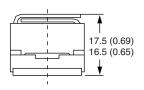
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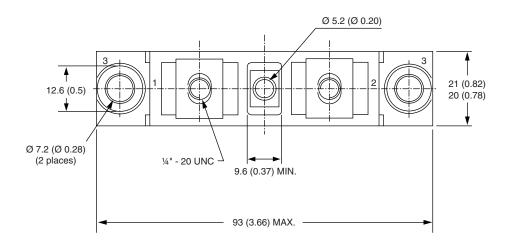


TO-244

DIMENSIONS in millimeters (inches)







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