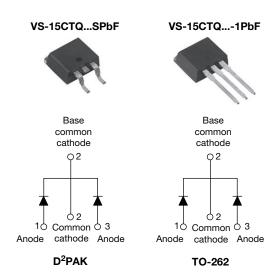
VS-15CTQ...SPbF, VS-15CTQ...-1PbF Series

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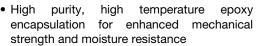
High Performance Schottky Rectifier, 2 x 7.5 A



PRODUCT SUMMARY	PRODUCT SUMMARY					
Package	D ² PAK, TO-262					
I _{F(AV)}	2 x 7.5 A					
V_{R}	35 V, 40 V, 45 V					
V _F at I _F	0.51 V					
I _{RM}	32 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	10 mJ					

FEATURES

- 150 °C T_J operation
- Center tap TO-220 package
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-15CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	15	Α		
V _{RRM}	Range	35 to 45	V		
I _{FSM}	t _p = 5 μs sine	810	Α		
V _F	7.5 A _{pk} , T _J = 125 °C (per leg)	0.51	V		
T _J	Range	-55 to +150	°C		

VOLTAGE RATINGS						
PARAMETER SYMBOL VS-15CTQ035SPbF VS-15CTQ040SPbF VS-15CTQ045SPbF VS-15CTQ045-1PbF VS-15CTQ045-1PbF						
Maximum DC reverse voltage	V _R	35	40	45	V	
Maximum working peak reverse voltage	V_{RWM}	33	40	43	V	



VS-15CTQ...SPbF, VS-15CTQ...-1PbF Series

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 123 °C	C, rectangular waveform	15	Α
Maximum peak one cycle non-repetitive surge current per leg	l	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	810	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	145	^
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11	.10 mH	10	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim		1.5	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1		7.5 A	T 05.00	0.55	V
	V _{FM} ⁽¹⁾	15 A	- T _J = 25 °C	0.70	
		7.5 A	T 105 00	0.51	
		15 A	- T _J = 125 °C	0.65	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	0.8	mA
See fig. 2		T _J = 125 °C	v _R = Rated v _R	32	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		400	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	3.50		
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	1.75	°C/W	
Typical thermal resistance, case to heatsink			Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
	minimum			6 (5)	kgf · cm	
Mounting torque maxir m				12 (10)	(lbf·in)	
Mayling davide			Case style D ² PAK	15CT0	Q045S	
Marking device			Case style TO-262	15CTC	045-1	

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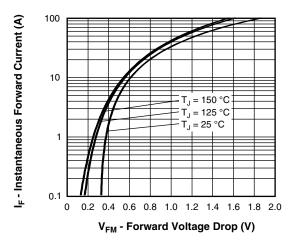


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

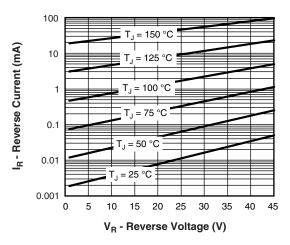


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

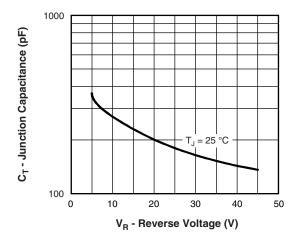


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

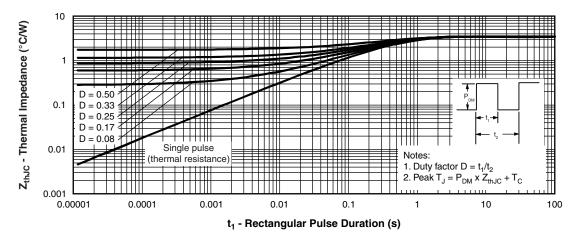


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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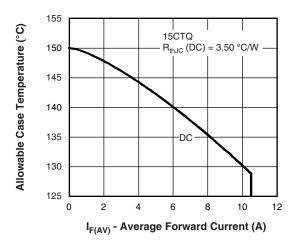


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

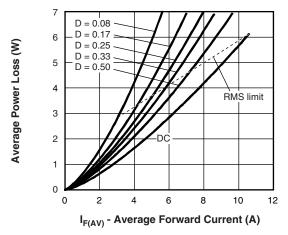


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

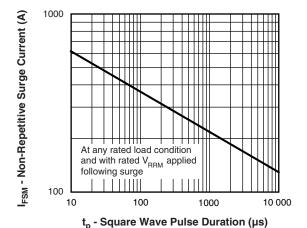


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

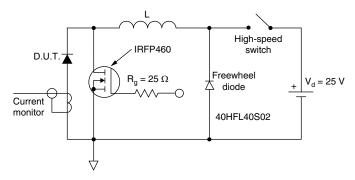


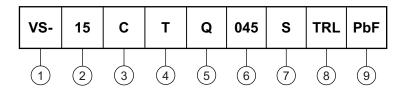
Fig. 8 - Unclamped Inductive Test Circuit

VS-15CTQ...SPbF, VS-15CTQ...-1PbF Series

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (15 A)

- Circuit configuration: C = common cathode

4 - T = TO-220

- Schottky "Q" series

035 = 35 V

Voltage ratings

040 = 40 V

- • S = D²PAK

045 = 45 V

. 3-D FAN

• -1 = TO-262

8 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented - for D²PAK only)

• TRR = tape and reel (right oriented - for D²PAK only)

9 - PbF = lead (Pb)-free

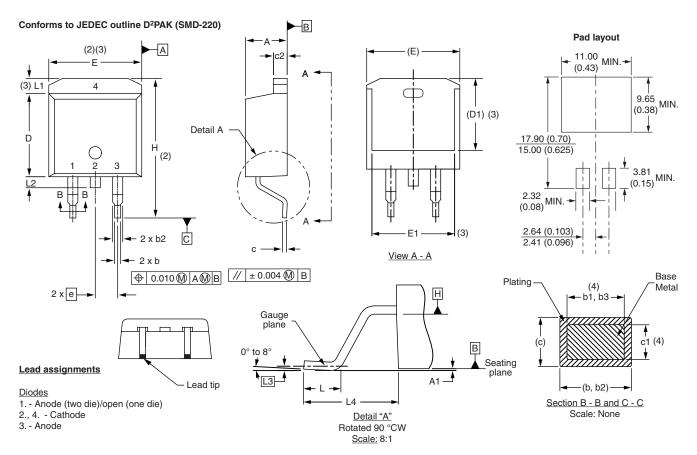
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95014</u>				
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			



Vishay Semiconductors

D²PAK, TO-262

DIMENSIONS - D²PAK in millimeters and inches



	1		1		t .	
SYMBOL	MILLIN	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

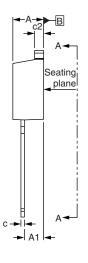
Document Number: 95014 Revision: 31-Mar-09

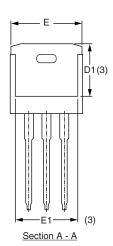
Vishay Semiconductors

D²PAK, TO-262



DIMENSIONS - TO-262 in millimeters and inches





⊕ 0.010 **M** A **M** B

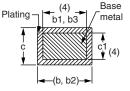
Lead assignments



Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIM	MILLIMETERS		INCHES		
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	D BSC		
L	13.46	14.10	0.530	0.555		
L1	=	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

Legal Disclaimer Notice



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